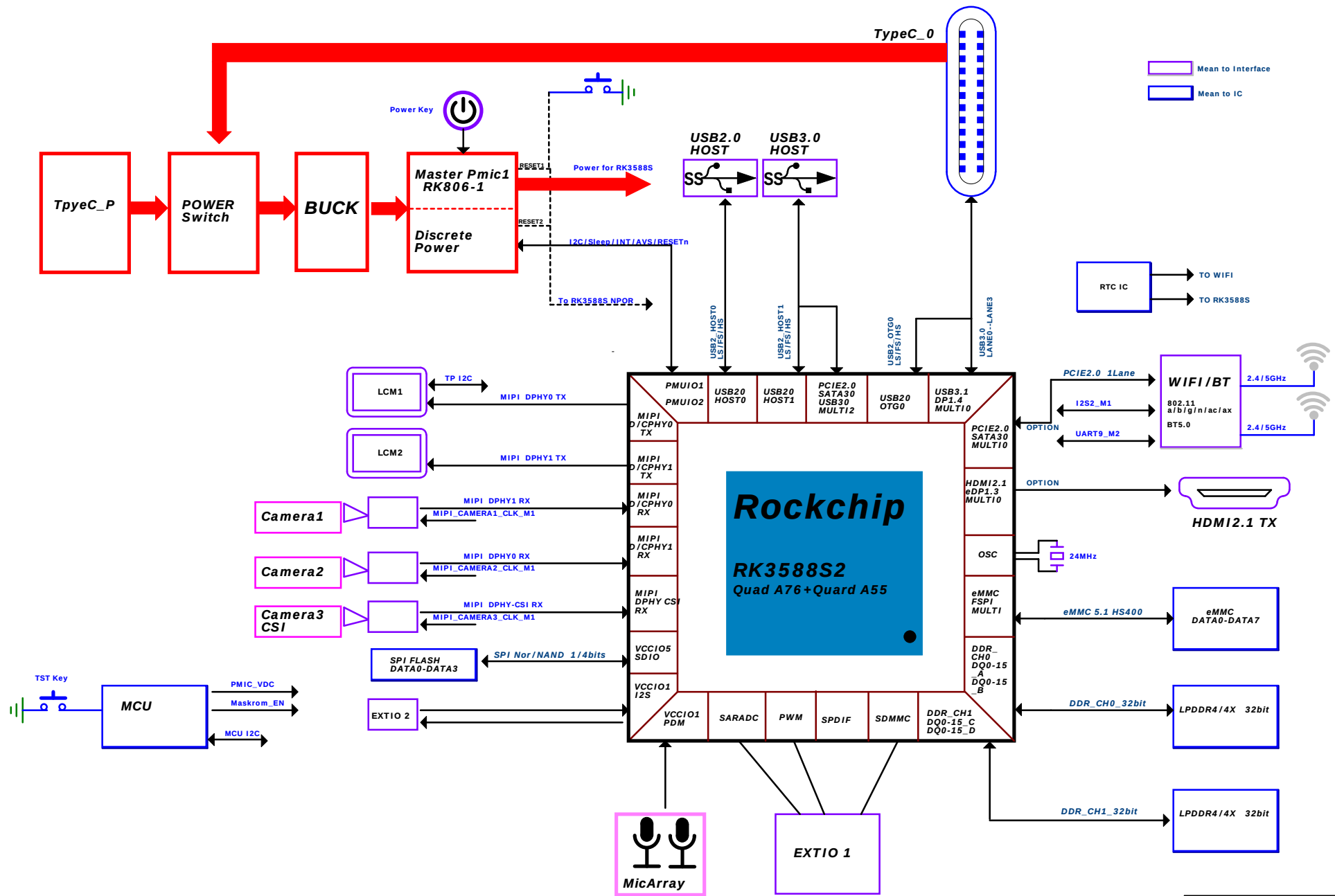




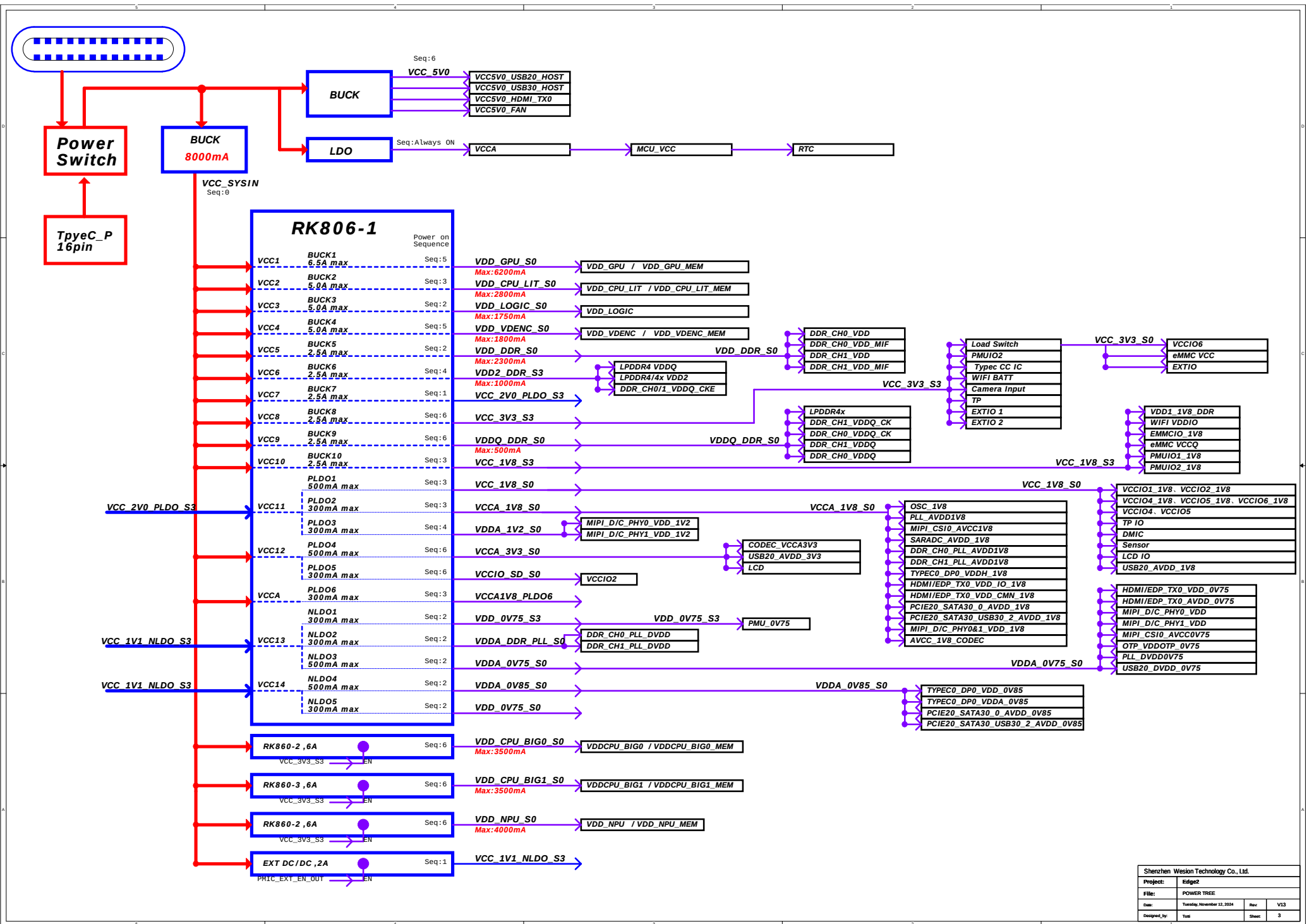
KHADAS

Something a little different.

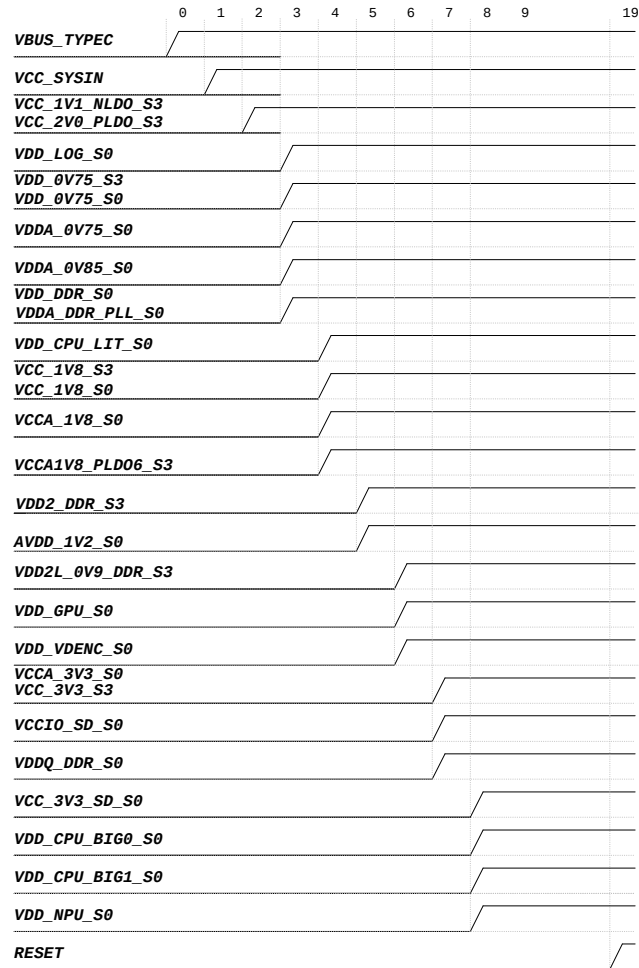
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	Khadas		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	1



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	BLOCK		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Tian	Sheet:	2



Power Sequence



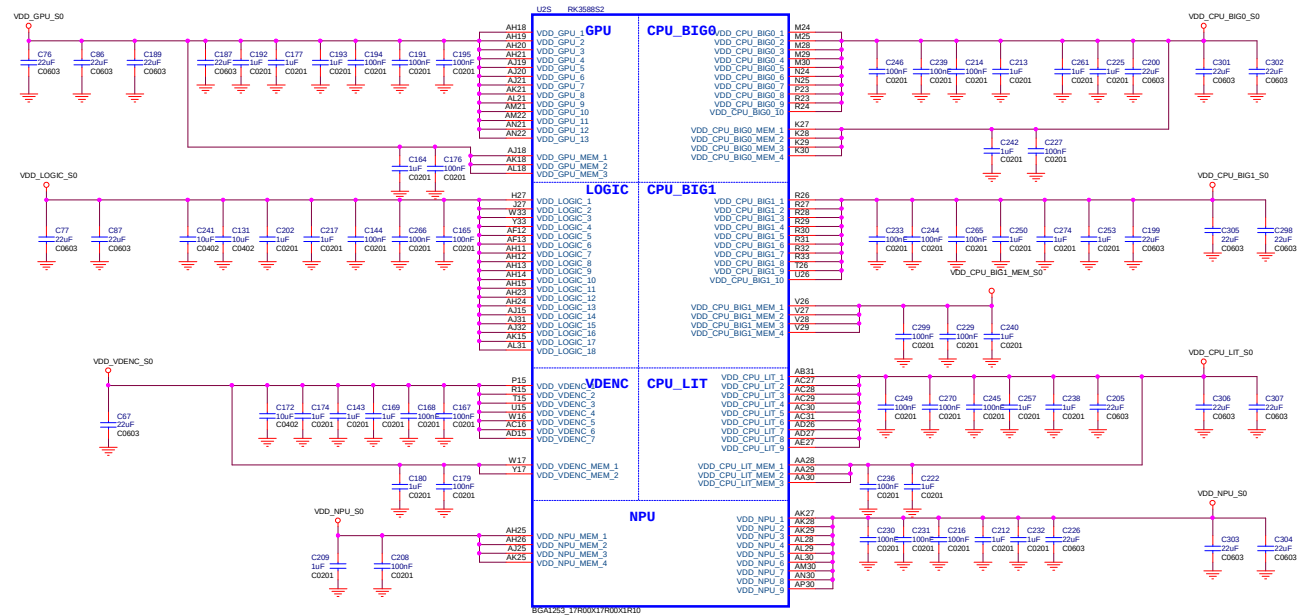
Power Supply	PMIC Channel	Supply Limit	Power Name	Time Slot	Default Voltage	Default ON/OFF	Sleep ON/OFF	Peak Current	Sleep Current
VCC_SYSIN	RK806-1_BUCK1	6.5A	VDD_GPU_S0	Slot:5	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK2	5A	VDD_CPU_LIT_S0	Slot:3	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK3	5A	VDD_LOG_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK4	3A	VDD_VDENC_S0	Slot:5	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK5	2.5A	VDD_DDR_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK6	2.5A	VDD2_DDR_S3	Slot:4	Adj FB=0.5V 2.0V	ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK7	2.5A	VCC_2V0_PLDO_S3	Slot:1		ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK8	2.5A	VCC_3V3_S3	Slot:6	3.3V	ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK9	2.5A	VDDQ_DDR_S0	Slot:6	Adj FB=0.5V 1.8V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK10	2.5A	VCC_1V8_S3	Slot:3		ON	ON	TBD	TBD
VCC_2V0_PLDO_S3	RK806-1_PLDO1	0.5A	VCC_1V8_S0	Slot:3	1.8V	ON	OFF	TBD	TBD
	RK806-1_PLDO2	0.3A	VCCA_1V8_S0	Slot:3	1.8V	ON	OFF	TBD	TBD
	RK806-1_PLDO3	0.3A	VDDA_1V2_S0	Slot:4	1.2V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_PLDO4	0.5A	VCCA_3V3_S0	Slot:6	3.3V	ON	OFF	TBD	TBD
	RK806-1_PLDO5	0.3A	VCCIO_SD_S0	Slot:6	3.3V	ON	OFF	TBD	TBD
	RK806-1_PLDO6	0.3A	VCCA1V8_PLDO6_S3	Slot:3	1.8V	ON	ON	TBD	TBD
VCC_1V1_NLDO_S3	RK806-1_NLDO1	0.3A	VDD_0V75_S3	Slot:2	0.75V	ON	ON	TBD	TBD
	RK806-1_NLDO2	0.3A	VDDA_DDR_PLL_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
	RK806-1_NLDO3	0.5A	VDDA_0V75_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_1V1_NLDO_S3	RK806-1_NLDO4	0.5A	VDDA_0V85_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
	RK806-1_NLDO5	0.3A	VDD_0V75_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-2	6A	VDD_CPU_BIG0_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-3	6A	VDD_CPU_BIG1_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-2	6A	VDD_NPU_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	EXT BUCK	2A	VCC_1V1_NLDO_S3	Slot:1	1.1V	ON	ON	TBD	TBD

IO Power Domain Map

IO Domain	Pin Num	Support IO Voltage	Supply Power Pin Name	Power Source	Operating Voltage
PMUIO1	Pin N36 N37	1.8V Only	PMUIO1_1V8	VCC_1V8_S3	1.8V
PMUIO2	Pin V37 Y37	1.8V or 3.3V	PMUIO2_1V8	VCC_1V8_S3	1.8V
	Pin V35 V36		PMUIO2	VCC_1V8_S3	1.8V
EMMCIO	Pin AC35	1.8V Only	EMMCIO_1V8	VCC_1V8_S0	1.8V
VCCIO1	Pin AC36	1.8V Only	VCCIO1_1V8	VCC_1V8_S0	1.8V
	Pin H31				
VCCIO2	Pin AK11	1.8V or 3.3V	VCCIO2_1V8	VCC_1V8_S0	1.8V
VCCIO2	Pin AK10	1.8V or 3.3V	VCCIO2	VCC_IO_SD	1.8V/3.3V
	Pin G27 G28		VCCIO4_1V8	VCC_1V8_S0	1.8V
VCCIO4	Pin G31	1.8V or 3.3V	VCCIO4	VCC_1V8_S0	1.8V
VCCIO5	Pin AF35 AF36	1.8V or 3.3V	VCCIO5_1V8	VCC_1V8_S0	1.8V
	Pin AC33 AC34		VCCIO5	VCC_1V8_S0	1.8V
VCCIO6	Pin AJ34	1.8V or 3.3V	VCCIO6_1V8	VCC_1V8_S0	1.8V
	Pin AL33 AM33		VCCIO6	VCC_3V3_S0	3.3V

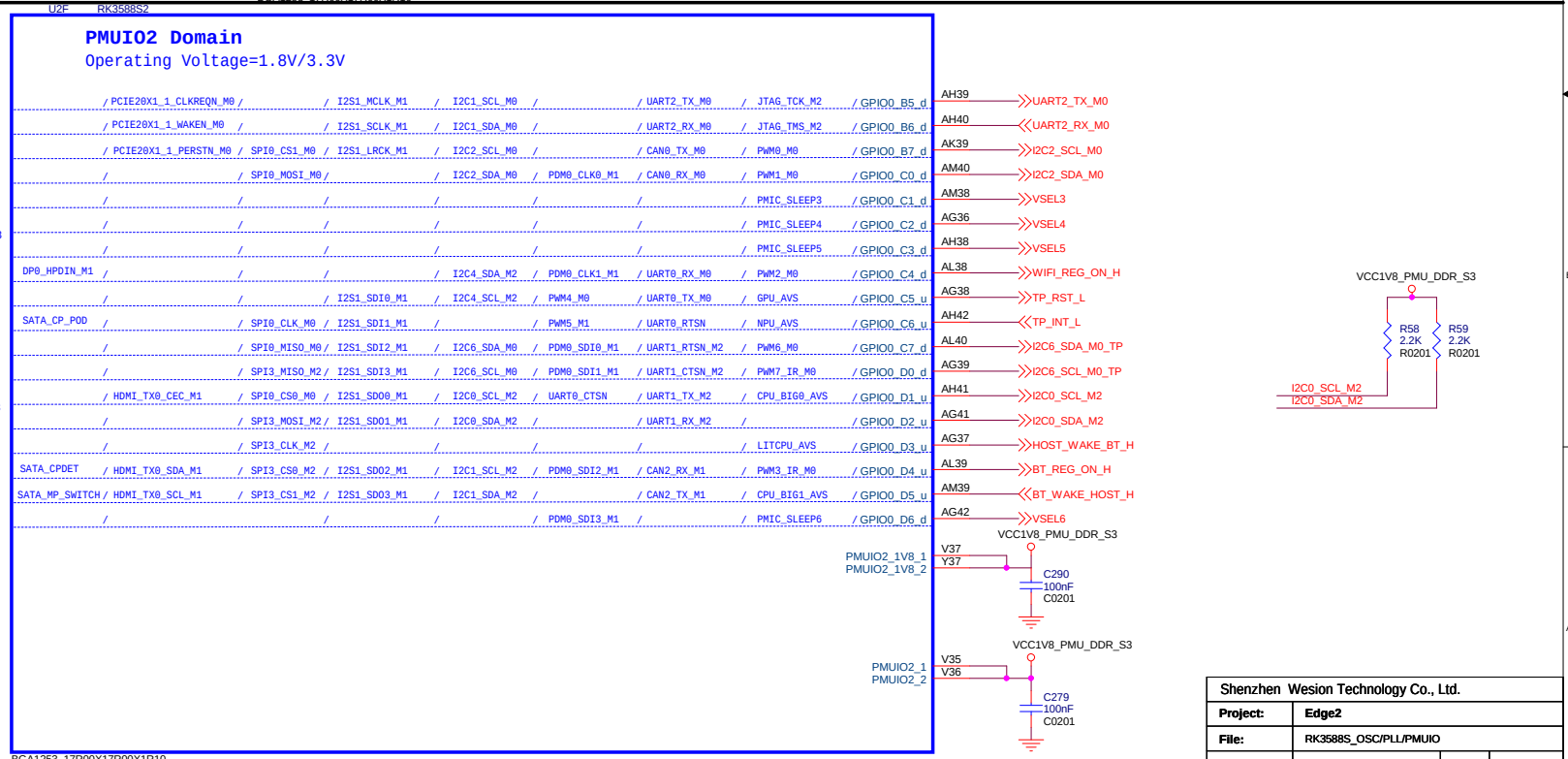
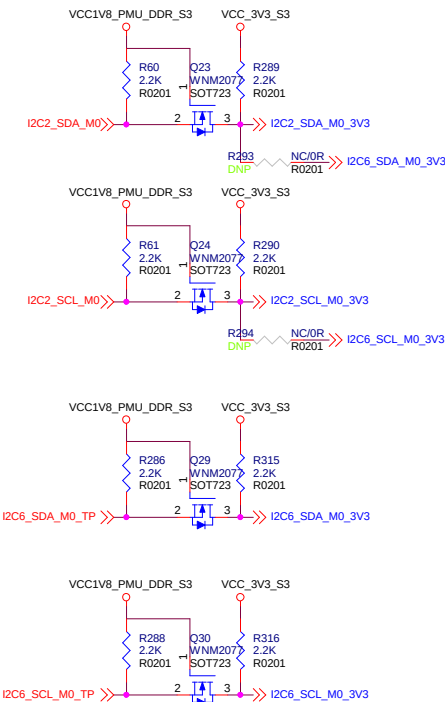
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	System Power Sequence		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Toll	Sheet:	4

RK3588S2 (Power&Gnd)



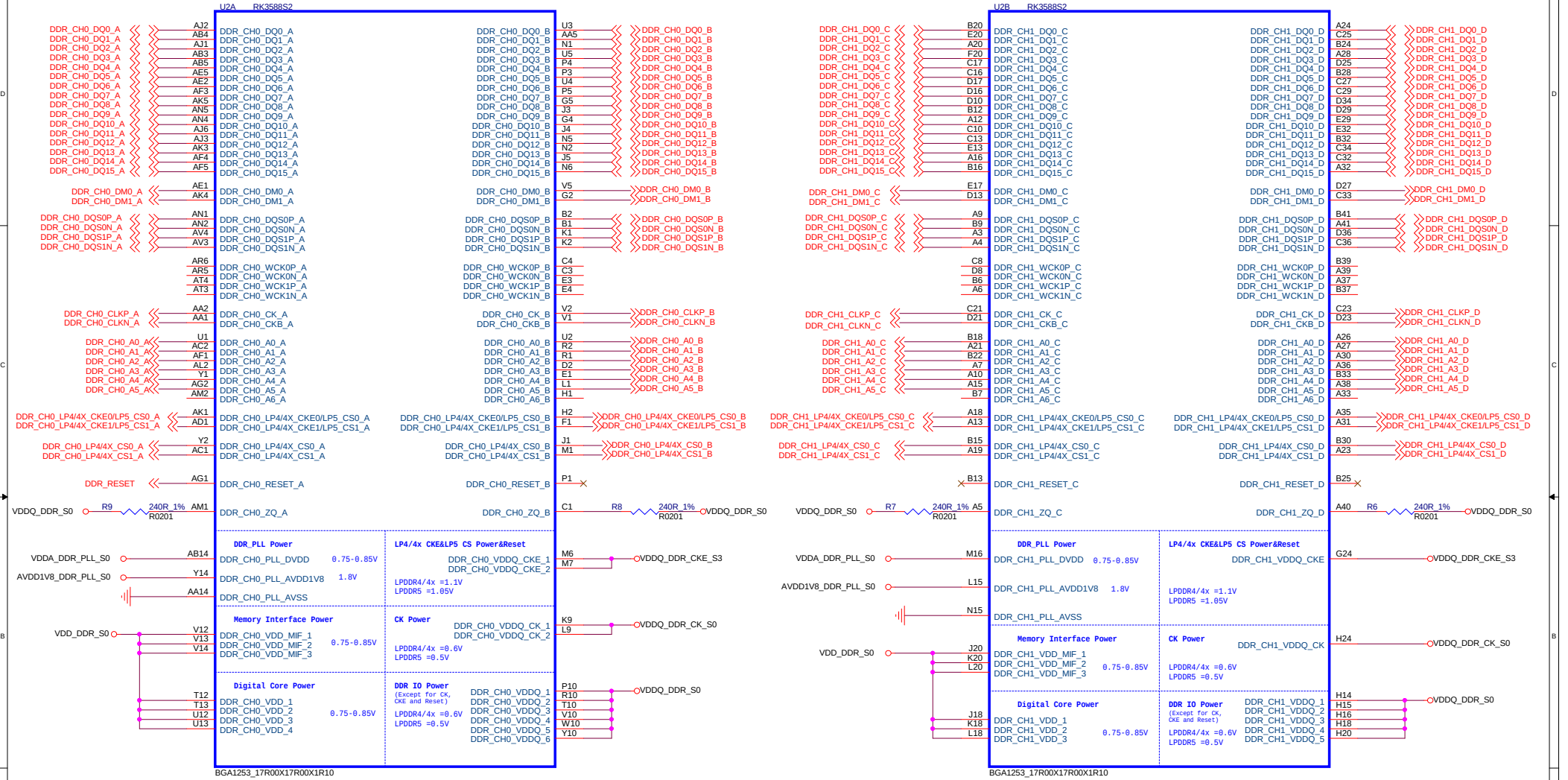
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A1 VSS_1	VSS_101	VSS_201	VSS_301	VSS_401	VSS_501	VSS_601	VSS_701
A2 VSS_2	VSS_102	VSS_202	VSS_302	VSS_402	VSS_502	VSS_602	VSS_702
A3 VSS_3	VSS_103	VSS_203	VSS_303	VSS_403	VSS_503	VSS_603	VSS_703
A4 VSS_4	VSS_104	VSS_204	VSS_304	VSS_404	VSS_504	VSS_604	VSS_704
A5 VSS_5	VSS_105	VSS_205	VSS_305	VSS_405	VSS_505	VSS_605	VSS_705
A6 VSS_6	VSS_106	VSS_206	VSS_306	VSS_406	VSS_506	VSS_606	VSS_706
A7 VSS_7	VSS_107	VSS_207	VSS_307	VSS_407	VSS_507	VSS_607	VSS_707
A8 VSS_8	VSS_108	VSS_208	VSS_308	VSS_408	VSS_508	VSS_608	VSS_708
A9 VSS_9	VSS_109	VSS_209	VSS_309	VSS_409	VSS_509	VSS_609	VSS_709
A10 VSS_10	VSS_110	VSS_210	VSS_310	VSS_410	VSS_510	VSS_610	VSS_710
A11 VSS_11	VSS_111	VSS_211	VSS_311	VSS_411	VSS_511	VSS_611	VSS_711
A12 VSS_12	VSS_112	VSS_212	VSS_312	VSS_412	VSS_512	VSS_612	VSS_712
A13 VSS_13	VSS_113	VSS_213	VSS_313	VSS_413	VSS_513	VSS_613	VSS_713
A14 VSS_14	VSS_114	VSS_214	VSS_314	VSS_414	VSS_514	VSS_614	VSS_714
A15 VSS_15	VSS_115	VSS_215	VSS_315	VSS_415	VSS_515	VSS_615	VSS_715
A16 VSS_16	VSS_116	VSS_216	VSS_316	VSS_416	VSS_516	VSS_616	VSS_716
A17 VSS_17	VSS_117	VSS_217	VSS_317	VSS_417	VSS_517	VSS_617	VSS_717
A18 VSS_18	VSS_118	VSS_218	VSS_318	VSS_418	VSS_518	VSS_618	VSS_718
A19 VSS_19	VSS_119	VSS_219	VSS_319	VSS_419	VSS_519	VSS_619	VSS_719
A20 VSS_20	VSS_120	VSS_220	VSS_320	VSS_420	VSS_520	VSS_620	VSS_720
A21 VSS_21	VSS_121	VSS_221	VSS_321	VSS_421	VSS_521	VSS_621	VSS_721
A22 VSS_22	VSS_122	VSS_222	VSS_322	VSS_422	VSS_522	VSS_622	VSS_722
A23 VSS_23	VSS_123	VSS_223	VSS_323	VSS_423	VSS_523	VSS_623	VSS_723
A24 VSS_24	VSS_124	VSS_224	VSS_324	VSS_424	VSS_524	VSS_624	VSS_724
A25 VSS_25	VSS_125	VSS_225	VSS_325	VSS_425	VSS_525	VSS_625	VSS_725
A26 VSS_26	VSS_126	VSS_226	VSS_326	VSS_426	VSS_526	VSS_626	VSS_726
A27 VSS_27	VSS_127	VSS_227	VSS_327	VSS_427	VSS_527	VSS_627	VSS_727
A28 VSS_28	VSS_128	VSS_228	VSS_328	VSS_428	VSS_528	VSS_628	VSS_728
A29 VSS_29	VSS_129	VSS_229	VSS_329	VSS_429	VSS_529	VSS_629	VSS_729
A30 VSS_30	VSS_130	VSS_230	VSS_330	VSS_430	VSS_530	VSS_630	VSS_730
A31 VSS_31	VSS_131	VSS_231	VSS_331	VSS_431	VSS_531	VSS_631	VSS_731
A32 VSS_32	VSS_132	VSS_232	VSS_332	VSS_432	VSS_532	VSS_632	VSS_732
A33 VSS_33	VSS_133	VSS_233	VSS_333	VSS_433	VSS_533	VSS_633	VSS_733
A34 VSS_34	VSS_134	VSS_234	VSS_334	VSS_434	VSS_534	VSS_634	VSS_734
A35 VSS_35	VSS_135	VSS_235	VSS_335	VSS_435	VSS_535	VSS_635	VSS_735
A36 VSS_36	VSS_136	VSS_236	VSS_336	VSS_436	VSS_536	VSS_636	VSS_736
A37 VSS_37	VSS_137	VSS_237	VSS_337	VSS_437	VSS_537	VSS_637	VSS_737
A38 VSS_38	VSS_138	VSS_238	VSS_338	VSS_438	VSS_538	VSS_638	VSS_738
A39 VSS_39	VSS_139	VSS_239	VSS_339	VSS_439	VSS_539	VSS_639	VSS_739
A40 VSS_40	VSS_140	VSS_240	VSS_340	VSS_440	VSS_540	VSS_640	VSS_740
A41 VSS_41	VSS_141	VSS_241	VSS_341	VSS_441	VSS_541	VSS_641	VSS_741
A42 VSS_42	VSS_142	VSS_242	VSS_342	VSS_442	VSS_542	VSS_642	VSS_742
A43 VSS_43	VSS_143	VSS_243	VSS_343	VSS_443	VSS_543	VSS_643	VSS_743
A44 VSS_44	VSS_144	VSS_244	VSS_344	VSS_444	VSS_544	VSS_644	VSS_744
A45 VSS_45	VSS_145	VSS_245	VSS_345	VSS_445	VSS_545	VSS_645	VSS_745
A46 VSS_46	VSS_146	VSS_246	VSS_346	VSS_446	VSS_546	VSS_646	VSS_746
A47 VSS_47	VSS_147	VSS_247	VSS_347	VSS_447	VSS_547	VSS_647	VSS_747
A48 VSS_48	VSS_148	VSS_248	VSS_348	VSS_448	VSS_548	VSS_648	VSS_748
A49 VSS_49	VSS_149	VSS_249	VSS_349	VSS_449	VSS_549	VSS_649	VSS_749
A50 VSS_50	VSS_150	VSS_250	VSS_350	VSS_450	VSS_550	VSS_650	VSS_750

RK3588S2 (PMUI02)

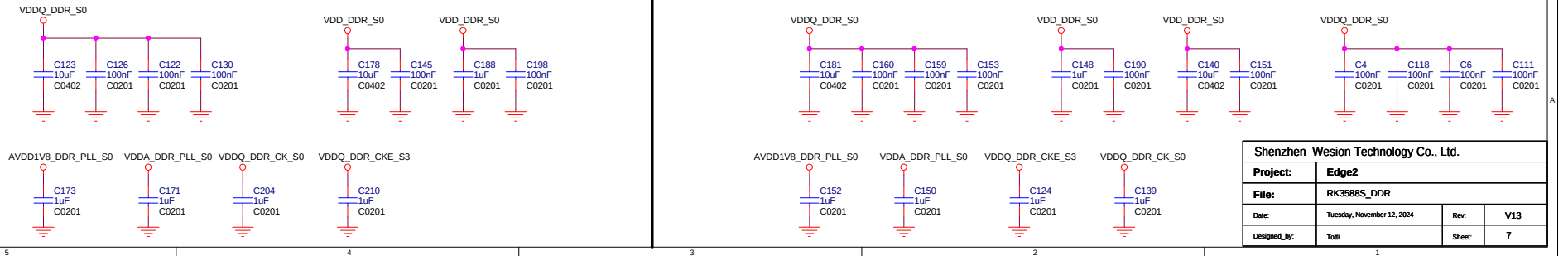


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	RK3588S_OSC/PLL/PMUIO		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed by:	Totli	Sheet:	6

RK3588S2(DDR PHY)



DDR FILTER



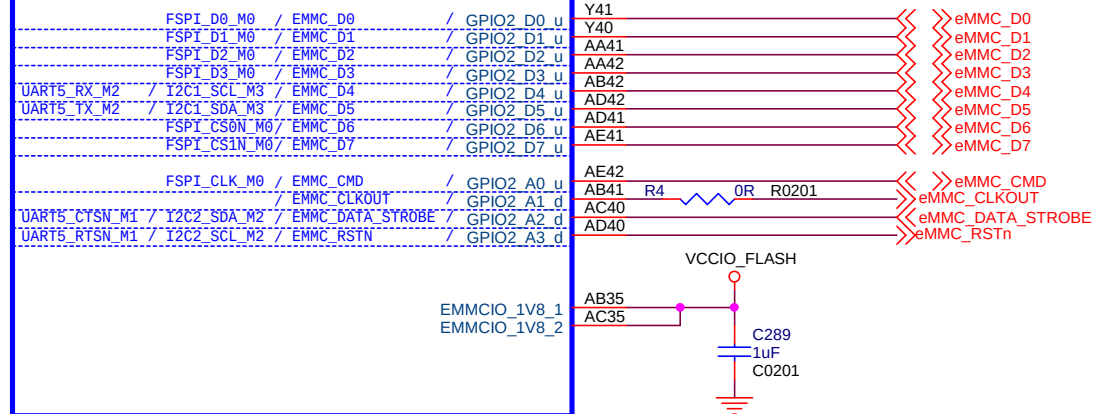
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	RK3588S_DDR		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Toni	Sheet:	7

RK3588S2(EMMCIO Domain)

U2C RK3588S2

EMMCIO Domain

Operating Voltage=1.8V Only



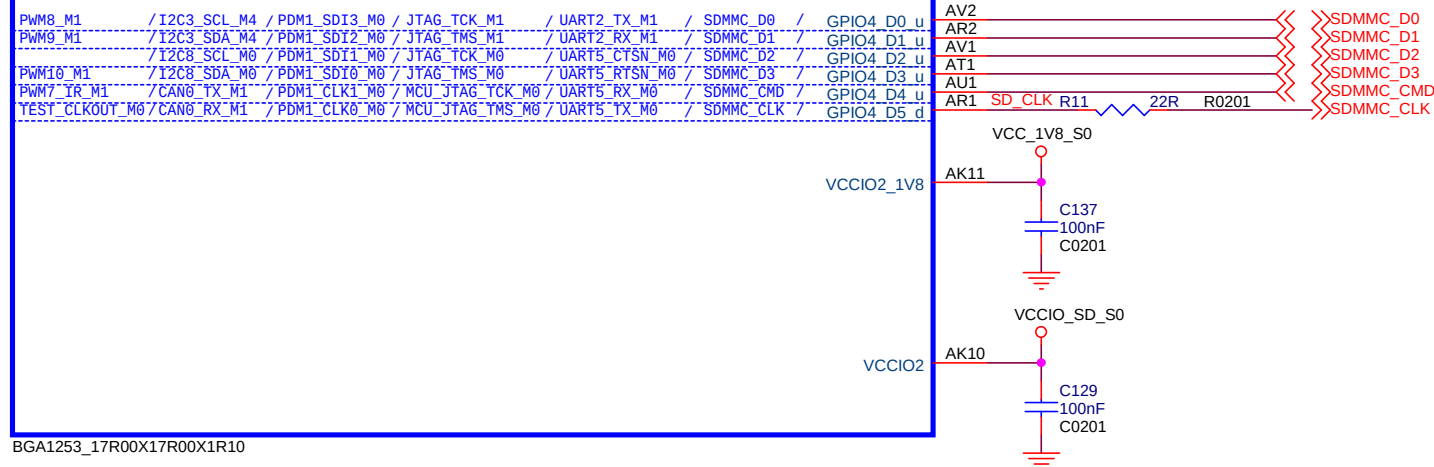
BGA1253_17R00X17R00X1R10

RK3588S2(VCCIO2 Domain)

U2D RK3588S2

VCCIO2 Domain

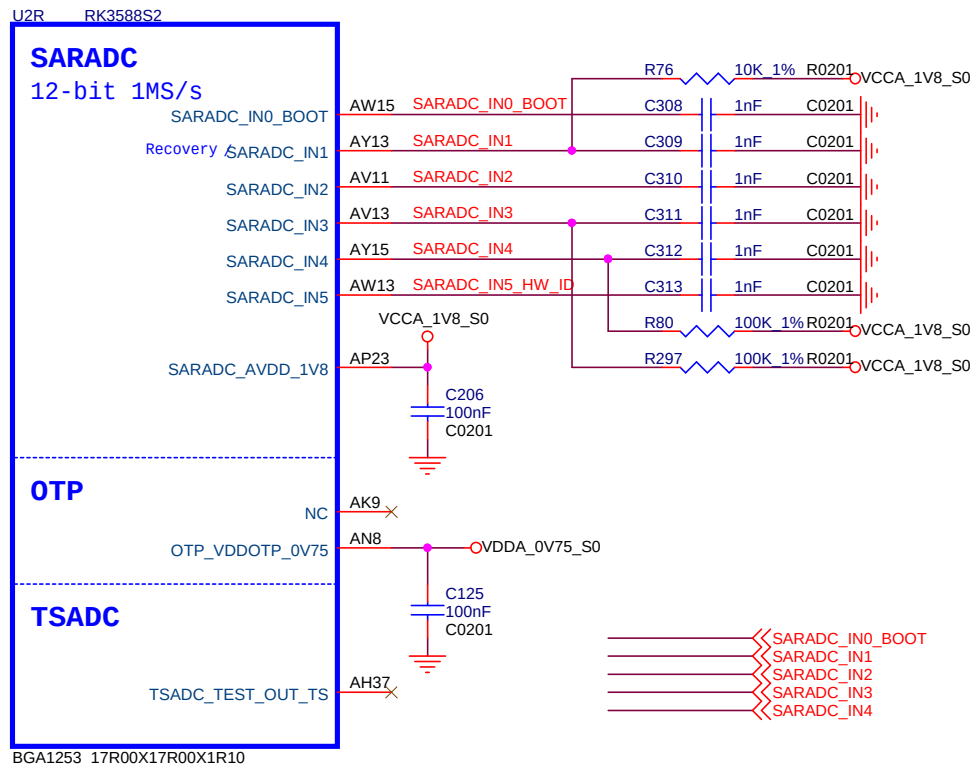
Operating Voltage=1.8V/3.3V



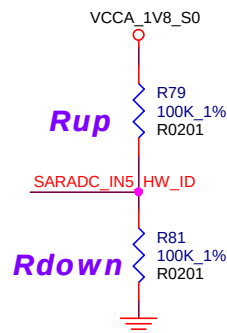
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Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	RK3588S_Flash/SD		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	8

RK3588S2 (SARADC/OTP/TSADC)

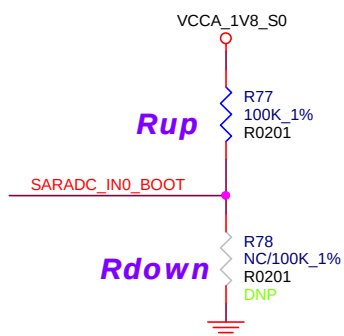


BOARD ID CONFIG



Item	Rup	Rdown	ADC	VERSION
LEVEL1	DNP	100K	0	V10
LEVEL2	100K	20K	682	V11
LEVEL3	100K	51K	1365	V12
LEVEL4	100K	100K	2047	V13
LEVEL5	100K	200K	2730	V14
LEVEL6	100K	499K	3412	V15
LEVEL7	100K	DNP	4095	V16

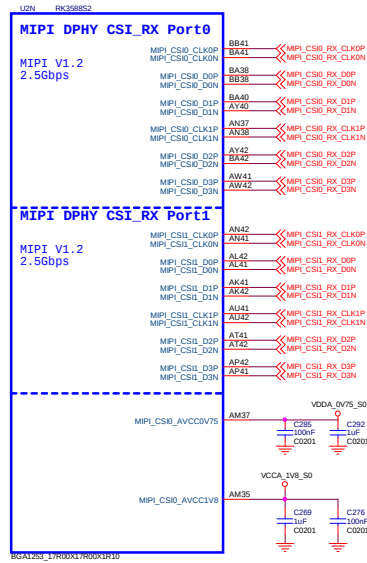
BOOT MODE CONFIG



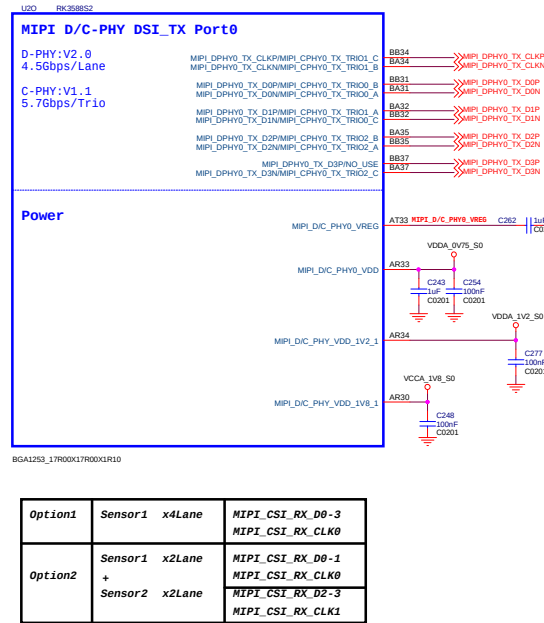
Item	Rup	Rdown	ADC	BOOT MODE
LEVEL1	DNP	100K	0	USB (Maskrom mode)
LEVEL2	100K	20K	682	SD Card-USB
LEVEL3	100K	51K	1365	EMMC-USB
LEVEL4	100K	100K	2047	FSPI M0-USB
LEVEL5	100K	200K	2730	FSPI M1-USB
LEVEL6	100K	499K	3412	FSPI M2-USB
LEVEL7	100K	DNP	4095	FSPI M2-FSPI M0-EMMC -SD Card-USB

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	RK3588S_SARADC		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	9

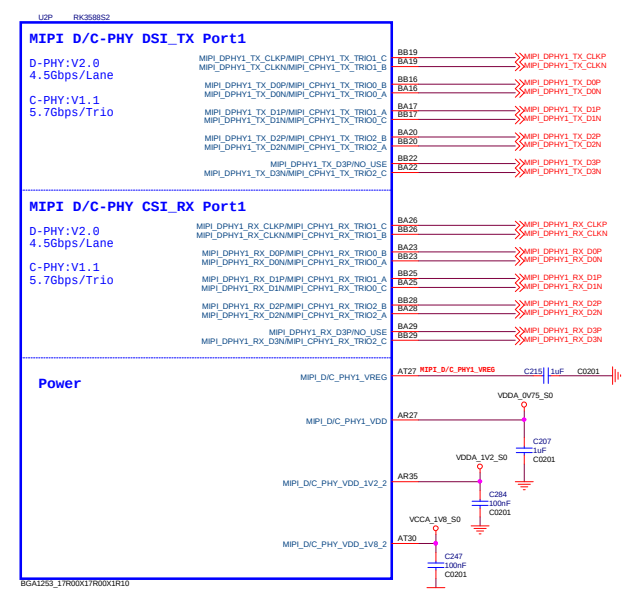
RK3588S2(MIPI_DPHY CSI0 RX)



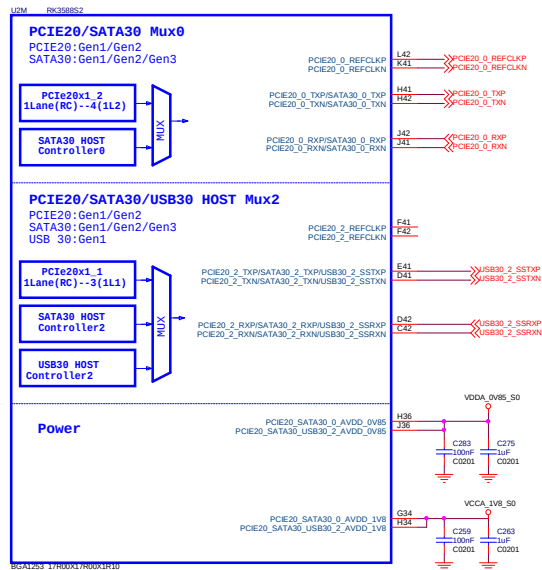
RK3588S2(MIPI_D/C PHY0)



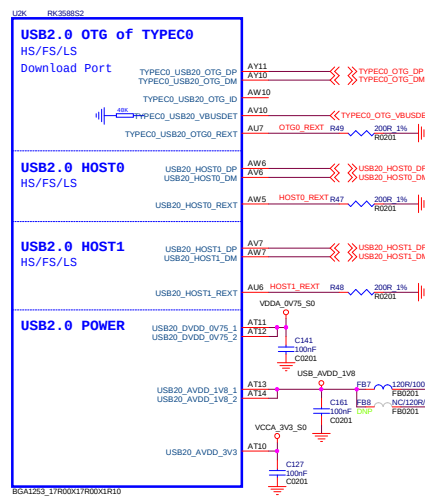
RK3588S2(MIPI_D/C PHY1)



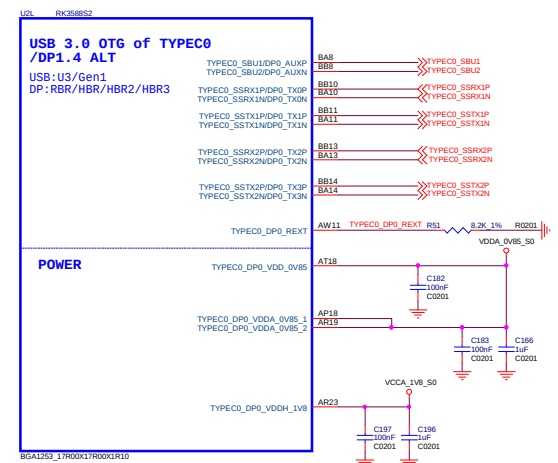
RK3588S2(PCIe20/SATA30/USB30)



RK3588S2(USB2.0)



RK3588S2(USB3.0/DP1.4)



PCB layout showing the VCCIO4 pin header and its connection to the VCCIO4_1 and VCCIO4_2 pins. The VCCIO4_1 pin is connected to the VCCIO4_1 pin on the board, and the VCCIO4_2 pin is connected to the VCCIO4_2 pin on the board. The VCCIO4_1 pin is also connected to the VCCIO4_1 pin on the board. The VCCIO4_2 pin is also connected to the VCCIO4_2 pin on the board. The VCCIO4_1 pin is also connected to the VCCIO4_1 pin on the board. The VCCIO4_2 pin is also connected to the VCCIO4_2 pin on the board.

VCCIO6 Domain
 Operating Voltage=1.8V/3.3V

Pin	Signal	Function
BT1120_00	/ CIP_D0	/
BT1120_01	/ CIP_D1	/
BT1120_02	/ CIP_D2	/
BT1120_03	/ CIP_D3	/
BT1120_04	/ CIP_D4	/
BT1120_05	/ CIP_D5	/
BT1120_06	/ CIP_D6	/
BT1120_07	/ CIP_D7	/
BT1120_CLKOUT	/ CIP_CLKIN	/
SPDIF3_TX_M1	/ MIP1_CAMERA0_CLK_M0	/ SATA2_ACT_LED_M0
CAN1_RX_M1	/ BT1120_08	/ CIP_HREF
CAN1_TX_M1	/ BT1120_09	/ CIP_VSYNC
BT1120_010	/ CIP_CLKOUT	/
BT1120_011	/	/
BT1120_012	/	/
BT1120_013	/	/
BT1120_014	/	/
BT1120_015	/ SPDIF3_TX_M2	/

Note:
 BT1120 Only Support Output

[illegible]

HDMI TX/eDP1.3 MUX Port0

HDMI-V2.1 12Gbps
eDP-V1.3 5.4Gbps

Signal connections:

- HDMI_TX0_D0P/EDP_TX0_D0P to BB4
- HDMI_TX0_D0N/EDP_TX0_D0N to BA5
- HDMI_TX0_D1P/EDP_TX0_D1P to BA5
- HDMI_TX0_D1N/EDP_TX0_D1N to BB5
- HDMI_TX0_D2P/EDP_TX0_D2P to BB7
- HDMI_TX0_D2N/EDP_TX0_D2N to BA7
- HDMI_TX0_D3P/EDP_TX0_D3P to BA2
- HDMI_TX0_D3N/EDP_TX0_D3N to BB2
- HDMI_TX0_SBDP/EDP_TX0_ALUP to BA1
- HDMI_TX0_SBDN/EDP_TX0_ALUN to AY1

Power connections:

- HDMIeDP_TX0_REXT to AY3 (HDMIeDP_TX0_REXT) via R44 (2.0K 2%) to VDDA_0V75_S0
- HDMIeDP_TX0_VDD_0V75_1 to AM13
- HDMIeDP_TX0_VDD_0V75_2 to AN12
- HDMIeDP_TX0_AVDD_0V75 to AN10
- HDMIeDP_TX0_VDD_IO_1V8 to AK12
- HDMIeDP_TX0_VDD_CMN_1V8 to AL14

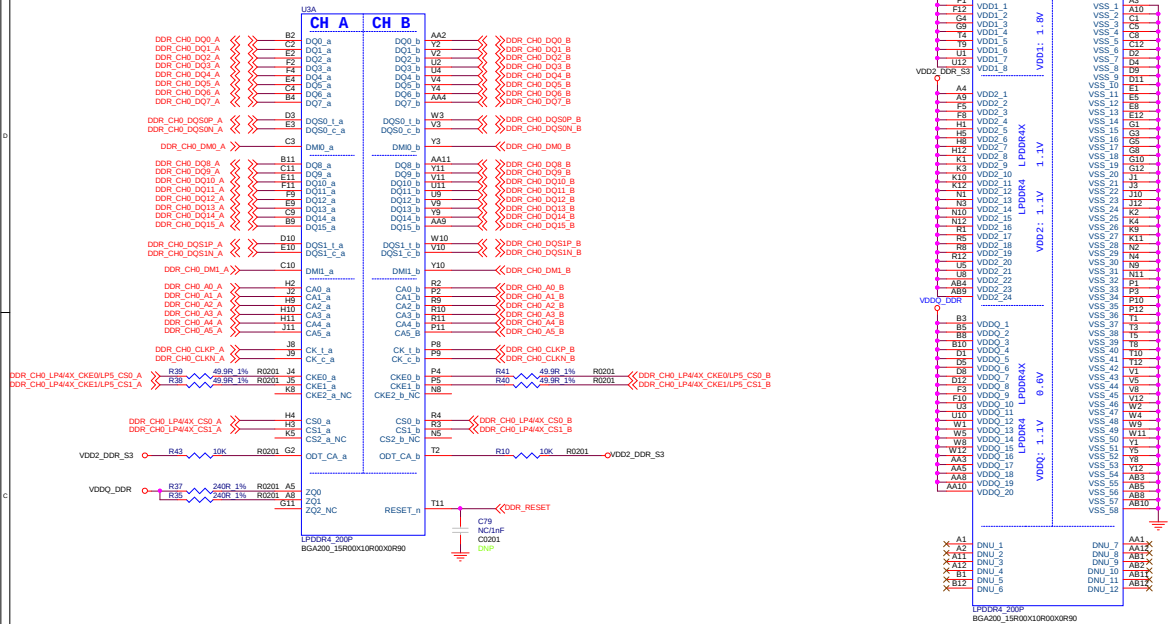
Component values:

- C147: 100nF, C0201
- C156: 4.7uF, C0402
- C138: 30uF, C0402
- C136: 100nF, C0201
- C132: 1uF, C0201
- C146: 100nF, C0201
- C155: 4.7uF, C0402
- C107: 4.7uF, C0402

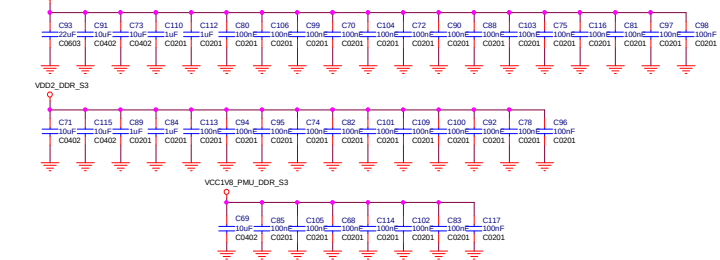
Reference: BGA1253 17R000417R000CP10

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	RK3588S_GPIOHDMI		
Date:	Tuesday, November 12, 2024	Rev:	V1.3
Designed_by:	Toni	Sheet:	11

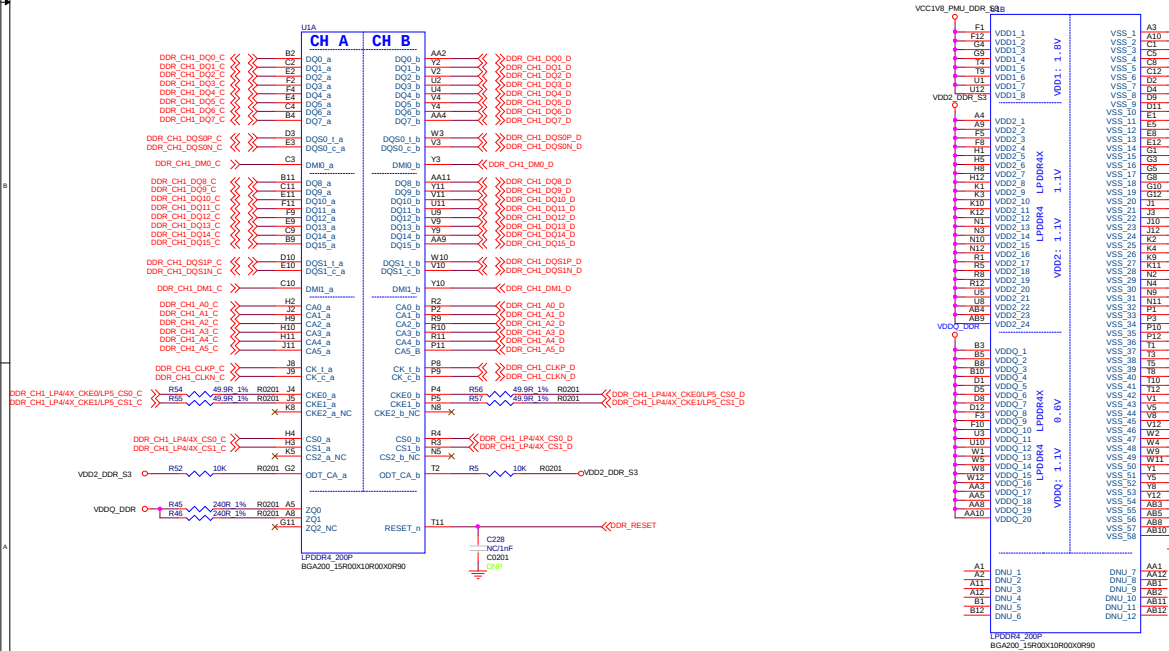
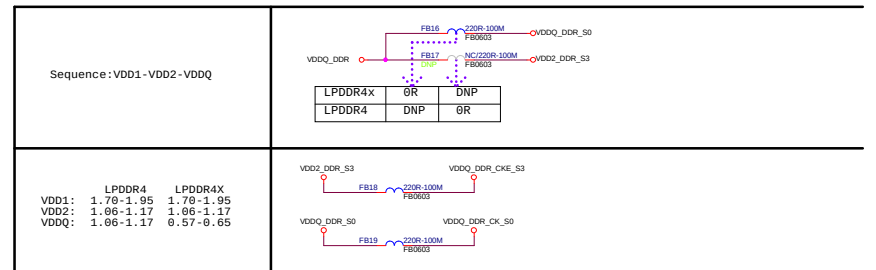
DRAM-LPDDR4/4x_2X32bit



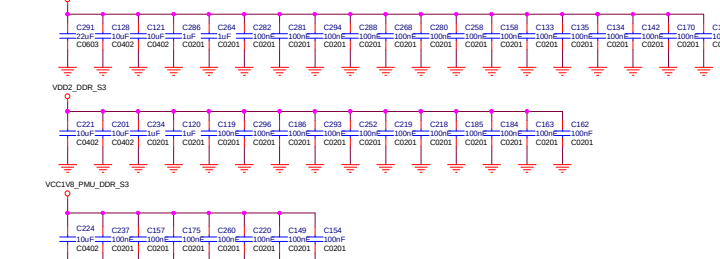
Sequence: VDD1-VDD2-VDDQ



DRAM-LPDDR4/4x Power

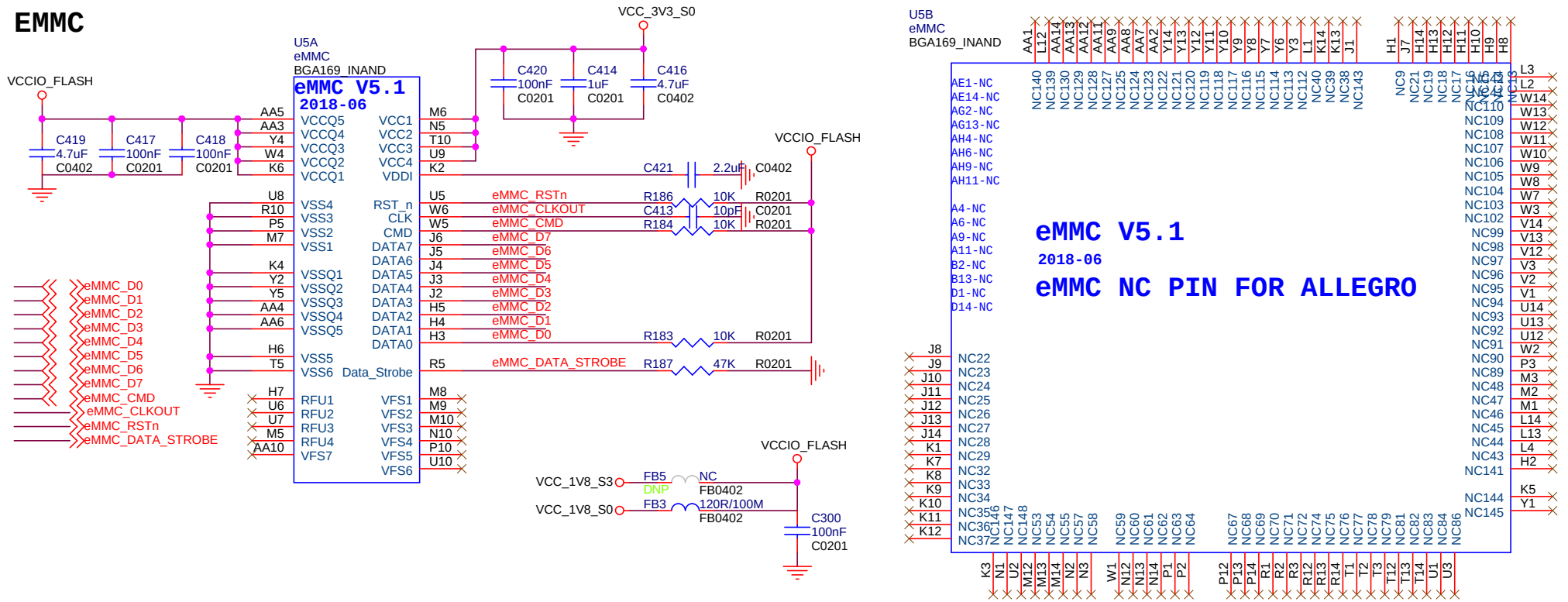


Sequence: VDD1-VDD2-VDDQ

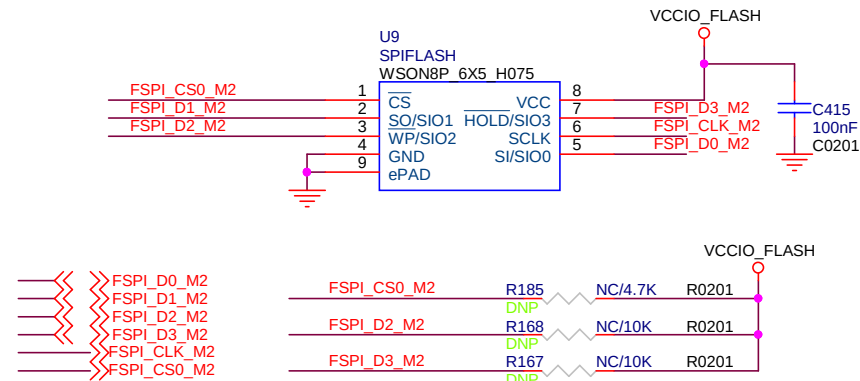


Shenzhen Wision Technology Co., Ltd.			
Project:	Edge2		
File:	DRAM_LPDDR4/4x		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed by:	Yan	Sheet:	12

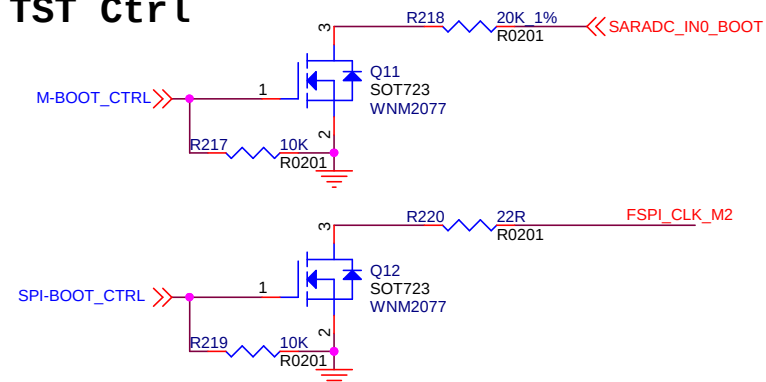
EMMC



SPI FLASH



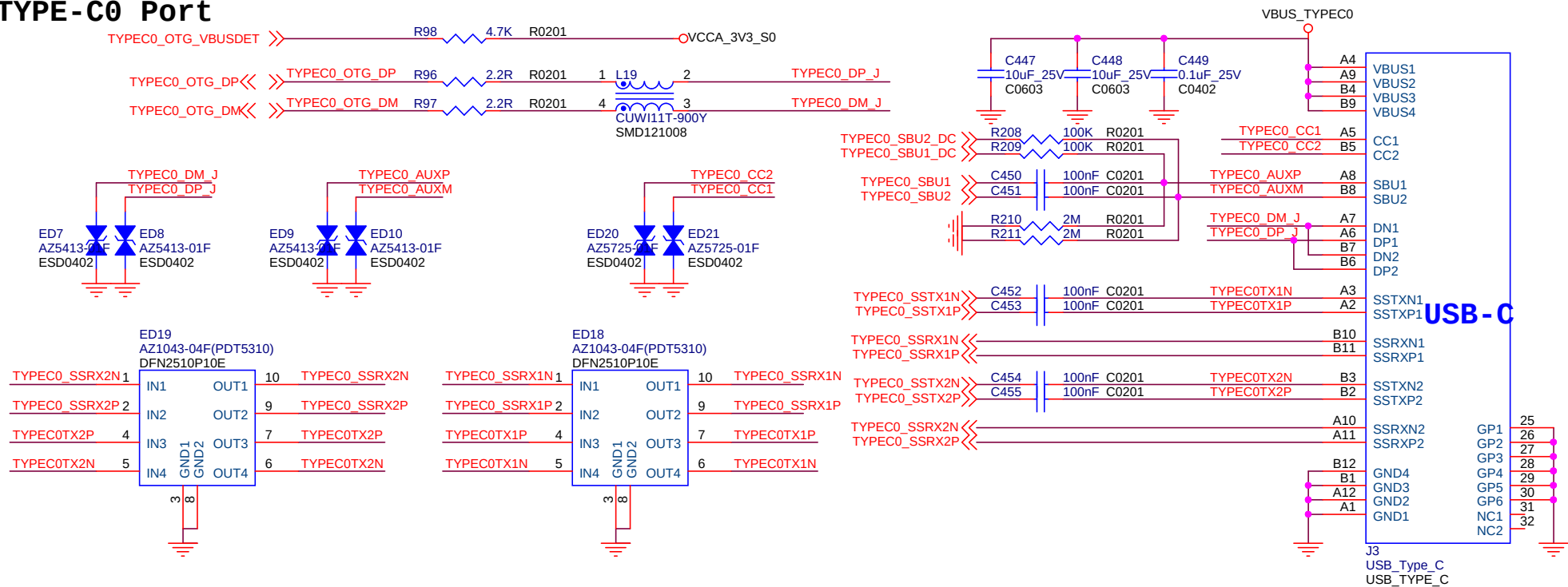
TST Ctrl



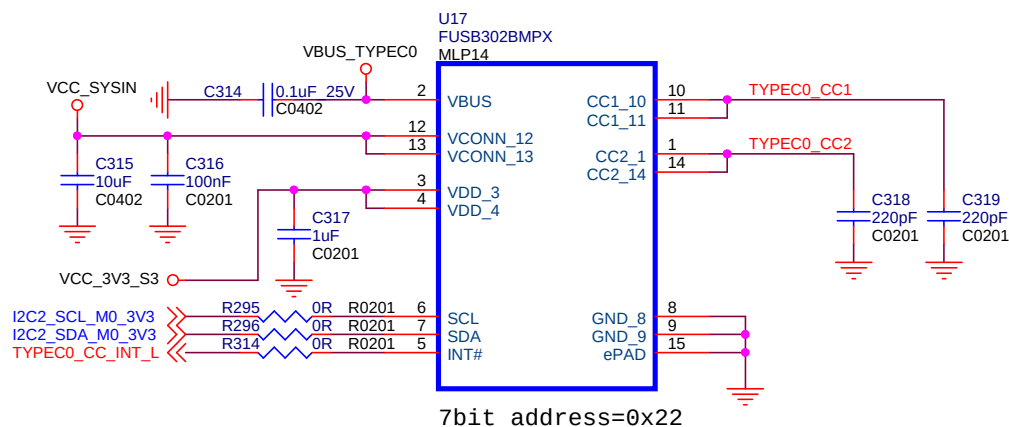
M-BOOT_CTRL	SPI-BOOT_CTRL	BOOT Priority
L	H	EMMC>USB
H	X	SD>USB
L	L	SPI>EMMC>USB

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	EMMC/SPI-FLASH		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	13

TYPE-C0 Port



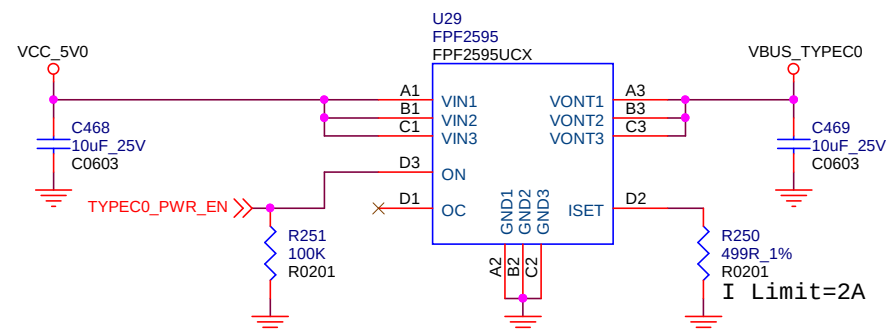
USB Type-C0 Controller(PD)



```
7bit address=0x22
```

VBUS of FUSB302BMPX: 4 to 21 TYP = 5V
VDD of FUSB302BMPX: 3 to 5.5 TYP = 3.3V
VCONN of FUSB302BMPX: 3 to 5.5

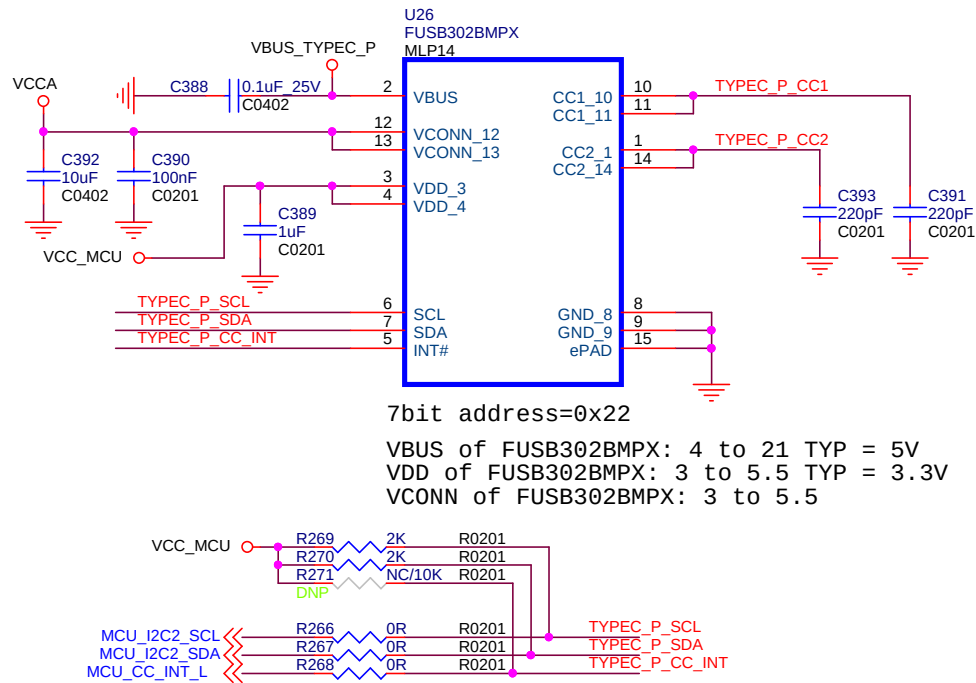
USB Type-C® POWER



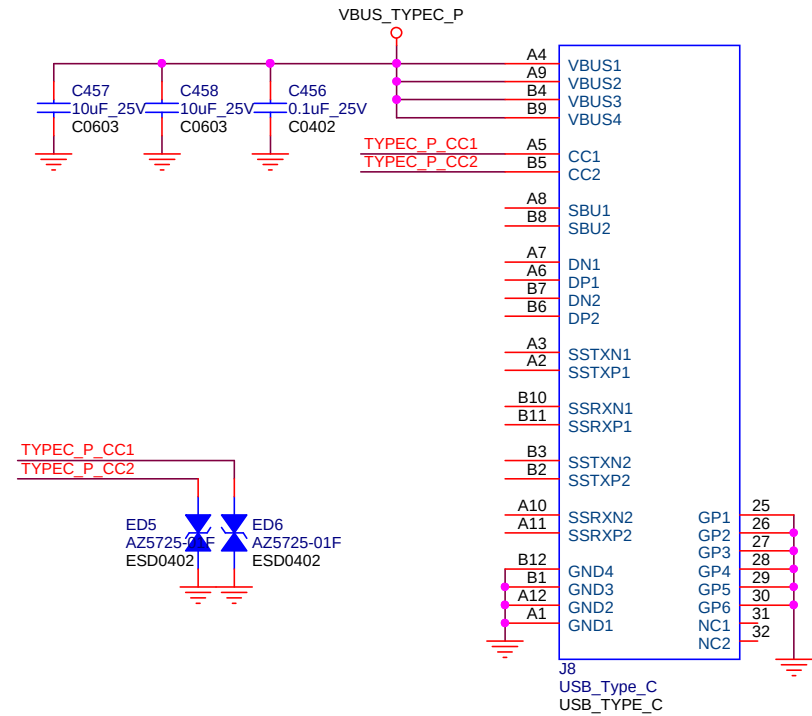
I Limit=2A

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	Type-C0 Port		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	14

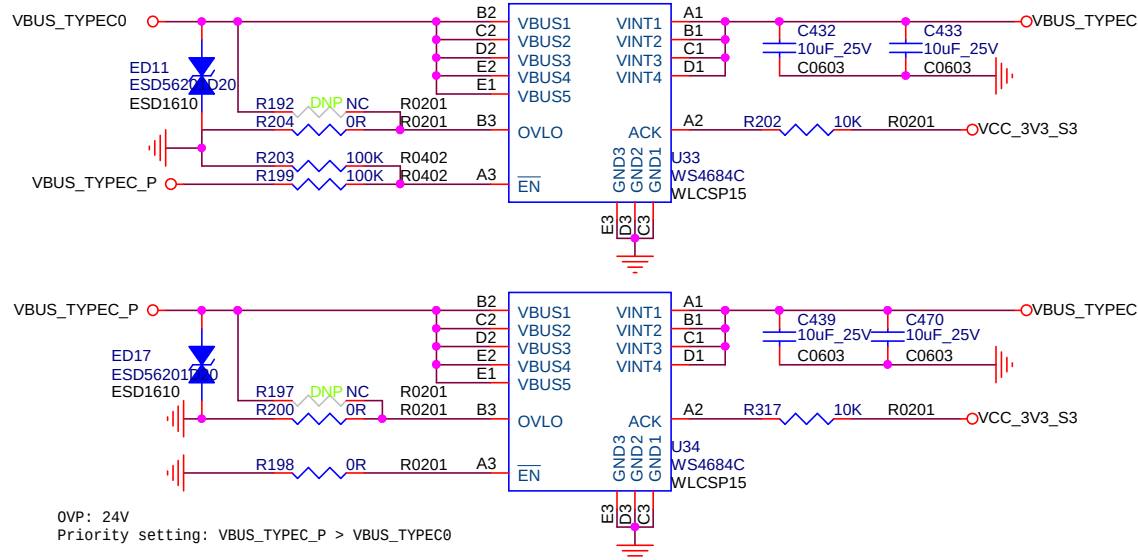
USB Type-C_P Controller(PD)



TYPE-C POWER-Only Port

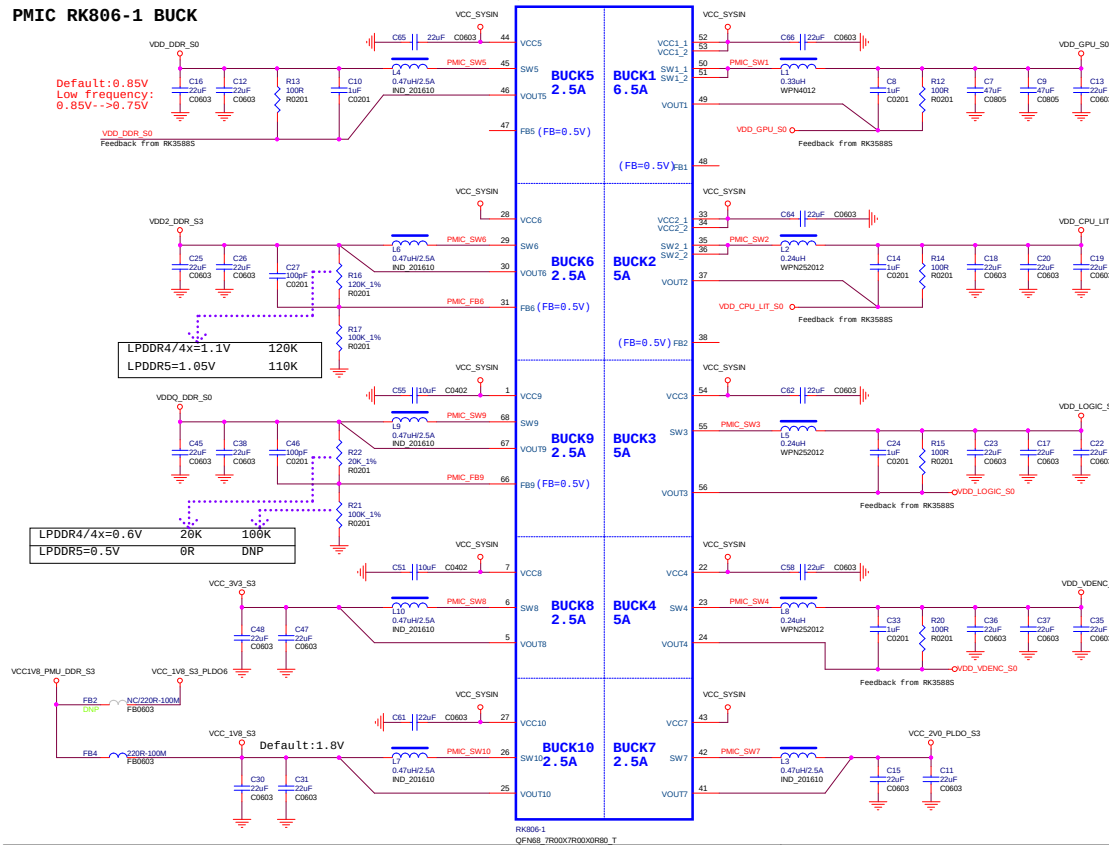


TPYE-C PWR PATH

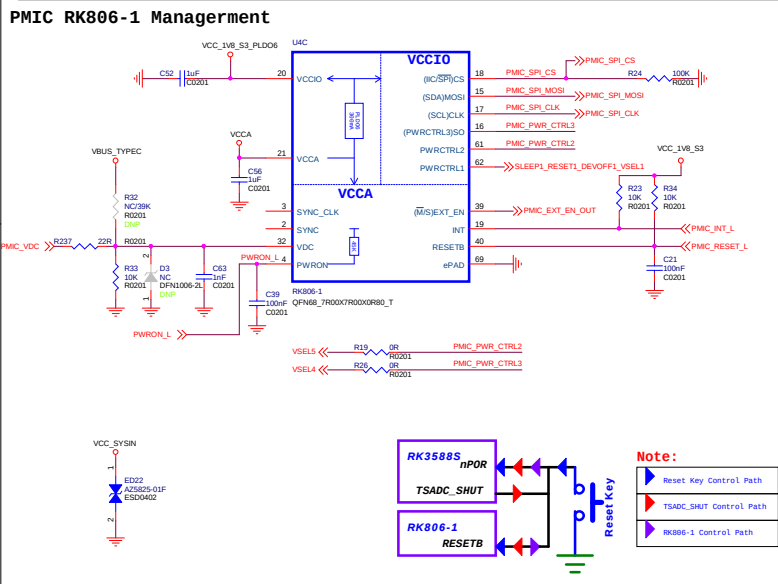


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	Type-C PWR Port		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	15

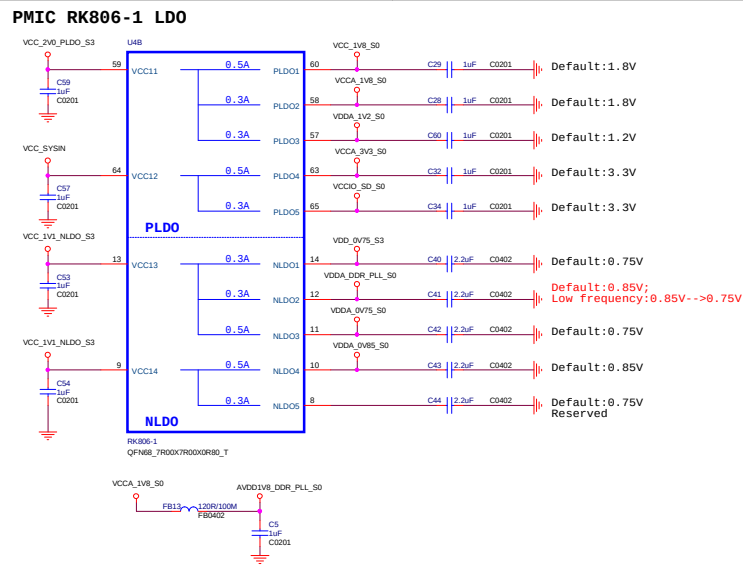
PMIC RK806-1 BUCK



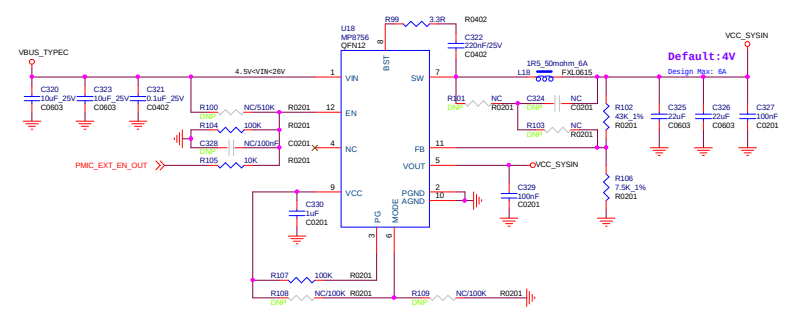
PMIC RK806-1 Managerment



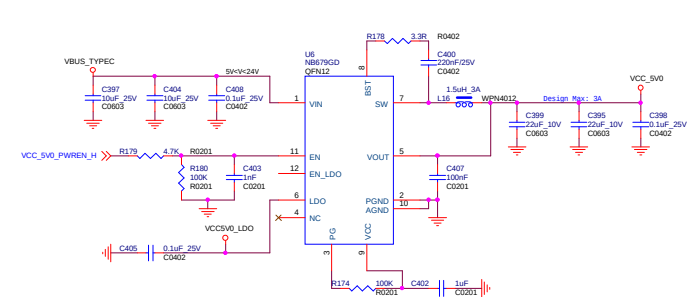
PMIC RK806-1 LDO



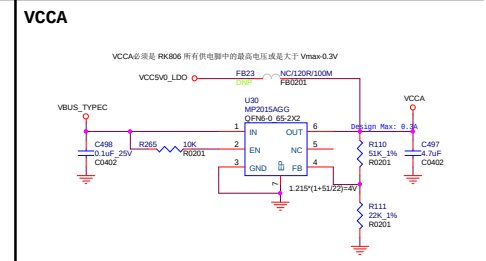
VCC_SYSIN



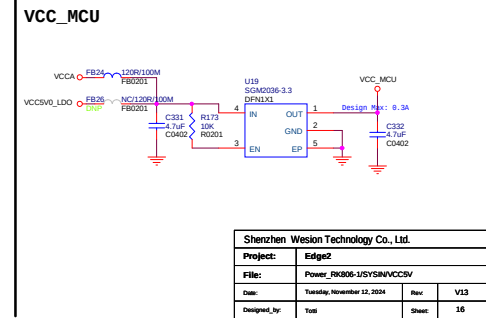
VCC5V



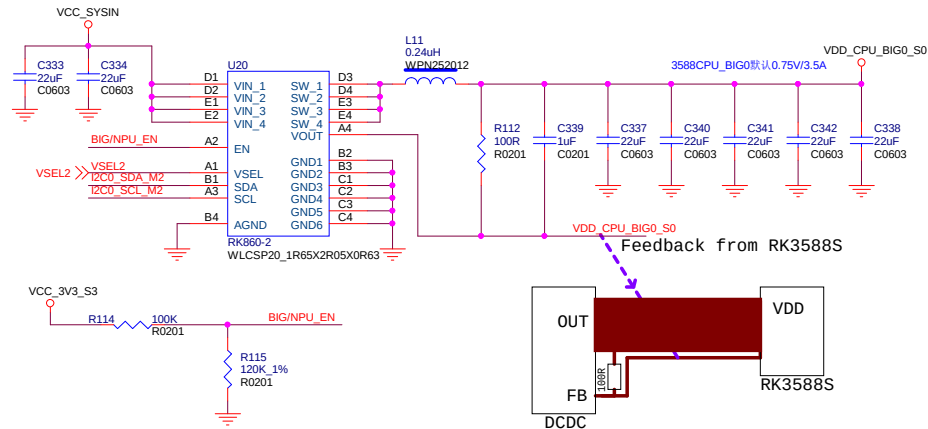
VCCA



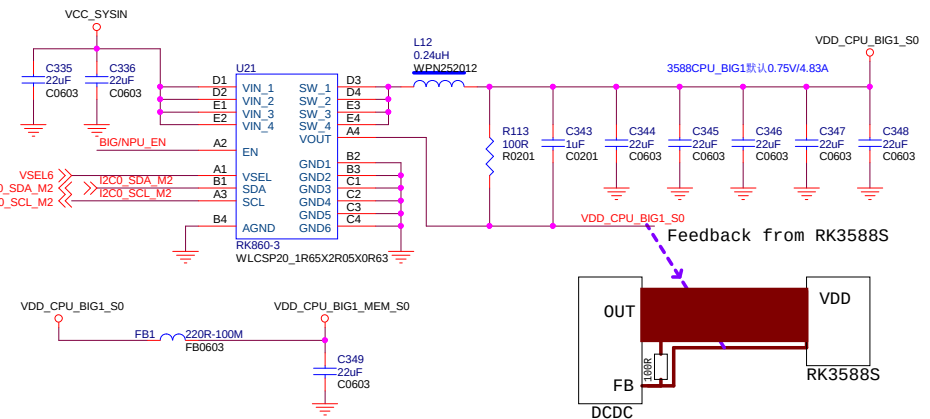
VCC_MCU



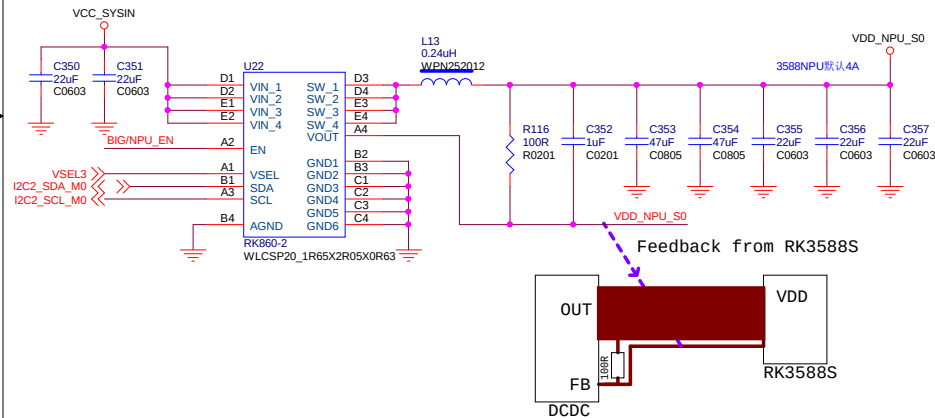
VDD_CPU_BIG0



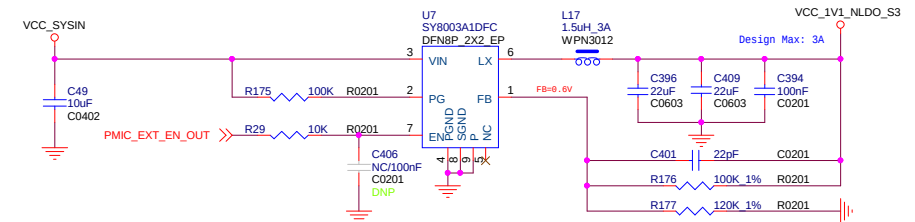
VDD_CPU_BIG1



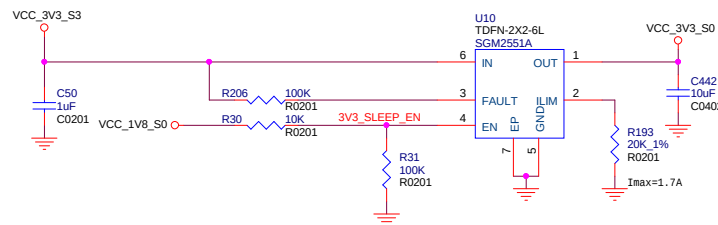
VDD_NPU



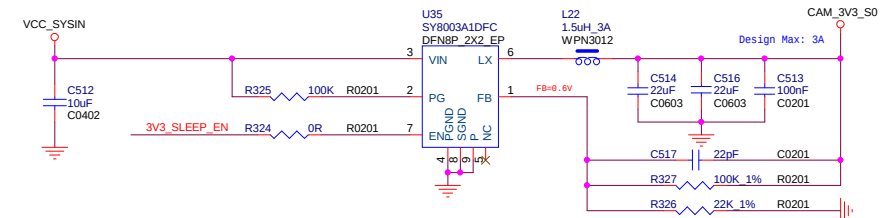
VCC_1V1_NLDO



VCC_3V3_S0

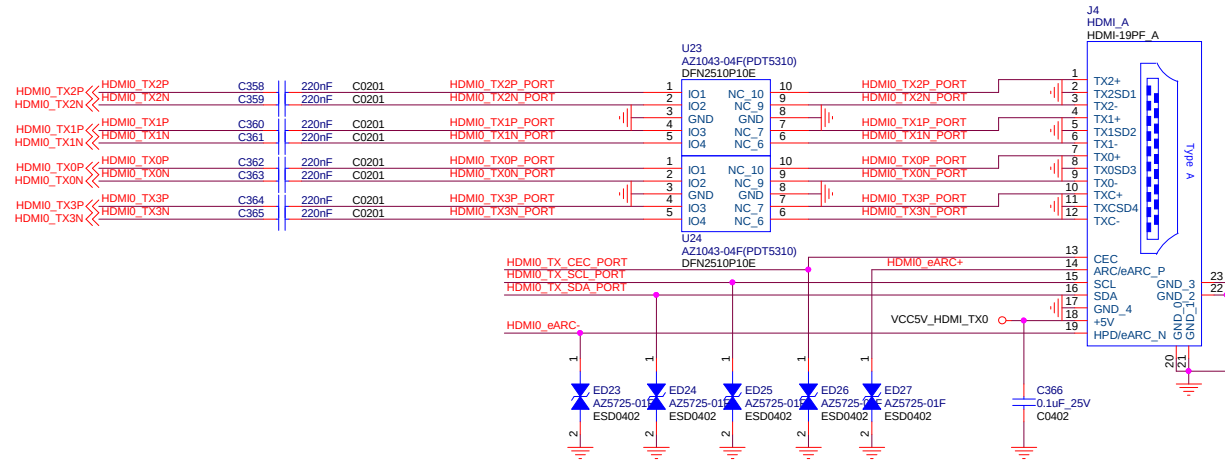


CAM_3V3_S0

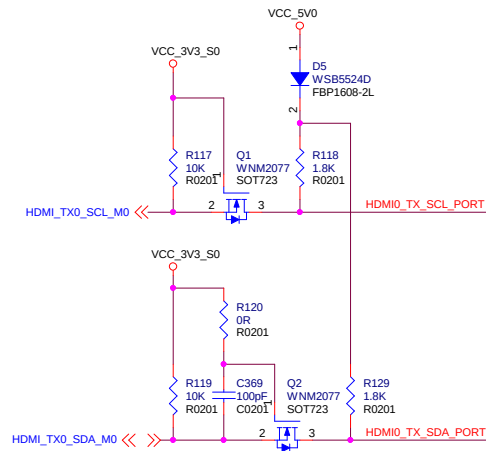


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	Power_Ext Discrete		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Toni	Sheet:	17

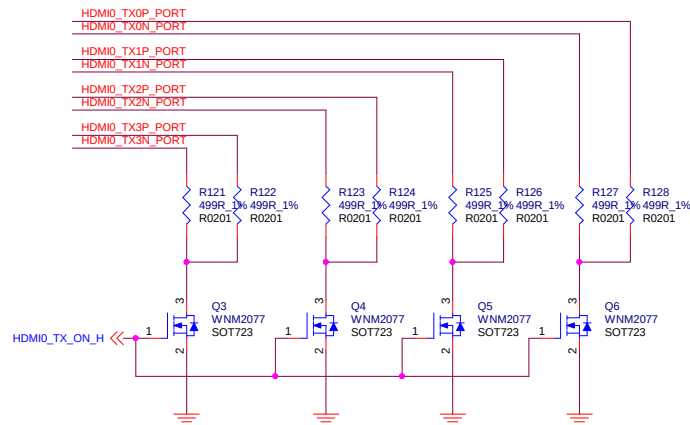
HDMI2.1 TX0



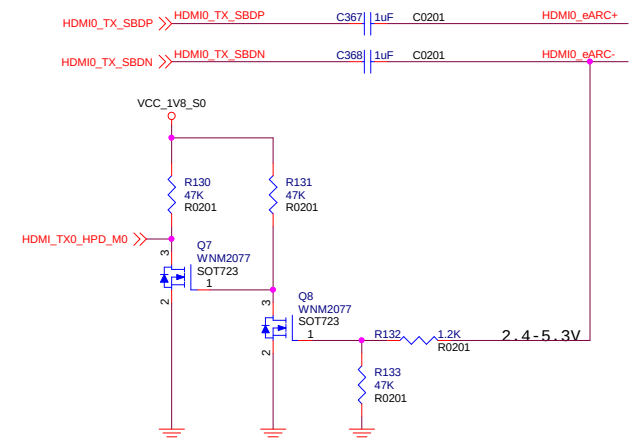
HDMI TX DDC



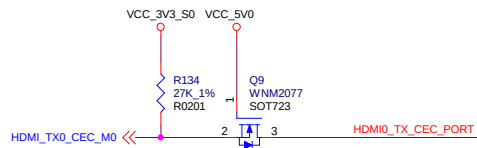
HDMI 2.0/2.1 SW



HDMI TX eARC



HDMI TX CEC

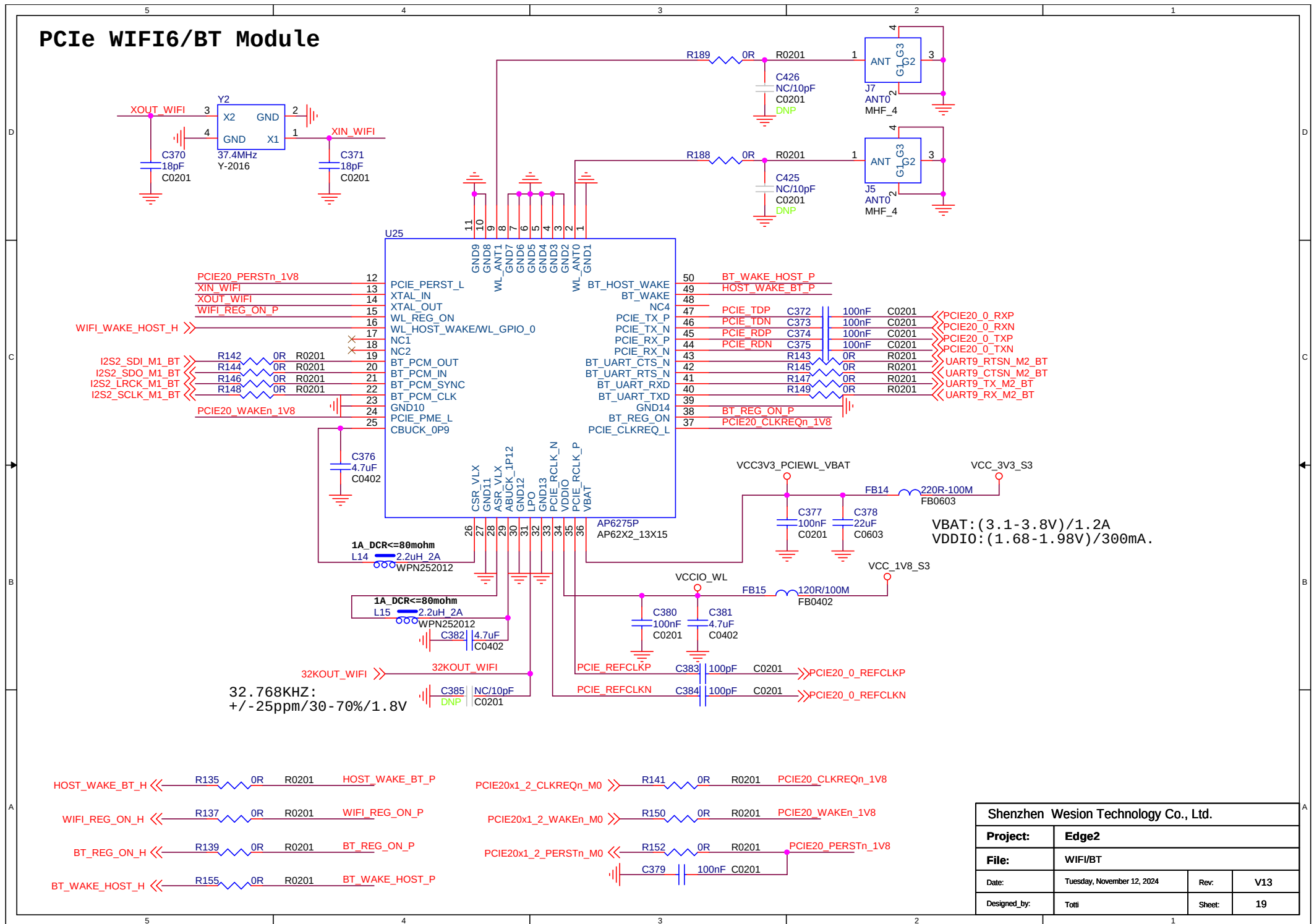


HDMI Power

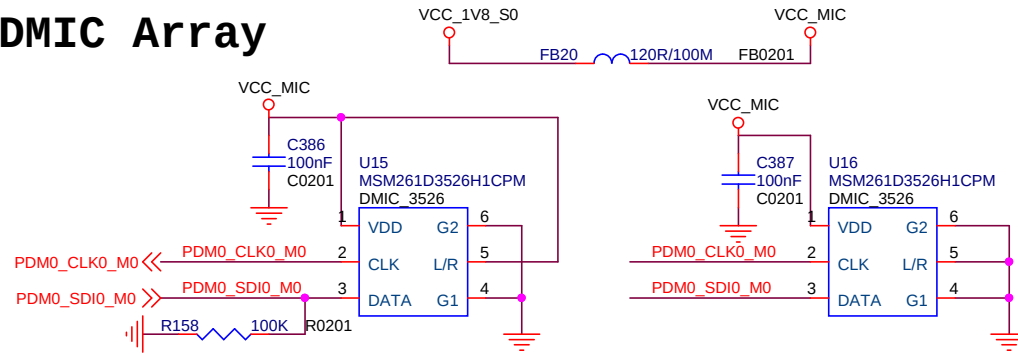


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	HDMI OUT		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Toni	Sheet:	18

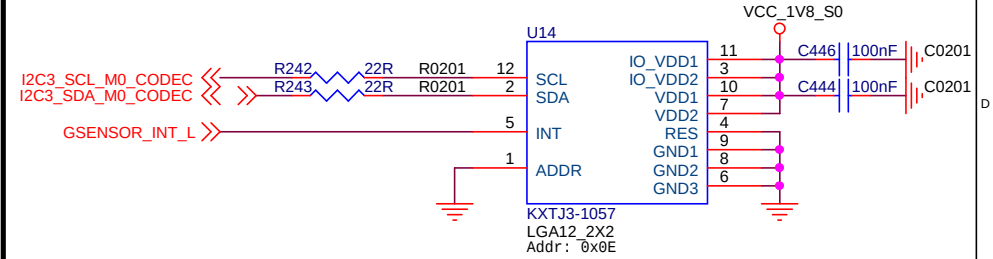
PCIe WIFI6/BT Module



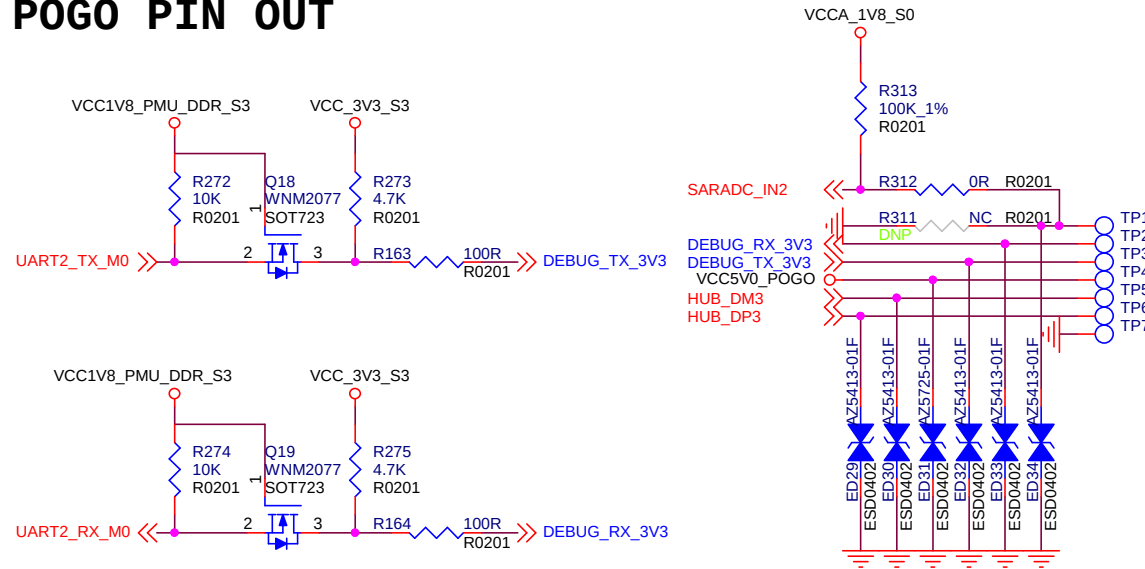
DMIC Array



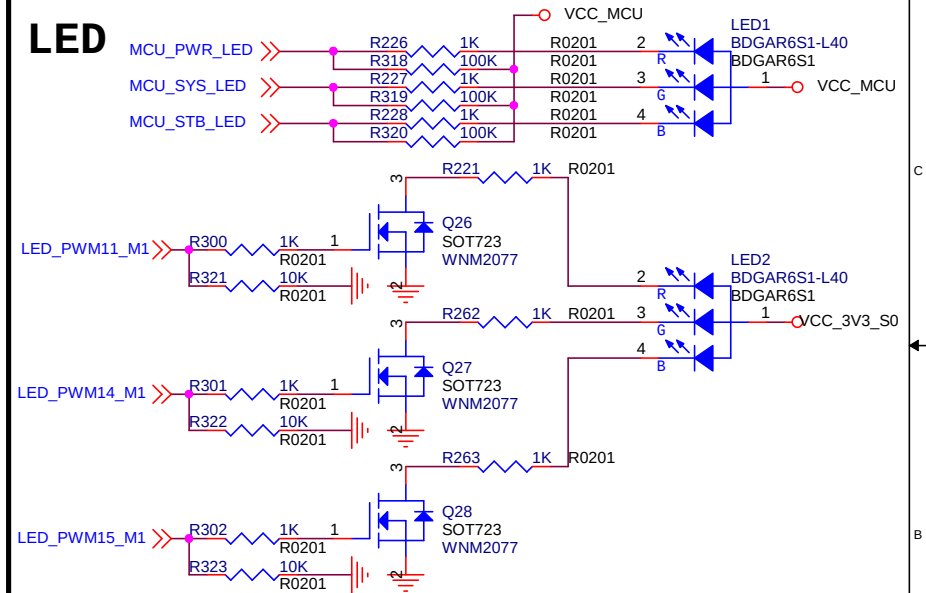
G-sensor



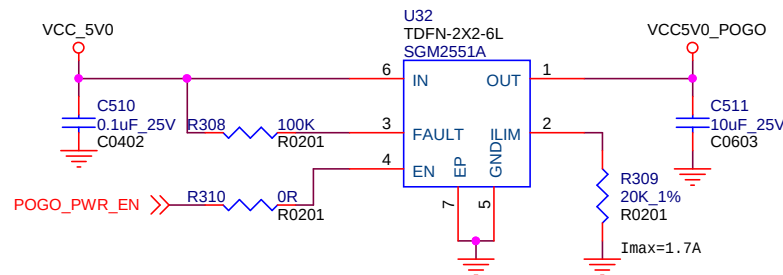
POGO PIN OUT



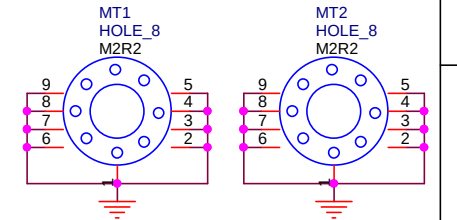
LED



POGO PIN PWR

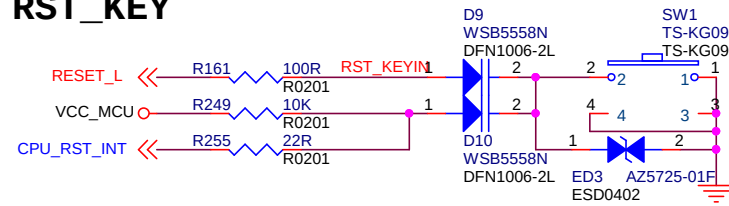


Mouting Holes

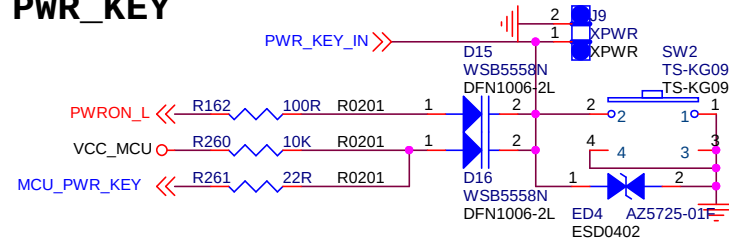


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	DMIC/Sensor/DEBUG/LED		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	21

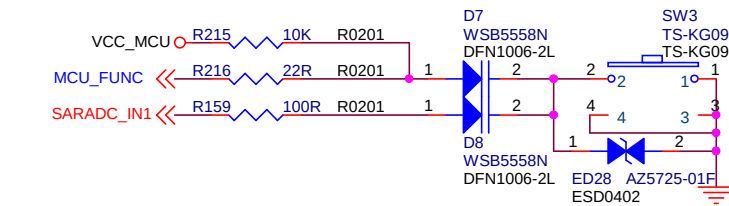
RST_KEY



PWR_KEY

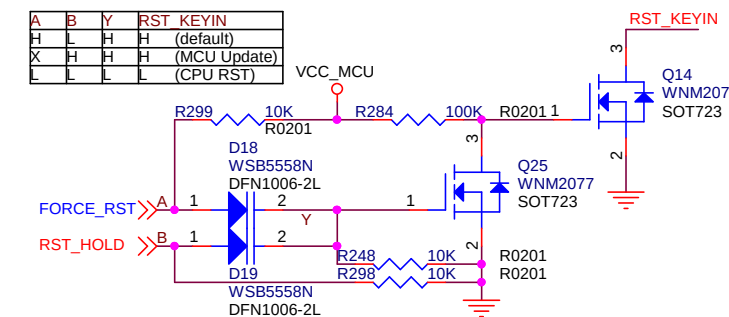


FUNCTION_KEY

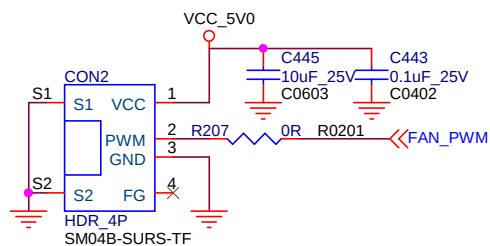


CPU RST CTRL

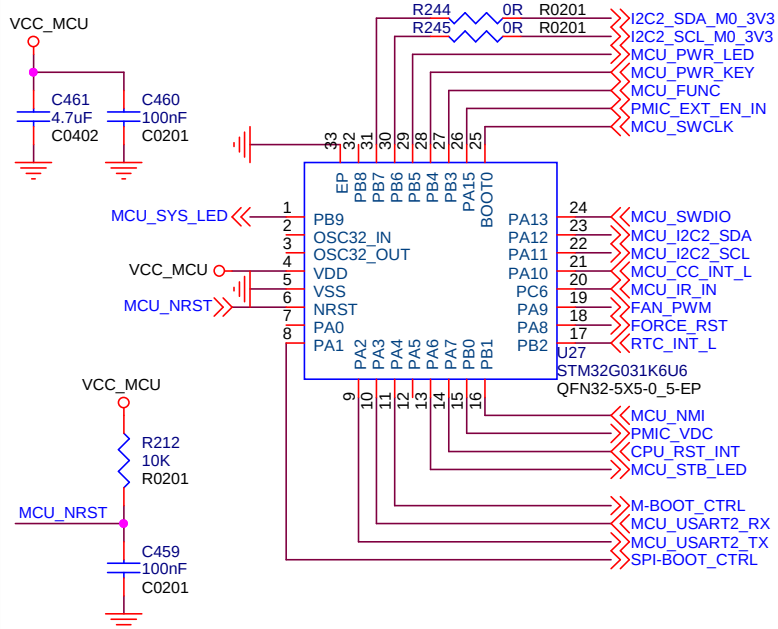
A	B	Y	RST_KEYIN
H	L	H	H (default)
X	H	H	H (MCU Update)
L	L	L	L (CPU RST)



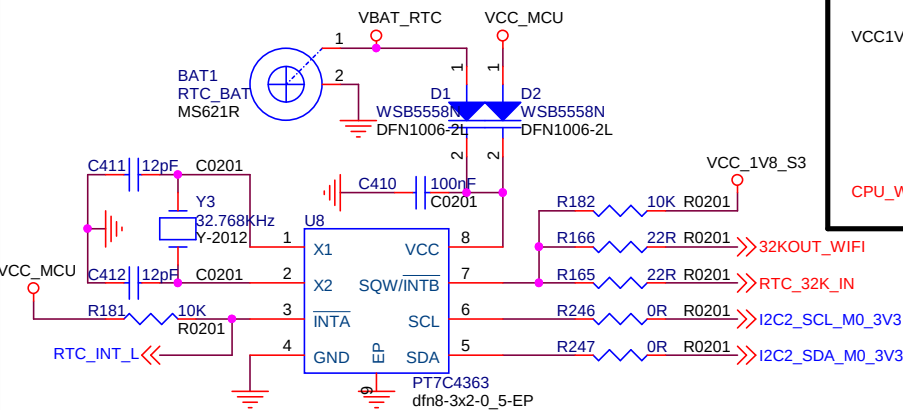
FAN



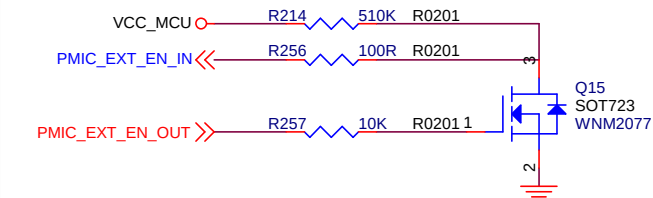
MCU



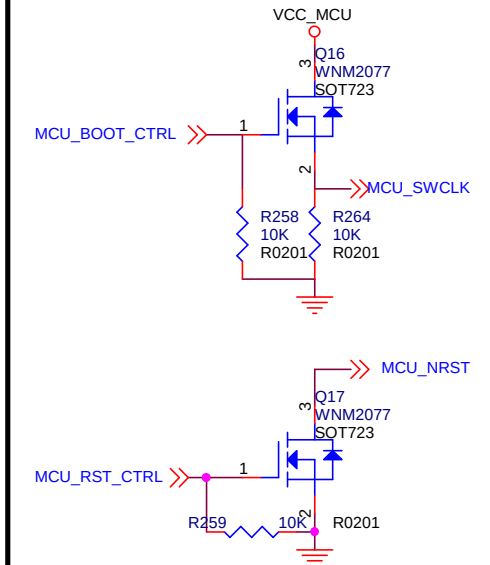
RTC



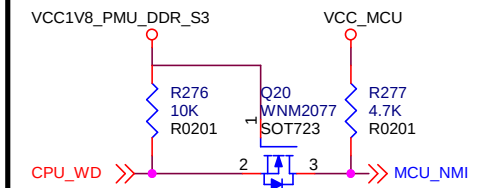
POWER OFF DET



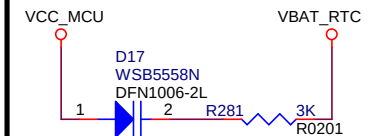
MCU BOOT CTRL



WATCH DOG



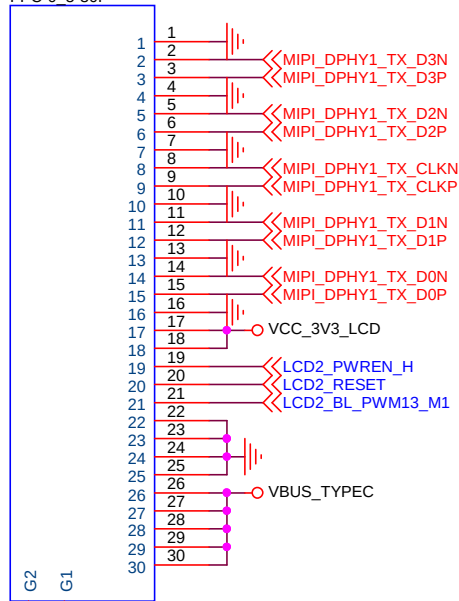
RTC CHARGE



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	MCU/RTC/KEY/FAN		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	22

DSI 2

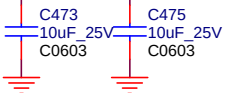
CON7
FH34SRJ-30S
FPC-0_5-30P



VCC_3V3_LCD



VBUS_TYPEC



VCCA_3V3_S0

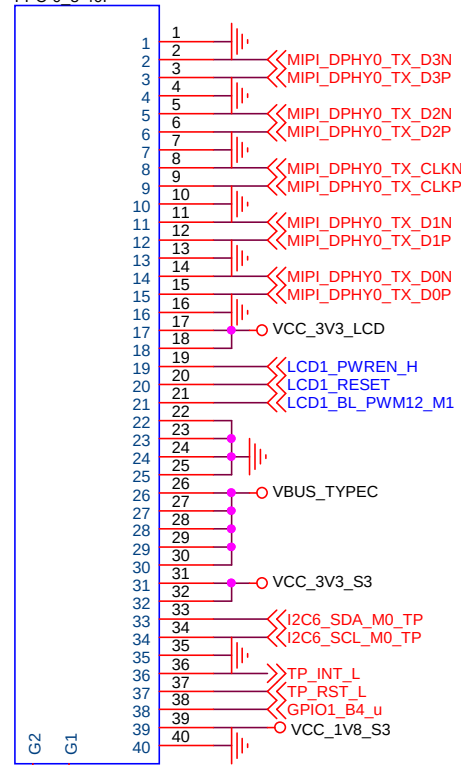
VCC_3V3_LCD

VCC_3V3_S0



DSI 1

CON4
FH34SRJ-40S
FPC-0_5-40P



VCC_3V3_LCD

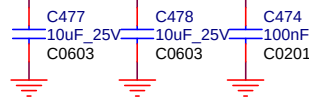


VCC_3V3_S3

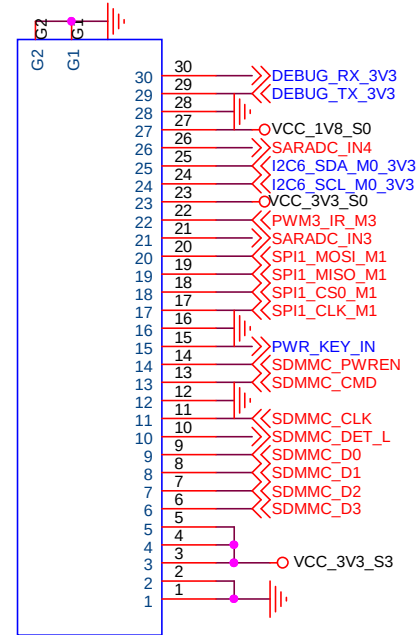


VBUS_TYPEC

VCC_1V8_S3



EXTIO 1



CON5
FH34SRJ-30S
FPC-0_5-30P

VCC_1V8_S0



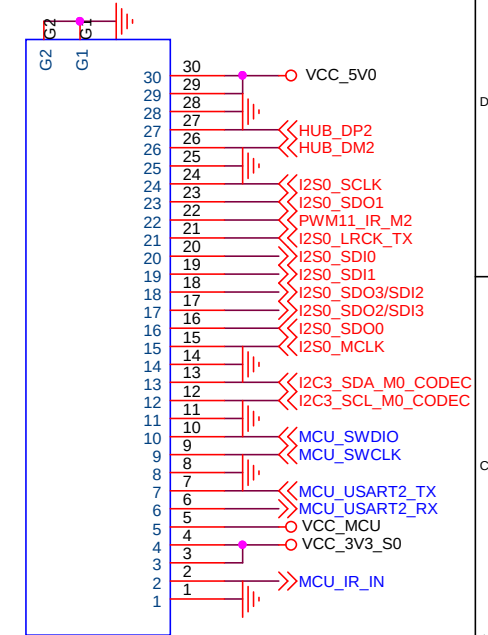
VCC_3V3_S0



VCC_3V3_S3



EXTIO 2



CON6
FH34SRJ-30S
FPC-0_5-30P

VCC_5V0



VCC_MCU



VCC_3V3_S0



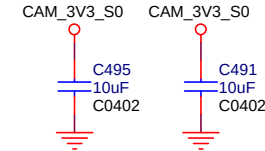
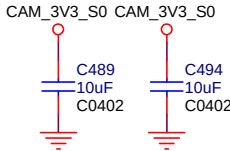
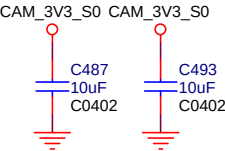
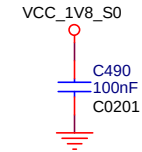
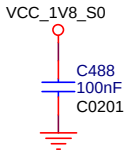
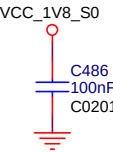
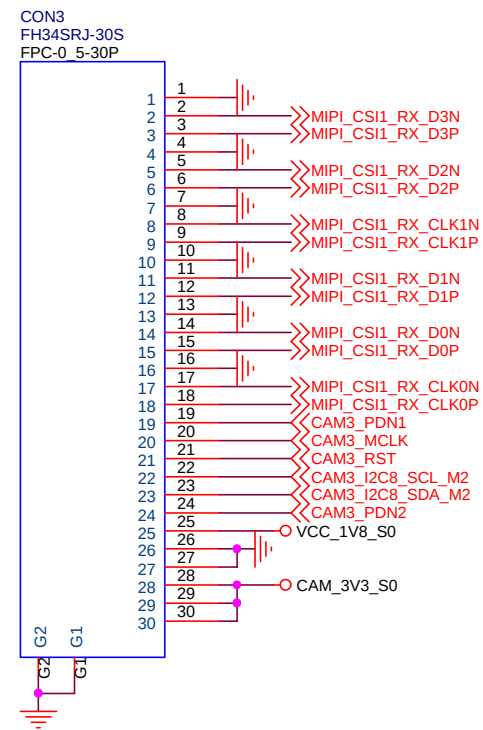
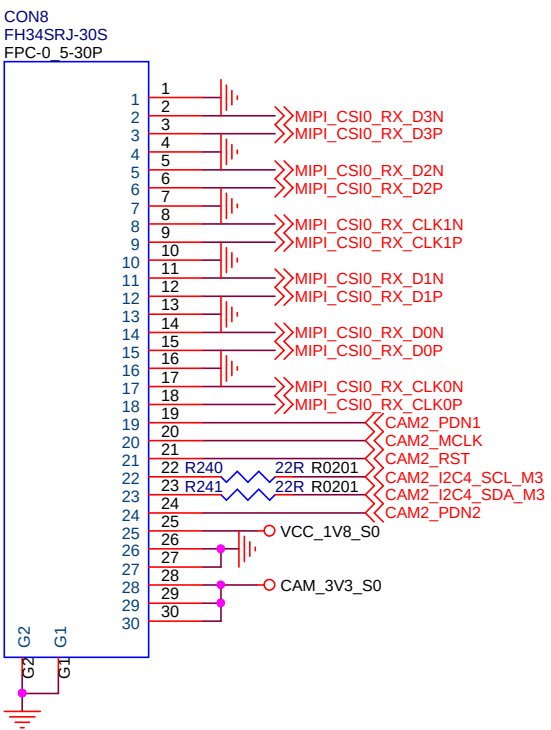
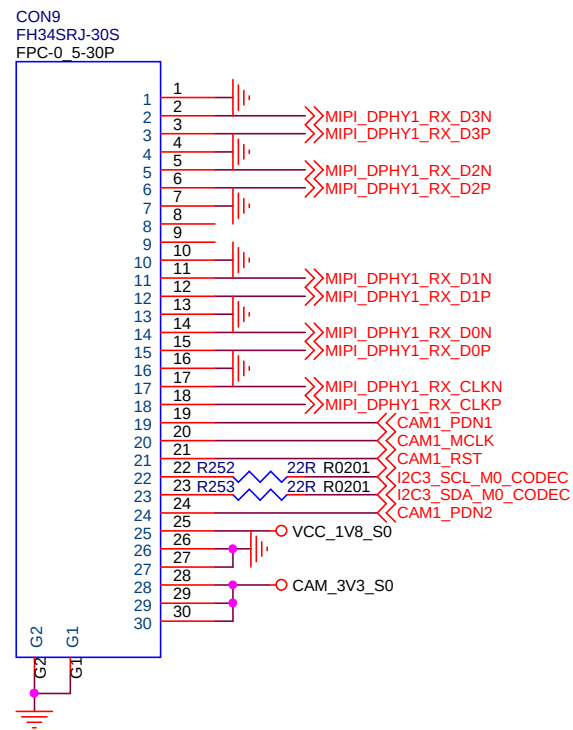
Shenzhen Wesion Technology Co., Ltd.

Project:	Edge2		
File:	EXT IO/MIPI LCD		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	23

Camera1

Camera2

Camera3



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	CAMERA_Connector		
Date:	Tuesday, November 12, 2024	Rev:	V13
Designed_by:	Totti	Sheet:	24