

VIM1S MCU Register Description

Notes :1、 I2C slave device address is 0x18. 2、 ROM could be saved when power off, but RAM couldn't.

Address	Register name	Byte	R/W	Store	Def Value	Register Description
0x12	VERSION1	1	R	RAM	-	MCU version Register. Read MCU firmware version.
0x13	VERSION2	1				
0x20	BOOT_MODE	1	R/W	ROM	1	Boot mode setting. 0x00: Start from SPI Flash. 0x01: Start from EMMC.
0x22	BOOT_RTC	1	R/W	ROM	1	SYS power on while RTC triger. 0x00: Disable. 0x01: Enable.
0x23	BOOT_EXP	1	R/W	ROM	1	SYS power on while GPIO triger. 0x00: Disable. 0x01: Enable.
0x24	BOOT_IR	1	R/W	ROM	1	SYS power on while IR remote triger. 0x00: Disable. 0x01: Enable.
0x25	BOOT_DCIN	1	R/W	ROM	1	SYS power on while DC in. 0x00: Disable. 0x01:Enable.
0x26	BOOT_KEY	1	R/W	ROM	1	SYS power on while KEY press. 0x00: Disable . 0x01: Enable .
0x27	KEY_MODE	1	R/W	ROM	0	Key power on mode setting. 0x00: Immediately power on. 0x01: Long press for 1 second to power on.
0x28	LED_ON	1	R/W	ROM	0	LED mode setting while power on. 0x00: Off. 0x01: Always bright. 0x02: breathing light. 0x03: Heartbeat light.
0x29	LED_OFF	1	R/W	ROM	1	LED mode setting while power off 0x00: Off. 0x01: Always bright. 0x02: breathing light. 0x03: Heartbeat light.
0x2C	REST_CONF	1	W	RAM	-	EEPROM reset. 0x00: Reset EEPROM configuration parameter.
0x2E	SLEEP_EN	1	R/W	ROM	0	MCU sleep after SYS power off. 0x00: Disable. 0x01: Enable.
0x2F	IR1_CUST1	1	R/W	ROM	0	Custom infrared button 1 address code high register. Note: Registers 0x2F-0x32 allow users to customize one infrared key.
0x30	IR1_CUST2	1	R/W	ROM	0	Custom infrared button 1 address code low register.
0x31	IR1_ORDER1	1	R/W	ROM	0	Custom infrared button 1 data code high register.
0x32	IR1_ORDER2	1	R/W	ROM	0	Custom infrared button 1 data code low register.
0x34	IR2_CUST1	1	R/W	ROM	0	Custom infrared button 2 address code high register. Note: Registers 0x34-0x37 allow users to customize another infrared key.
0x35	IR2_CUST2	1	R/W	ROM	0	Custom infrared button 2 address code low register.
0x36	IR2_ORDER1	1	R/W	ROM	0	Custom infrared button 2 data code high register.
0x37	IR2_ORDER2	1	R/W	ROM	0	Custom infrared button 2 data code low register.
0x80	PWR_OFF	1	W	RAM	-	Shutdown control. 0x01: System shutdown.
0x89	LED_ON_RAM	1	W	RAM	0	LED mode setting while power on(setting not save while power off). 0x00: Off. 0x01: Always on. 0x02: breathing light. 0x03: Heartbeat light.
0x8A	FAN_CTRL	1	W	RAM	-	Fan speed setting register (Note: the parameter value of fan normally open is 0x1E-0x64). Parameter range: 0x00-0x64.
0x8B	WDT_EN	1	W	RAM	-	WDT Enable or disable. 0x00: Disable WDG detection. 0x01: Enable WDG detection.(System set at 500ms, MCU detects at 1000ms.)
0x91	SYS_RST	1	W	RAM	-	System reset (Single operation not saved). 0x00: System reset and start from SPI Flash. 0x02: System reset and start from EMMC. 0x01: System reset.

Version	Edit	Review	Revision content
V1.0	Kenny	Kenny	Initial version -230605