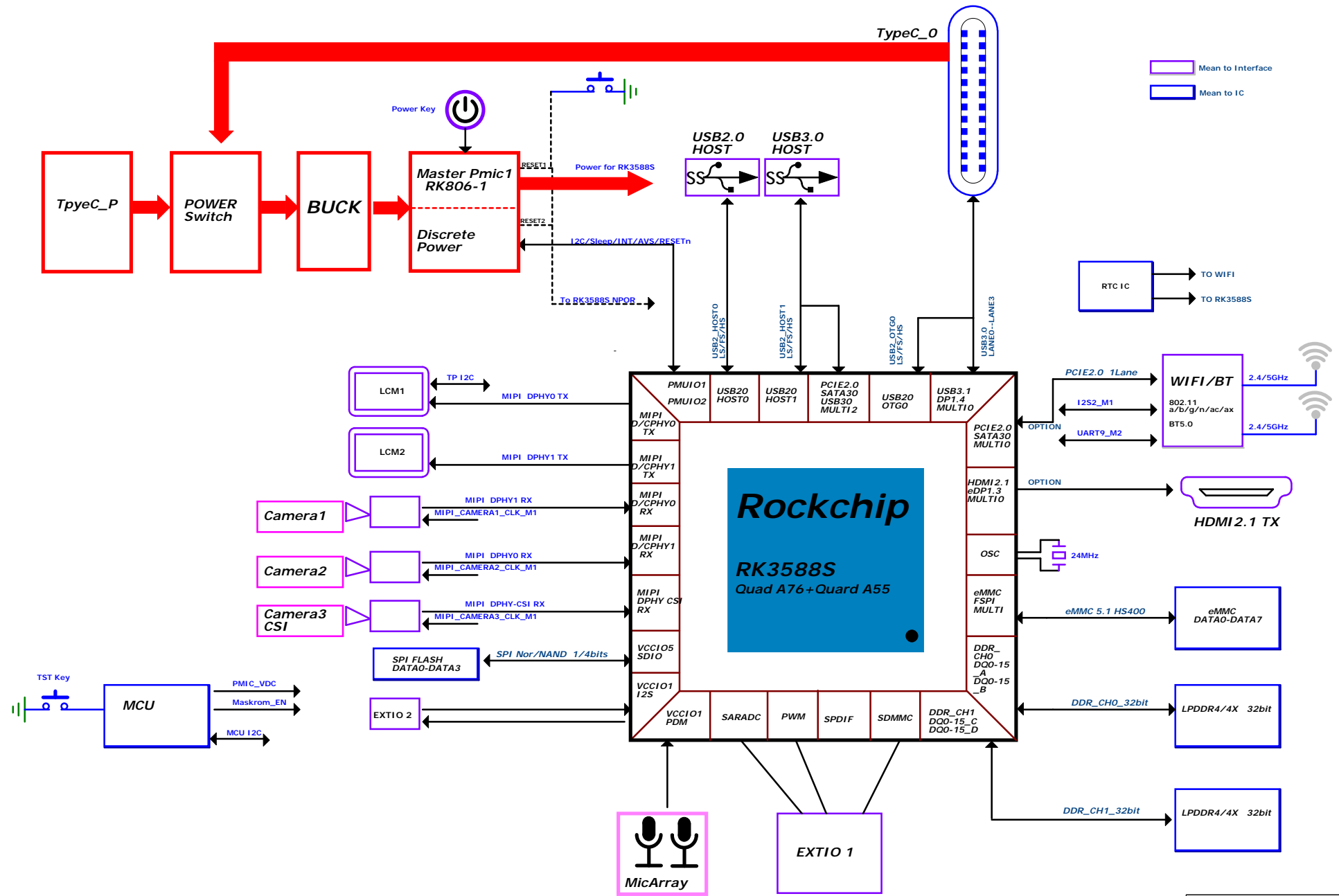




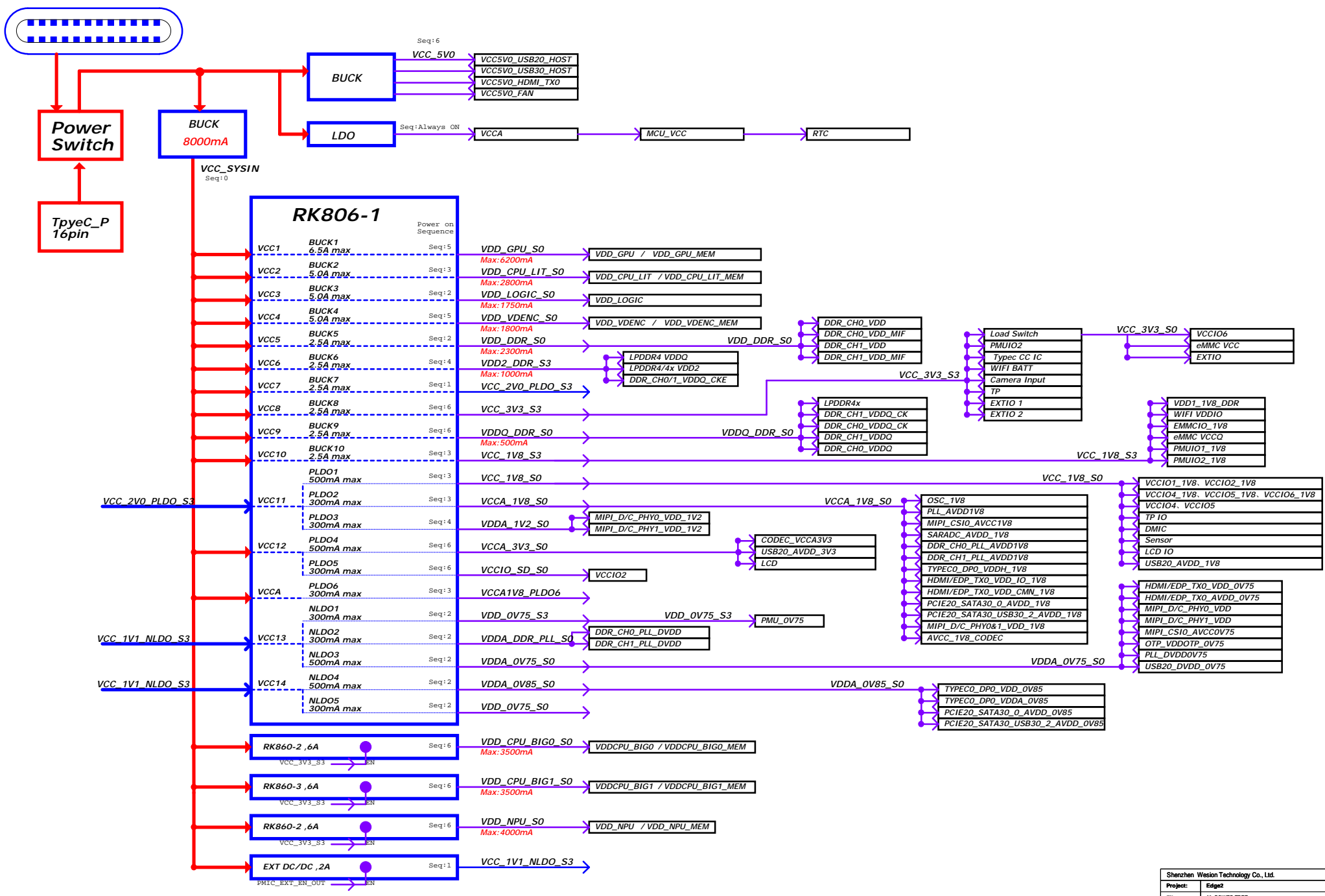
KHADAS

Something a little different.

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	01_Khadas		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	1

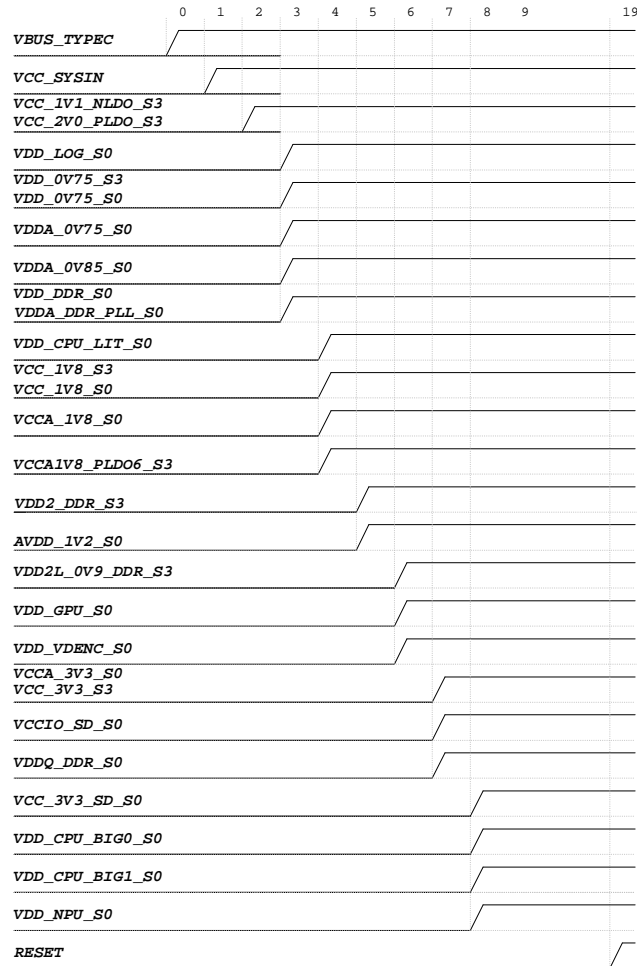


Shenzhen Wision Technology Co., Ltd.			
Project:	Edge2	Rev:	V11
File:	02_BLOCK	Draw:	2
Date:	Tuesday, September 20, 2023	Draw:	2
Design:by:	Tian	Draw:	2



Shenzhen Wision Technology Co., Ltd.			
Project:	Edge2	Rev:	V11
File:	03_POWER TREE	Date:	Tuesday, September 20, 2023
Design: Jyr	Yan	Draw:	3

Power Sequence



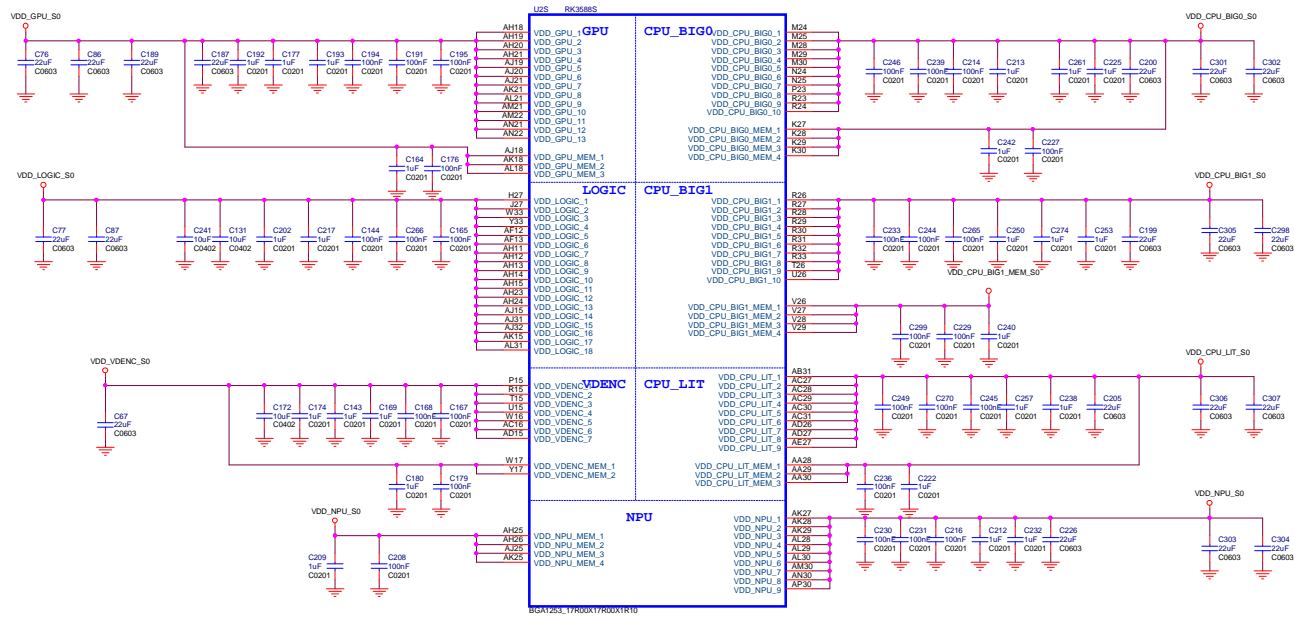
Power Supply	PMIC Channel	Supply Limit	Power Name	Time Slot	Default Voltage	Default ON/OFF	Sleep ON/OFF	Peak Current	Sleep Current
VCC_SYSIN	RK806-1_BUCK1	6.5A	VDD_GPU_S0	Slot:5	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK2	5A	VDD_CPU_LIT_S0	Slot:3	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK3	5A	VDD_LOG_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK4	3A	VDD_VDENC_S0	Slot:5	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK5	2.5A	VDD_DDR_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK6	2.5A	VDD2_DDR_S3	Slot:4	ADJ FB=0.5V	ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK7	2.5A	VCC_2V0_PLDO_S3	Slot:1	2.0V	ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK8	2.5A	VCC_3V3_S3	Slot:6	3.3V	ON	ON	TBD	TBD
VCC_SYSIN	RK806-1_BUCK9	2.5A	VDDQ_DDR_S0	Slot:6	ADJ FB=0.5V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_BUCK10	2.5A	VCC_1V8_S3	Slot:3	1.8V	ON	ON	TBD	TBD
VCC_2V0_PLDO_S3	RK806-1_PLDO1	0.5A	VCC_1V8_S0	Slot:3	1.8V	ON	OFF	TBD	TBD
	RK806-1_PLDO2	0.3A	VCCA_1V8_S0	Slot:3	1.8V	ON	OFF	TBD	TBD
	RK806-1_PLDO3	0.3A	VDDA_1V2_S0	Slot:4	1.2V	ON	OFF	TBD	TBD
VCC_SYSIN	RK806-1_PLDO4	0.5A	VCCA_3V3_S0	Slot:6	3.3V	ON	OFF	TBD	TBD
	RK806-1_PLDO5	0.3A	VCCIO_SD_S0	Slot:6	3.3V	ON	OFF	TBD	TBD
	RK806-1_PLDO6	0.3A	VCCA1V8_PLDO6_S3	Slot:3	1.8V	ON	ON	TBD	TBD
VCC_1V1_NLDO_S3	RK806-1_NLDO1	0.3A	VDD_0V75_S3	Slot:2	0.75V	ON	ON	TBD	TBD
	RK806-1_NLDO2	0.3A	VDDA_DDR_PLL_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
	RK806-1_NLDO3	0.5A	VDDA_0V75_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_1V1_NLDO_S3	RK806-1_NLDO4	0.5A	VDDA_0V85_S0	Slot:2	0.85V	ON	OFF	TBD	TBD
	RK806-1_NLDO5	0.3A	VDD_0V75_S0	Slot:2	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-2	6A	VDD_CPU_BIG0_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-3	6A	VDD_CPU_BIG1_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	BUCK_RK860-2	6A	VDD_NPU_S0	Slot:6A	0.75V	ON	OFF	TBD	TBD
VCC_SYSIN	EXT BUCK	2A	VCC_1V1_NLDO_S3	Slot:1	1.1V	ON	ON	TBD	TBD

IO Power Domain Map

IO Domain	Pin Num	Support IO Voltage	Supply Power Pin Name	Power Source	Operating Voltage
PMUIO1	Pin N36 N37	1.8V Only	PMUIO1_1V8	VCC_1V8_S3	1.8V
PMUIO2	Pin V37 Y37	1.8V or 3.3V	PMUIO2_1V8	VCC_1V8_S3	1.8V
	Pin V35 V36		PMUIO2	VCC_1V8_S3	1.8V
EMMCIO	Pin AC35	1.8V Only	EMMCIO_1V8	VCC_1V8_S0	1.8V
	Pin AC36				
VCCIO1	Pin H31	1.8V Only	VCCIO1_1V8	VCC_1V8_S0	1.8V
VCCIO2	Pin AK11	1.8V or 3.3V	VCCIO2_1V8	VCC_1V8_S0	1.8V
	Pin AK10		VCCIO2	VCC_IO_SD	1.8V/3.3V
VCCIO4	Pin G27 G28	1.8V or 3.3V	VCCIO4_1V8	VCC_1V8_S0	1.8V
	Pin G31		VCCIO4	VCC_1V8_S0	1.8V
VCCIO5	Pin AF35 AF36	1.8V or 3.3V	VCCIO5_1V8	VCC_1V8_S0	1.8V
	Pin AC33 AC34		VCCIO5	VCC_1V8_S0	1.8V
VCCIO6	Pin AJ34	1.8V or 3.3V	VCCIO6_1V8	VCC_1V8_S0	1.8V
	Pin AL33 AM33		VCCIO6	VCC_3V3_S0	3.3V

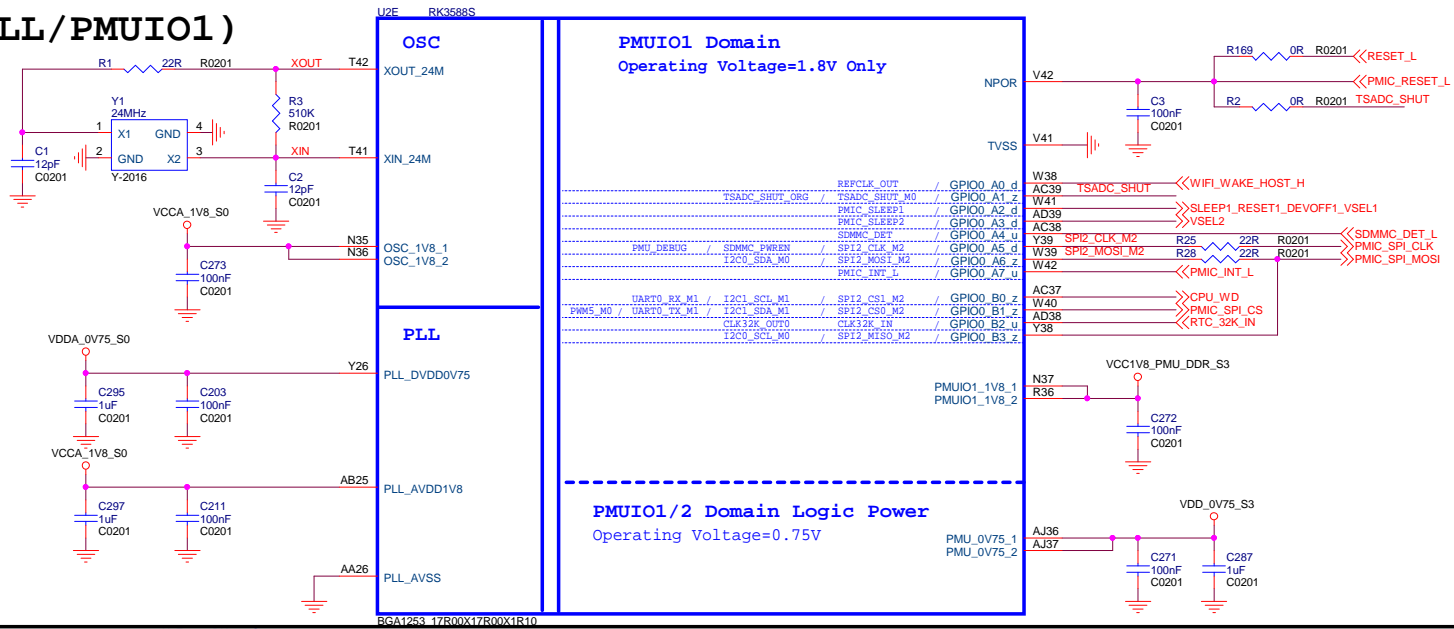
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	04_System Power Sequence		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Toxi	Sheet:	4

RK3588S (Power&Gnd)

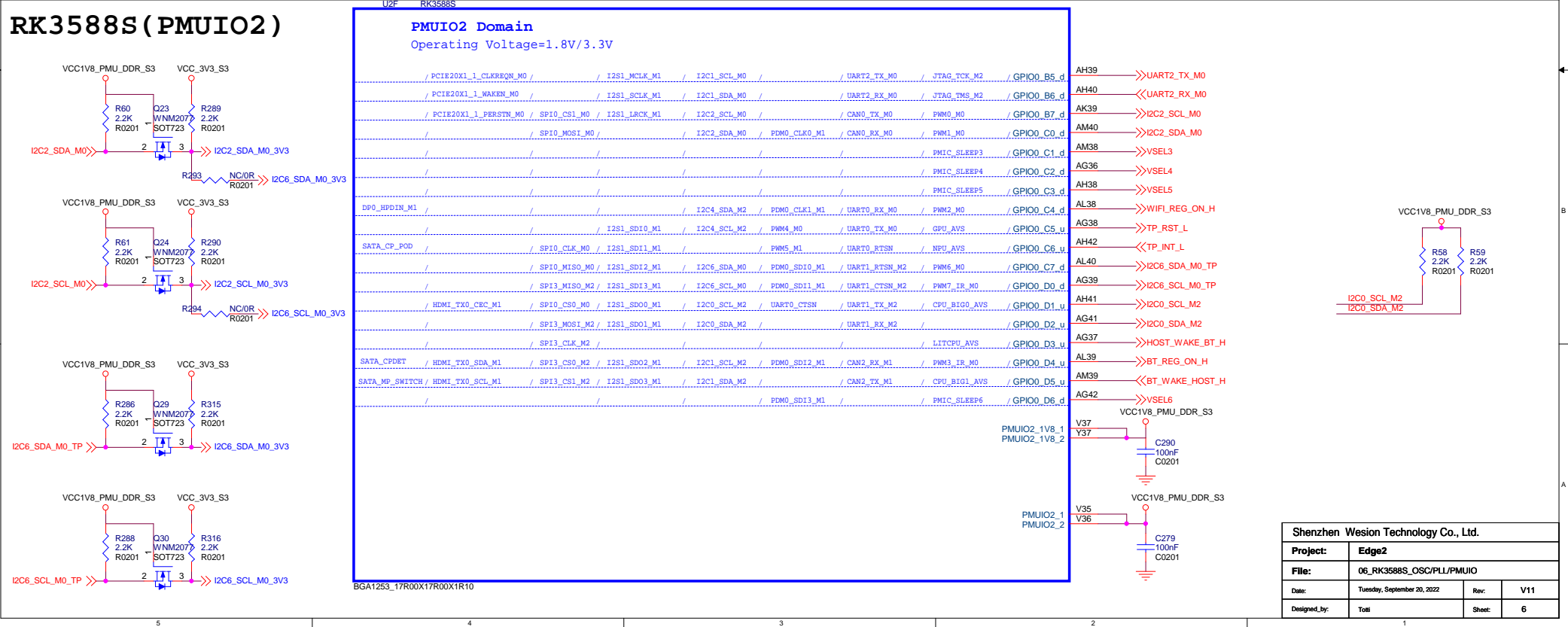


U21 RK3588S	G9 U21 RK3588S	U2V RK3588S	U41 U2V RK3588S	AA10 U2V RK3588S	AD11 U2V RK3588S	AF3 U2V RK3588S	AJ12 U2V RK3588S	UZ2 RK3588S	UZ2 RK3588S
A1 VSS_1	VSS_101	VSS_201	VSS_201	VSS_301	VSS_401	VSS_401	VSS_401	VSS_501	VSS_501
A2 VSS_2	VSS_102	VSS_202	VSS_202	VSS_302	VSS_402	VSS_402	VSS_402	VSS_502	VSS_502
A3 VSS_3	VSS_103	VSS_203	VSS_203	VSS_303	VSS_403	VSS_403	VSS_403	VSS_503	VSS_503
A4 VSS_4	VSS_104	VSS_204	VSS_204	VSS_304	VSS_404	VSS_404	VSS_404	VSS_504	VSS_504
A5 VSS_5	VSS_105	VSS_205	VSS_205	VSS_305	VSS_405	VSS_405	VSS_405	VSS_505	VSS_505
A6 VSS_6	VSS_106	VSS_206	VSS_206	VSS_306	VSS_406	VSS_406	VSS_406	VSS_506	VSS_506
A7 VSS_7	VSS_107	VSS_207	VSS_207	VSS_307	VSS_407	VSS_407	VSS_407	VSS_507	VSS_507
A8 VSS_8	VSS_108	VSS_208	VSS_208	VSS_308	VSS_408	VSS_408	VSS_408	VSS_508	VSS_508
A9 VSS_9	VSS_109	VSS_209	VSS_209	VSS_309	VSS_409	VSS_409	VSS_409	VSS_509	VSS_509
A10 VSS_10	VSS_110	VSS_210	VSS_210	VSS_310	VSS_410	VSS_410	VSS_410	VSS_510	VSS_510
A11 VSS_11	VSS_111	VSS_211	VSS_211	VSS_311	VSS_411	VSS_411	VSS_411	VSS_511	VSS_511
A12 VSS_12	VSS_112	VSS_212	VSS_212	VSS_312	VSS_412	VSS_412	VSS_412	VSS_512	VSS_512
A13 VSS_13	VSS_113	VSS_213	VSS_213	VSS_313	VSS_413	VSS_413	VSS_413	VSS_513	VSS_513
A14 VSS_14	VSS_114	VSS_214	VSS_214	VSS_314	VSS_414	VSS_414	VSS_414	VSS_514	VSS_514
A15 VSS_15	VSS_115	VSS_215	VSS_215	VSS_315	VSS_415	VSS_415	VSS_415	VSS_515	VSS_515
A16 VSS_16	VSS_116	VSS_216	VSS_216	VSS_316	VSS_416	VSS_416	VSS_416	VSS_516	VSS_516
A17 VSS_17	VSS_117	VSS_217	VSS_217	VSS_317	VSS_417	VSS_417	VSS_417	VSS_517	VSS_517
A18 VSS_18	VSS_118	VSS_218	VSS_218	VSS_318	VSS_418	VSS_418	VSS_418	VSS_518	VSS_518
A19 VSS_19	VSS_119	VSS_219	VSS_219	VSS_319	VSS_419	VSS_419	VSS_419	VSS_519	VSS_519
A20 VSS_20	VSS_120	VSS_220	VSS_220	VSS_320	VSS_420	VSS_420	VSS_420	VSS_520	VSS_520
A21 VSS_21	VSS_121	VSS_221	VSS_221	VSS_321	VSS_421	VSS_421	VSS_421	VSS_521	VSS_521
A22 VSS_22	VSS_122	VSS_222	VSS_222	VSS_322	VSS_422	VSS_422	VSS_422	VSS_522	VSS_522
A23 VSS_23	VSS_123	VSS_223	VSS_223	VSS_323	VSS_423	VSS_423	VSS_423	VSS_523	VSS_523
A24 VSS_24	VSS_124	VSS_224	VSS_224	VSS_324	VSS_424	VSS_424	VSS_424	VSS_524	VSS_524
A25 VSS_25	VSS_125	VSS_225	VSS_225	VSS_325	VSS_425	VSS_425	VSS_425	VSS_525	VSS_525
A26 VSS_26	VSS_126	VSS_226	VSS_226	VSS_326	VSS_426	VSS_426	VSS_426	VSS_526	VSS_526
A27 VSS_27	VSS_127	VSS_227	VSS_227	VSS_327	VSS_427	VSS_427	VSS_427	VSS_527	VSS_527
A28 VSS_28	VSS_128	VSS_228	VSS_228	VSS_328	VSS_428	VSS_428	VSS_428	VSS_528	VSS_528
A29 VSS_29	VSS_129	VSS_229	VSS_229	VSS_329	VSS_429	VSS_429	VSS_429	VSS_529	VSS_529
A30 VSS_30	VSS_130	VSS_230	VSS_230	VSS_330	VSS_430	VSS_430	VSS_430	VSS_530	VSS_530
A31 VSS_31	VSS_131	VSS_231	VSS_231	VSS_331	VSS_431	VSS_431	VSS_431	VSS_531	VSS_531
A32 VSS_32	VSS_132	VSS_232	VSS_232	VSS_332	VSS_432	VSS_432	VSS_432	VSS_532	VSS_532
A33 VSS_33	VSS_133	VSS_233	VSS_233	VSS_333	VSS_433	VSS_433	VSS_433	VSS_533	VSS_533
A34 VSS_34	VSS_134	VSS_234	VSS_234	VSS_334	VSS_434	VSS_434	VSS_434	VSS_534	VSS_534
A35 VSS_35	VSS_135	VSS_235	VSS_235	VSS_335	VSS_435	VSS_435	VSS_435	VSS_535	VSS_535
A36 VSS_36	VSS_136	VSS_236	VSS_236	VSS_336	VSS_436	VSS_436	VSS_436	VSS_536	VSS_536
A37 VSS_37	VSS_137	VSS_237	VSS_237	VSS_337	VSS_437	VSS_437	VSS_437	VSS_537	VSS_537
A38 VSS_38	VSS_138	VSS_238	VSS_238	VSS_338	VSS_438	VSS_438	VSS_438	VSS_538	VSS_538
A39 VSS_39	VSS_139	VSS_239	VSS_239	VSS_339	VSS_439	VSS_439	VSS_439	VSS_539	VSS_539
A40 VSS_40	VSS_140	VSS_240	VSS_240	VSS_340	VSS_440	VSS_440	VSS_440	VSS_540	VSS_540
A41 VSS_41	VSS_141	VSS_241	VSS_241	VSS_341	VSS_441	VSS_441	VSS_441	VSS_541	VSS_541
A42 VSS_42	VSS_142	VSS_242	VSS_242	VSS_342	VSS_442	VSS_442	VSS_442	VSS_542	VSS_542
A43 VSS_43	VSS_143	VSS_243	VSS_243	VSS_343	VSS_443	VSS_443	VSS_443	VSS_543	VSS_543
A44 VSS_44	VSS_144	VSS_244	VSS_244	VSS_344	VSS_444	VSS_444	VSS_444	VSS_544	VSS_544
A45 VSS_45	VSS_145	VSS_245	VSS_245	VSS_345	VSS_445	VSS_445	VSS_445	VSS_545	VSS_545
A46 VSS_46	VSS_146	VSS_246	VSS_246	VSS_346	VSS_446	VSS_446	VSS_446	VSS_546	VSS_546
A47 VSS_47	VSS_147	VSS_247	VSS_247	VSS_347	VSS_447	VSS_447	VSS_447	VSS_547	VSS_547
A48 VSS_48	VSS_148	VSS_248	VSS_248	VSS_348	VSS_448	VSS_448	VSS_448	VSS_548	VSS_548
A49 VSS_49	VSS_149	VSS_249	VSS_249	VSS_349	VSS_449	VSS_449	VSS_449	VSS_549	VSS_549
A50 VSS_50	VSS_150	VSS_250	VSS_250	VSS_350	VSS_450	VSS_450	VSS_450	VSS_550	VSS_550

RK3588S (OSC/PLL/PMUIO1)

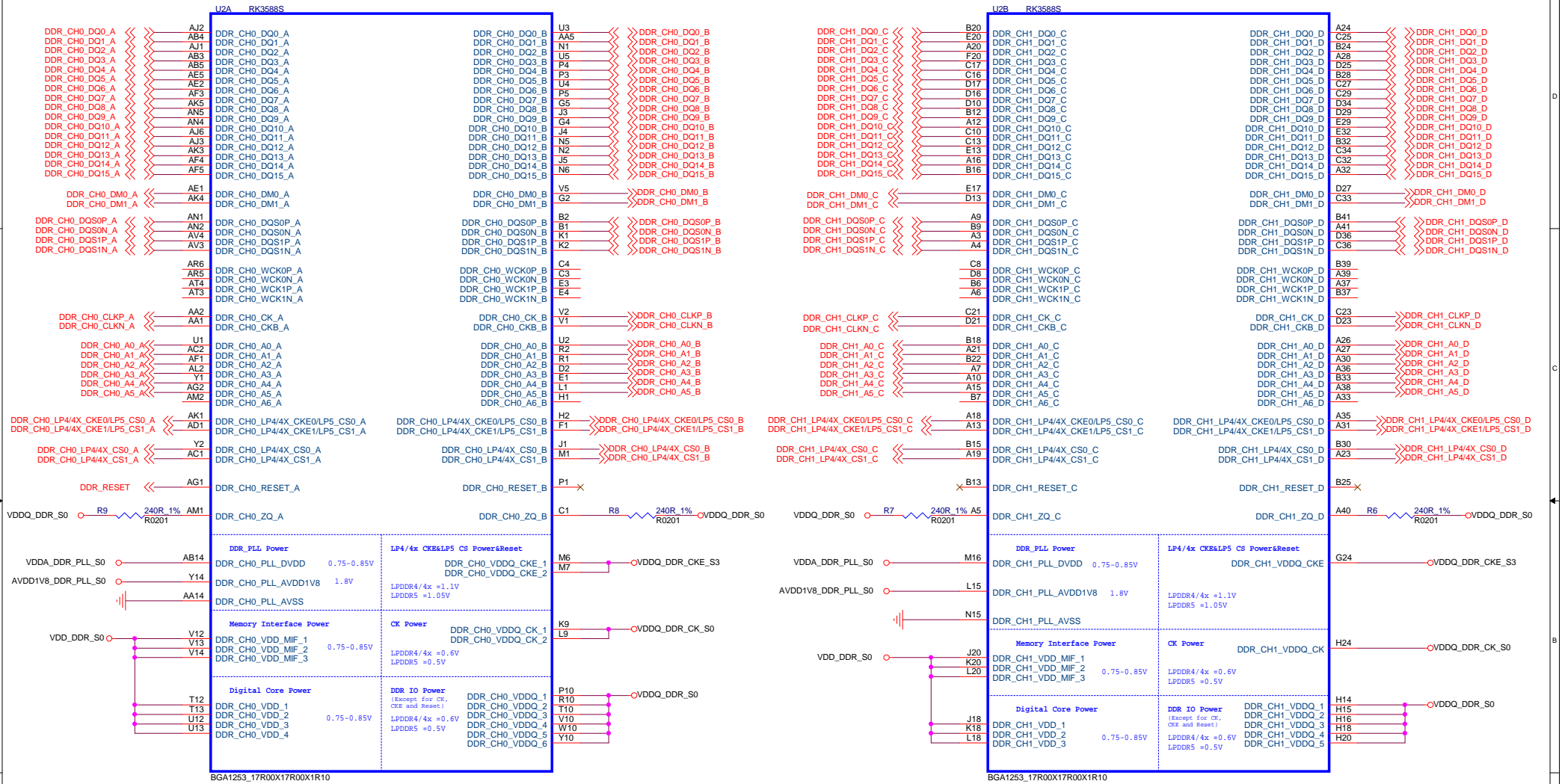


RK3588S (PMUIO2)

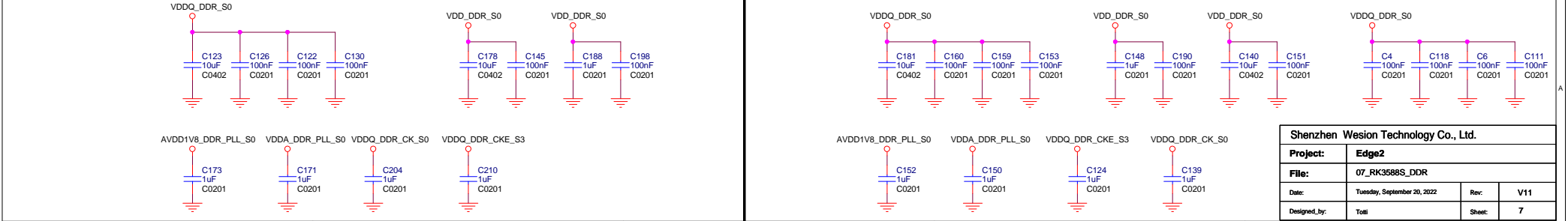


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	06_RK3588S_OSC/PLL/PMUIO		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Toil	Sheet:	6

RK3588S (DDR PHY)



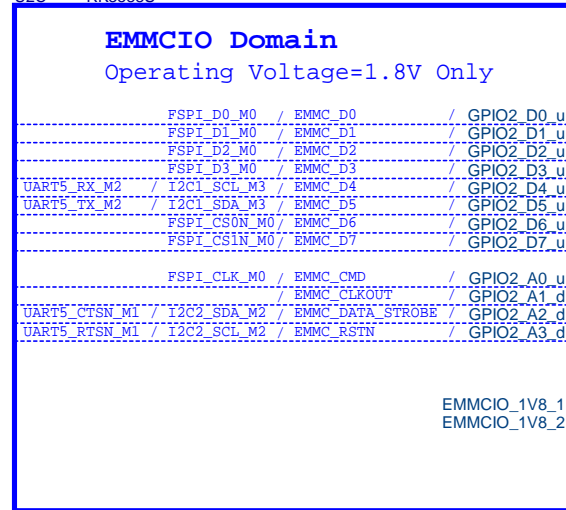
DDR FILTER



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	07_RK3588S_DDR		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed by:	Toni	Sheet:	7

RK3588S (EMMCIO Domain)

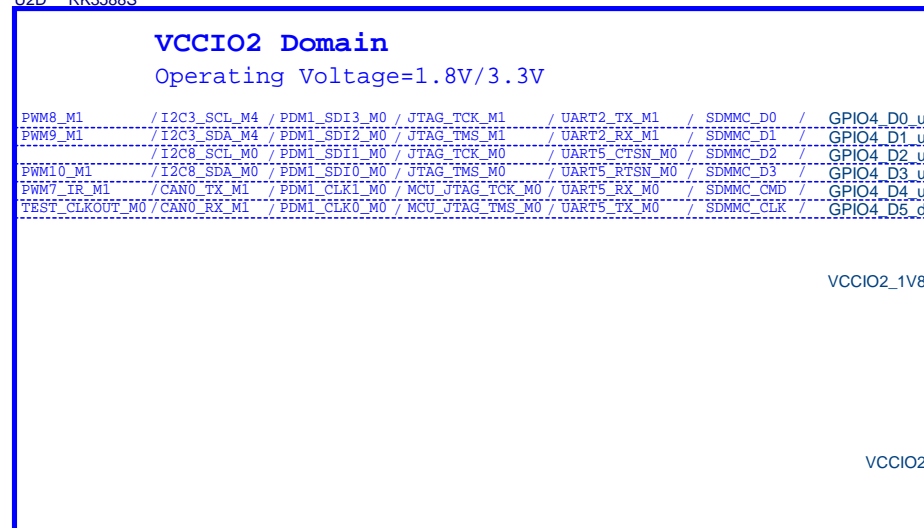
U2C RK3588S



BGA1253_17R00X17R00X1R10

RK3588S (VCCIO2 Domain)

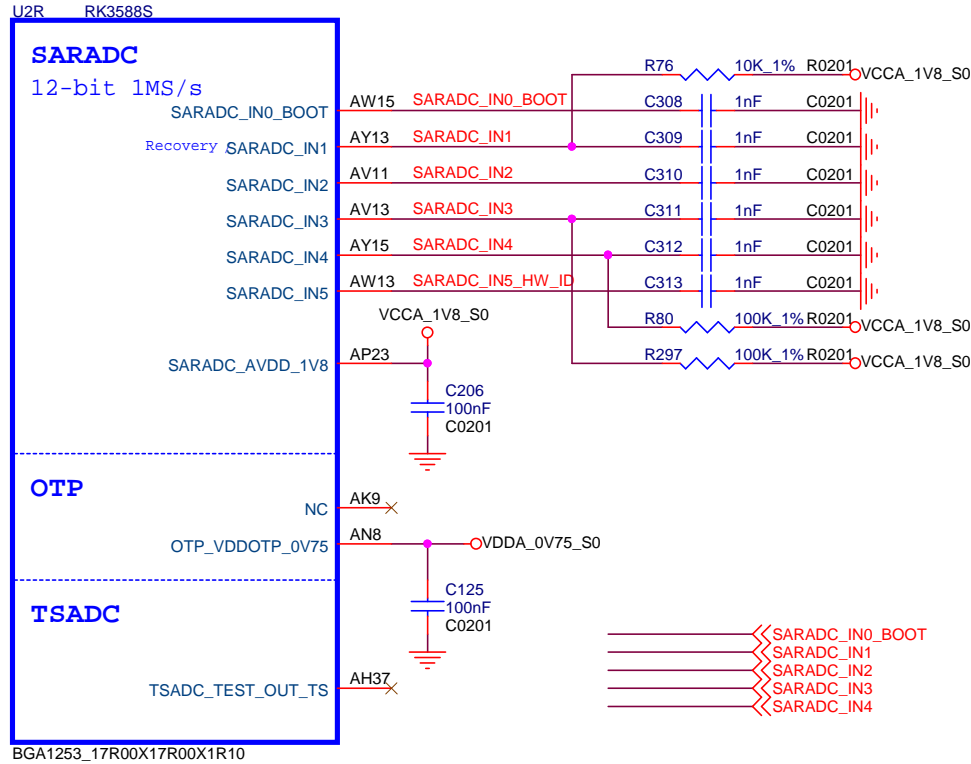
U2D RK3588S



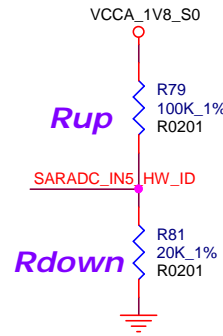
BGA1253_17R00X17R00X1R10

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	08_RK3588S_Flash/SD		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	8

RK3588S (SARADC/OTP/TSADC)

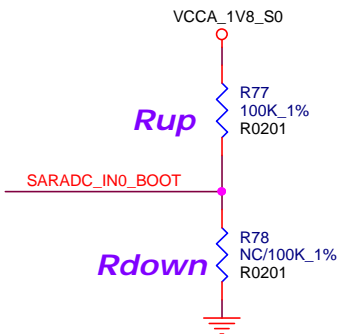


BOARD ID CONFIG



Item	Rup	Rdown	ADC	VERSION
LEVEL1	DNP	100K	0	V10
LEVEL2	100K	20K	682	V11
LEVEL3	100K	51K	1365	V12
LEVEL4	100K	100K	2047	V13
LEVEL5	100K	200K	2730	V14
LEVEL6	100K	499K	3412	V15
LEVEL7	100K	DNP	4095	V16

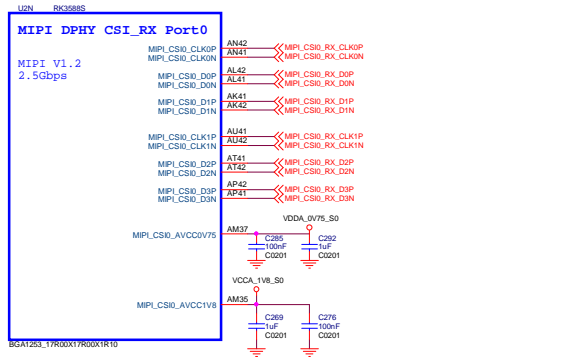
BOOT MODE CONFIG



Item	Rup	Rdown	ADC	BOOT MODE
LEVEL1	DNP	100K	0	USB (Maskrom mode)
LEVEL2	100K	20K	682	SD Card-USB
LEVEL3	100K	51K	1365	EMMC-USB
LEVEL4	100K	100K	2047	FSPI M0-USB
LEVEL5	100K	200K	2730	FSPI M1-USB
LEVEL6	100K	499K	3412	FSPI M2-USB
LEVEL7	100K	DNP	4095	FSPI_M2-FSPI_M0-EMMC -SD Card-USB

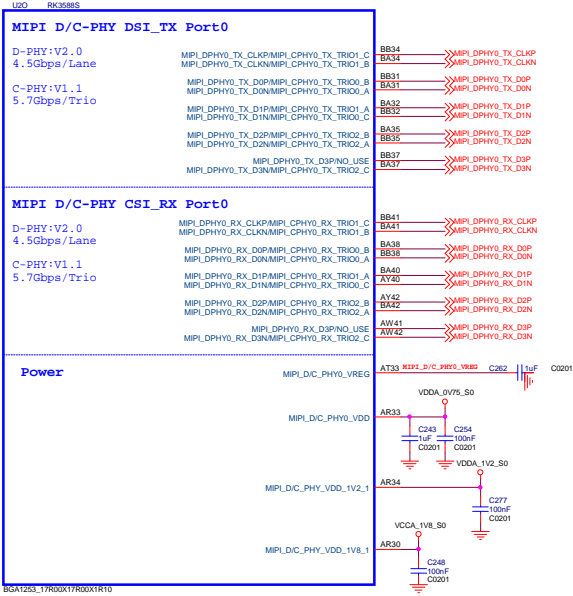
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	09_RK3588S_SARADC		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	9

RK3588S(MIPI_DPHY CSI0 RX)



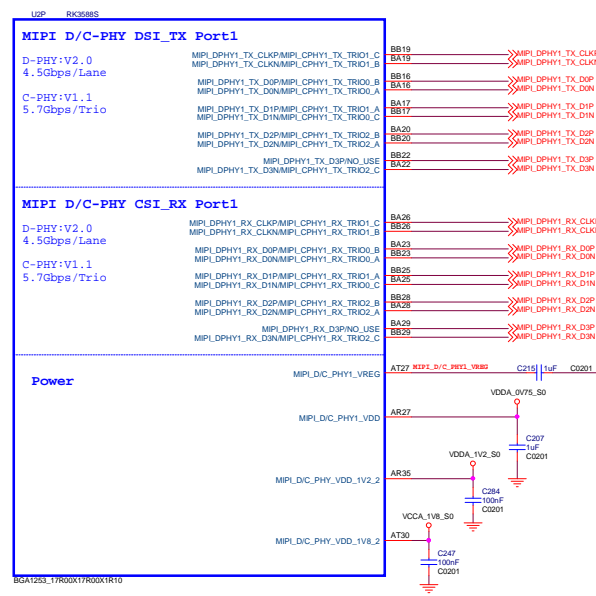
Option1	Sensor1 x4Lane	MIPI_CSI_RX_D0-3
		MIPI_CSI_RX_CLK0
Option2	Sensor1 x2Lane	MIPI_CSI_RX_D0-1
	+ Sensor2 x2Lane	MIPI_CSI_RX_CLK0
		MIPI_CSI_RX_D2-3
		MIPI_CSI_RX_CLK1

RK3588S(MIPI_D/C PHY0)



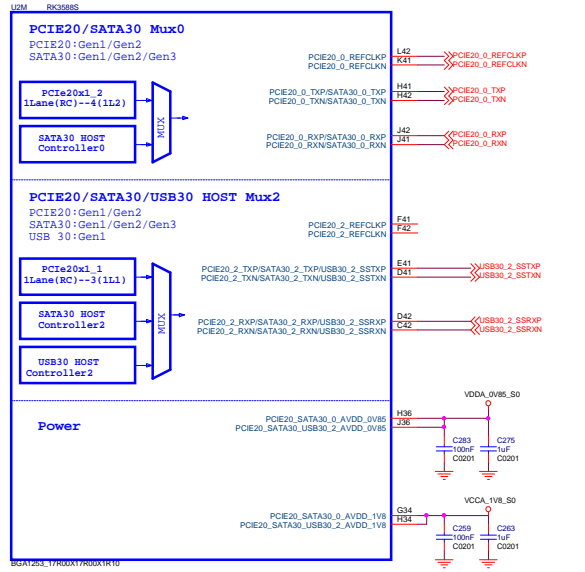
BGA1253_17R00X17R00X1R10

RK3588S(MIPI_D/C PHY1)



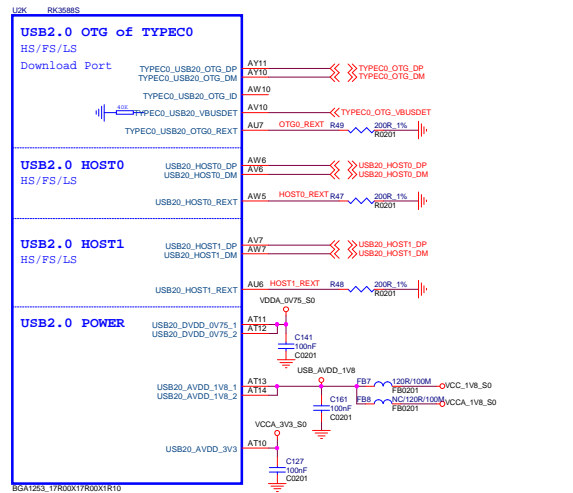
BGA1253_17R00X17R00X1R10

RK3588S(PCIe20/SATA30/USB30)



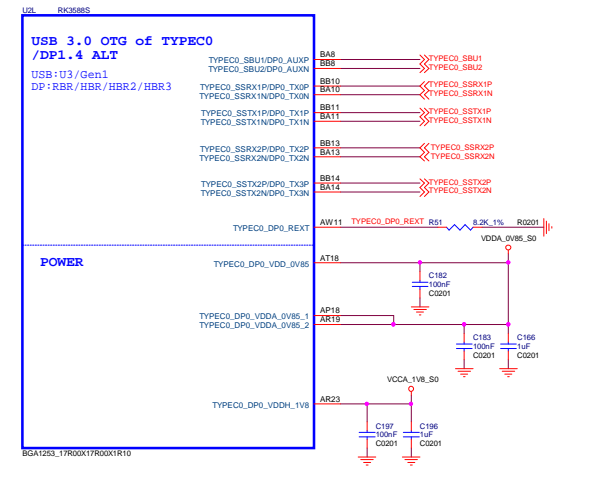
BGA1253_17R00X17R00X1R10

RK3588S(USB2.0)



BGA1253_17R00X17R00X1R10

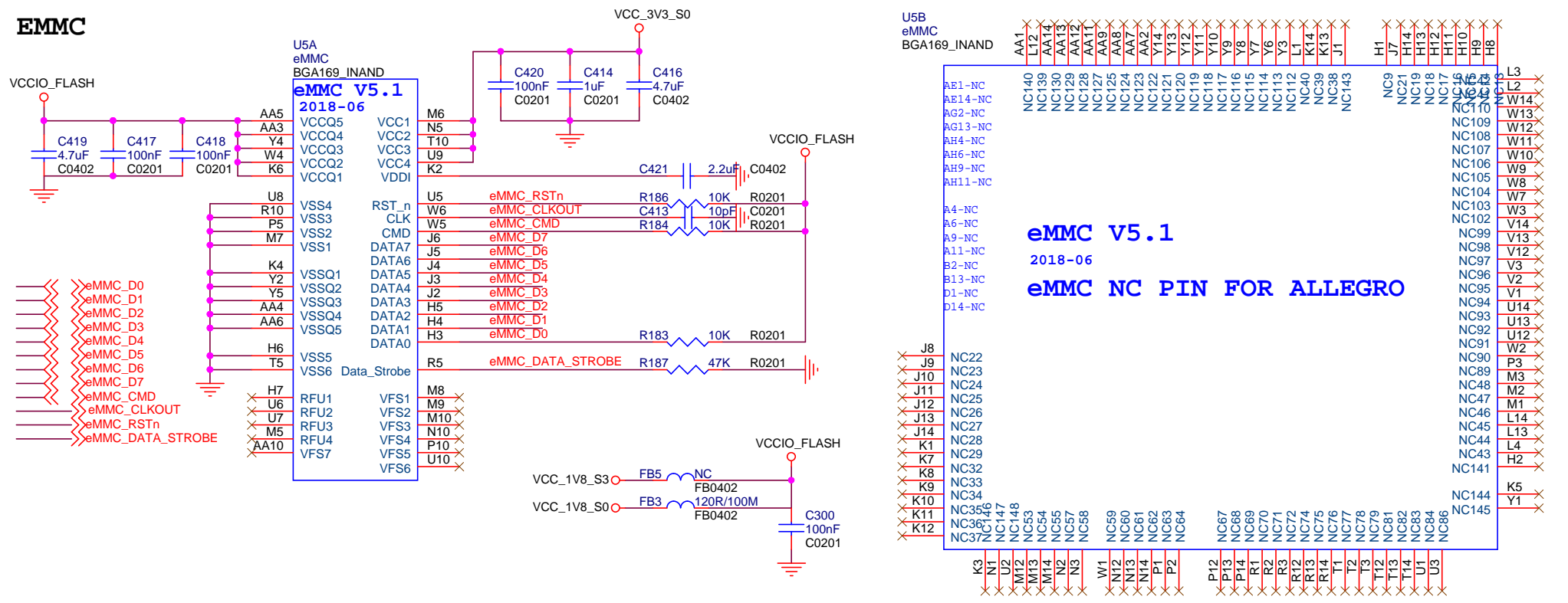
RK3588S(USB3.0/DP1.4)



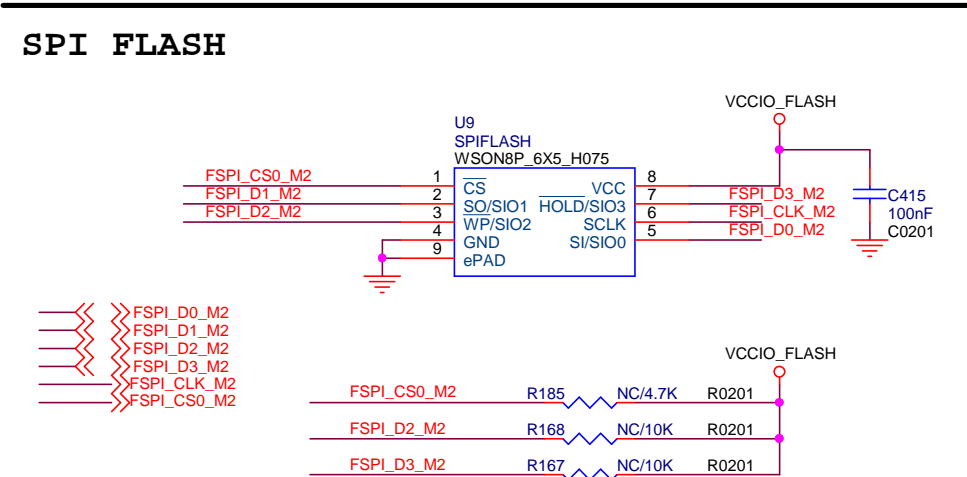
BGA1253_17R00X17R00X1R10

Shenzhen Wision Technology Co., Ltd.			
Project:	Edge2	Rev:	V11
File:	10_RK3588S_MPUSUB/PCIE	Draw:	10
Date:	Tuesday, September 20, 2022		
Designed by:	Yan		

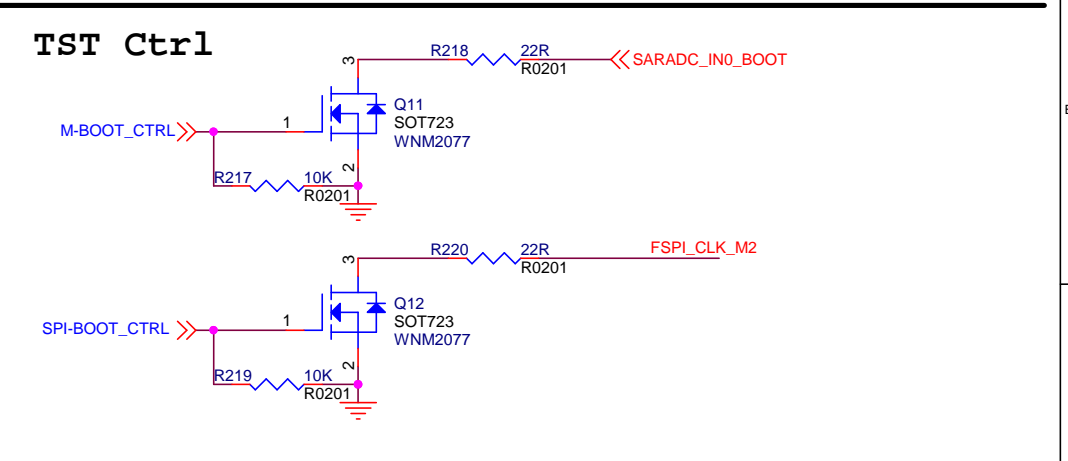
EMMC



SPI FLASH



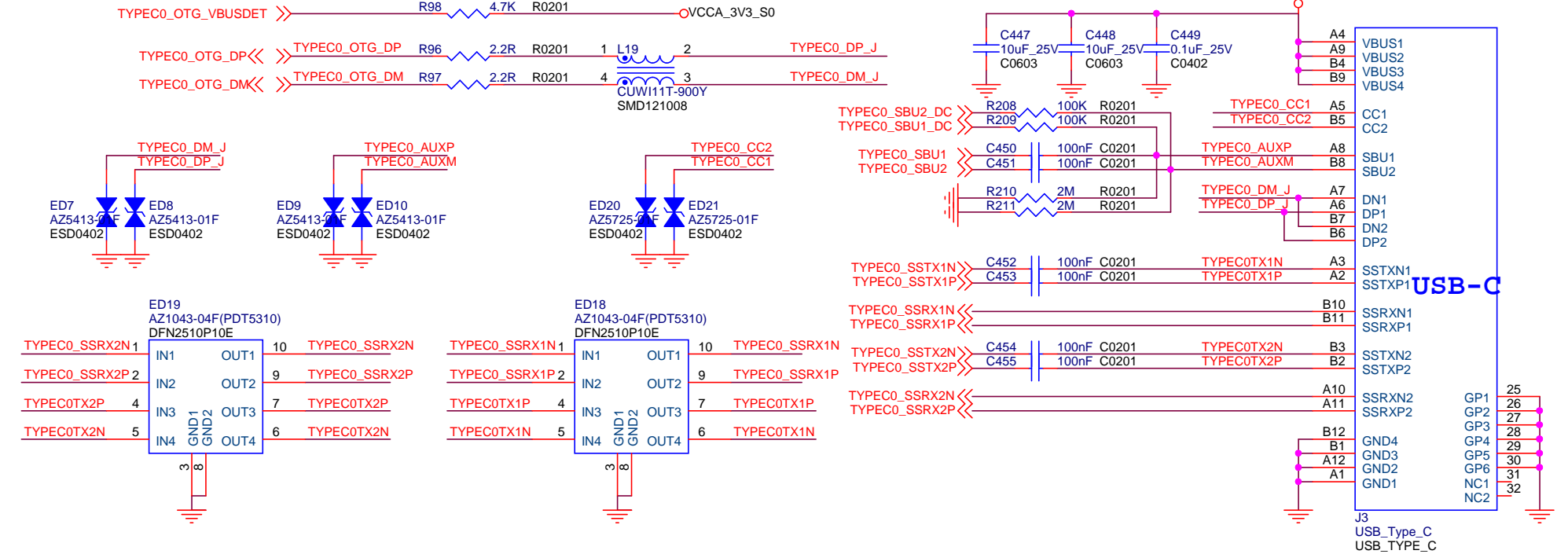
TST Ctrl



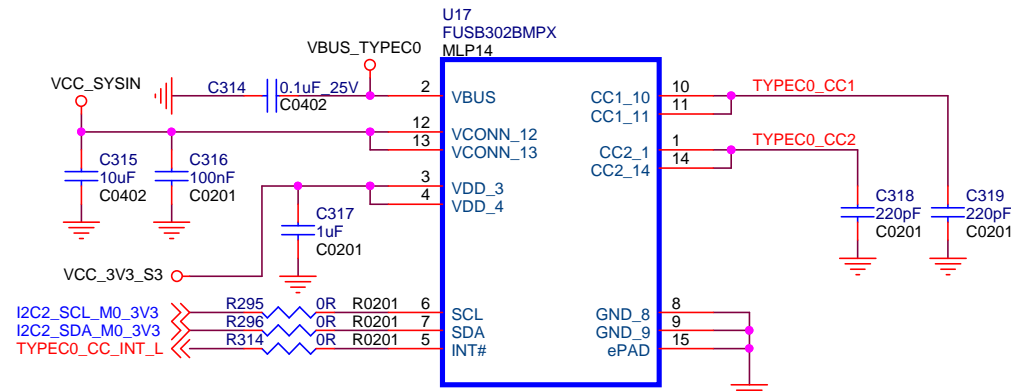
M-BOOT_CTRL	SPI-BOOT_CTRL	BOOT Priority	default
L	H	EMMC>USB	
H	X	USB	
L	L	SPI>EMMC>USB	

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	13_EMMC/SPI-FLASH		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totit	Sheet:	13

TYPE-C0 Port



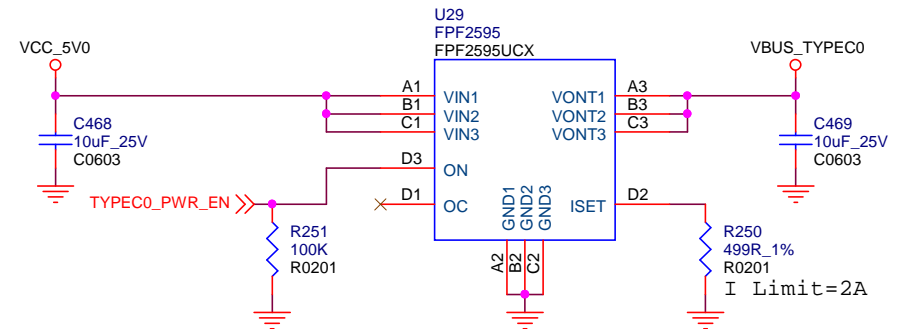
USB Type-C0 Controller(PD)



7bit address=0x22

VBUS of FUSB302BMPX: 4 to 21 TYP = 5V
 VDD of FUSB302BMPX: 3 to 5.5 TYP = 3.3V
 VCONN of FUSB302BMPX: 3 to 5.5

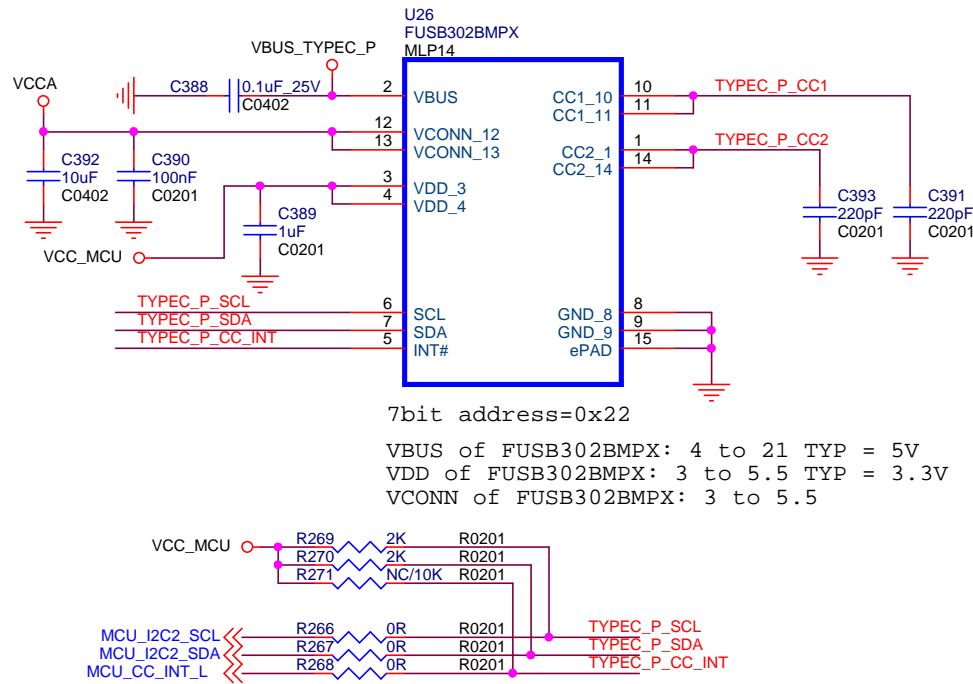
USB Type-C0 POWER



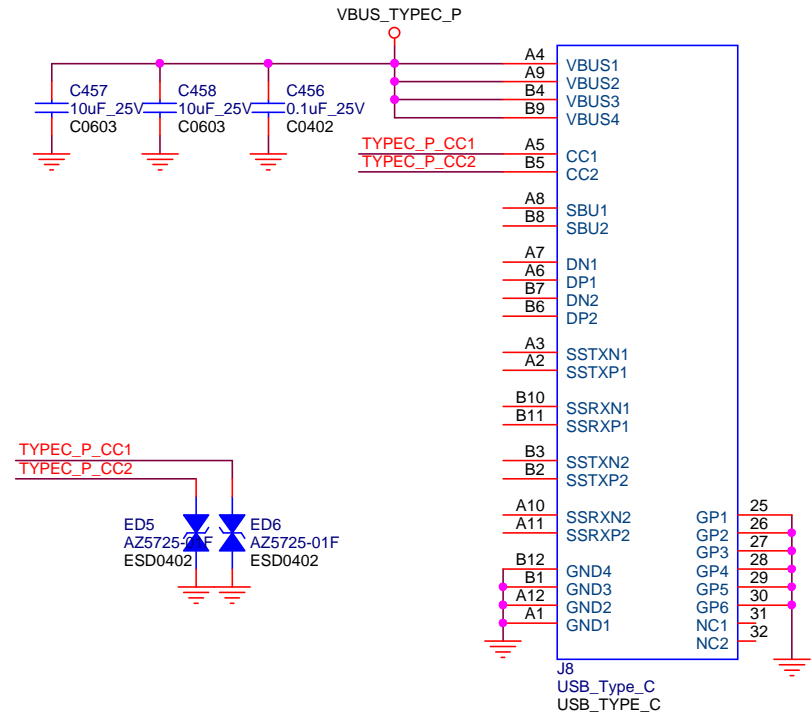
I Limit=2A

Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	14_Type-C0 Port		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totit	Sheet:	14

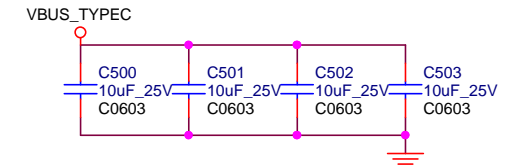
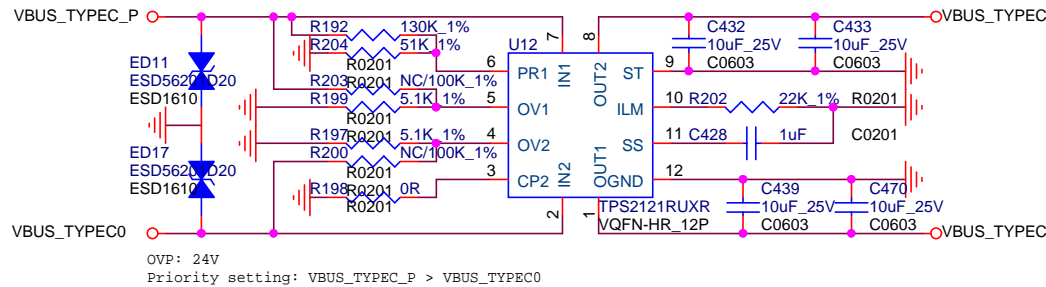
USB Type-C_P Controller(PD)



TYPE-C POWER-Only Port

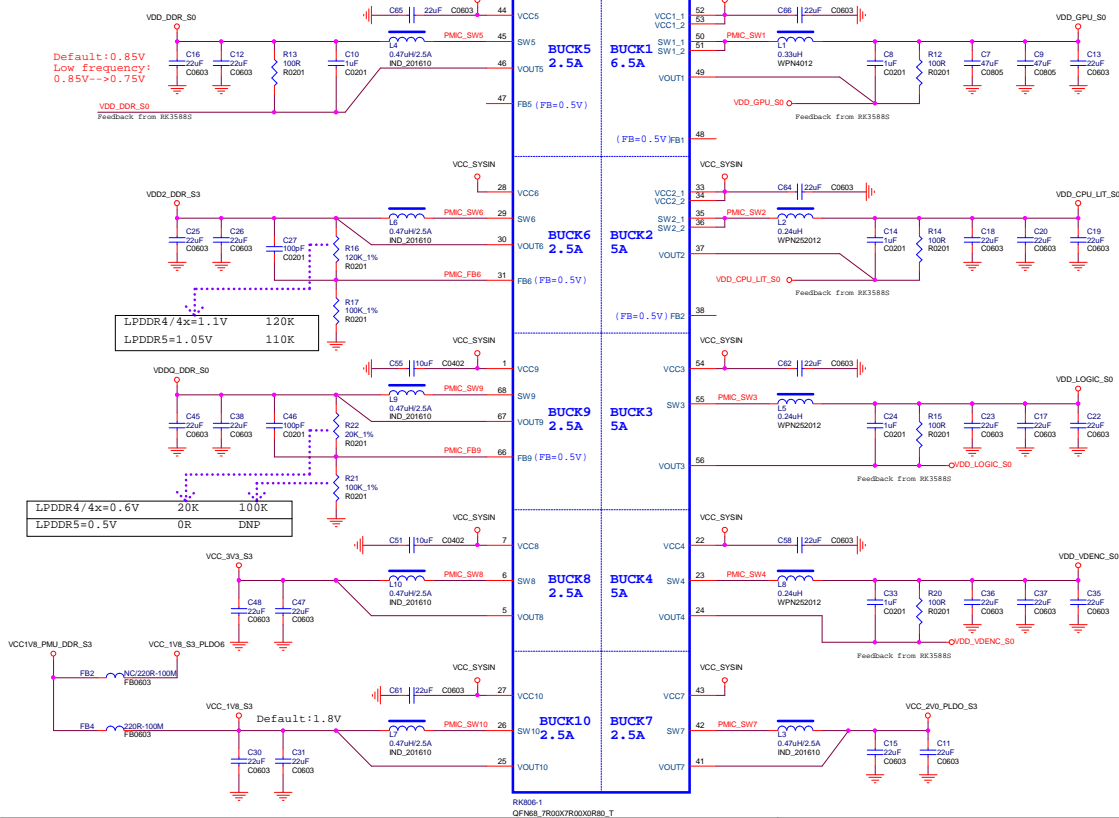


TYPE-C PWR PATH

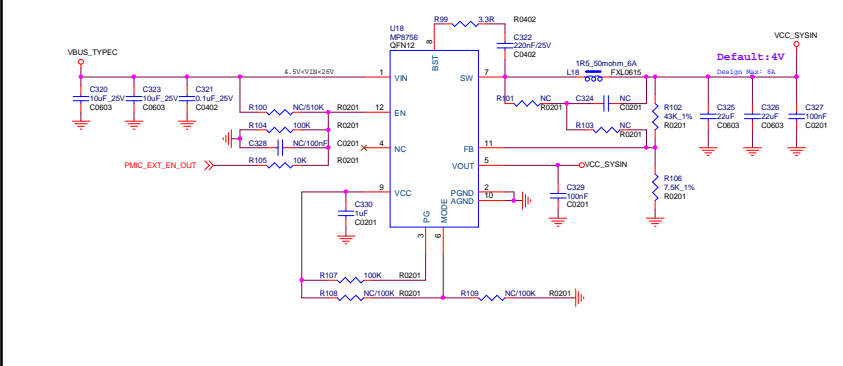


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	15_Type-C PWR Port		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	15

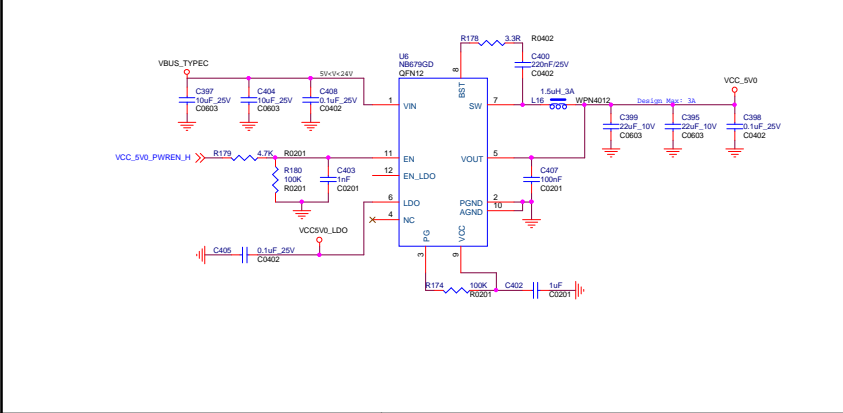
PMIC RK806-1 BUCK



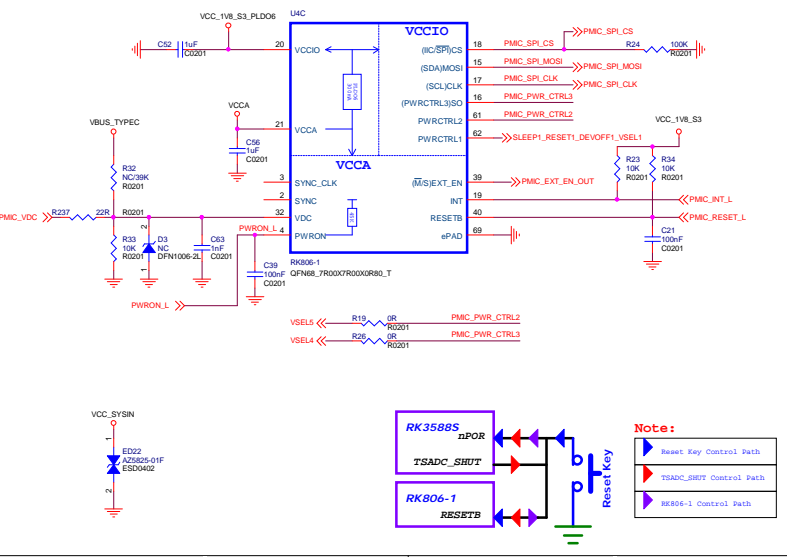
VCC_SYSIN



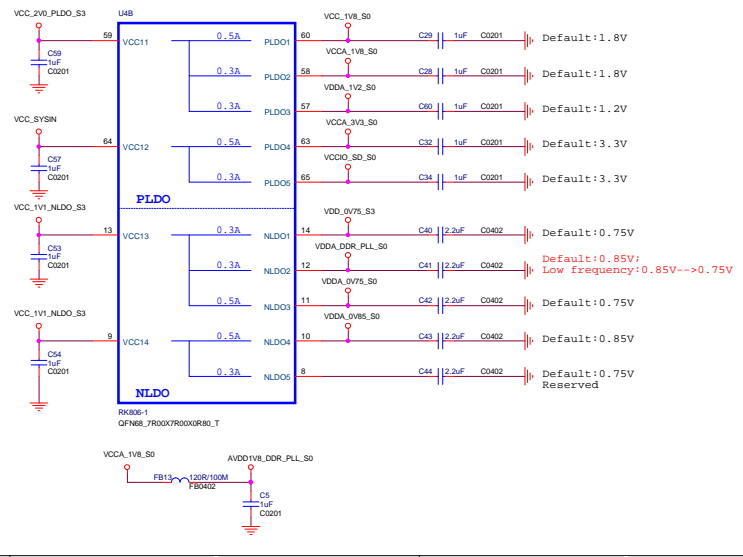
VCC5V



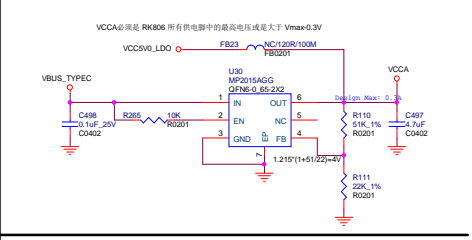
PMIC RK806-1 Management



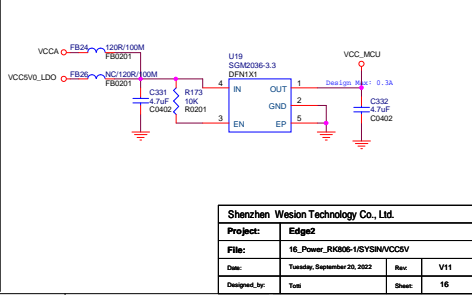
PMIC RK806-1 LDO



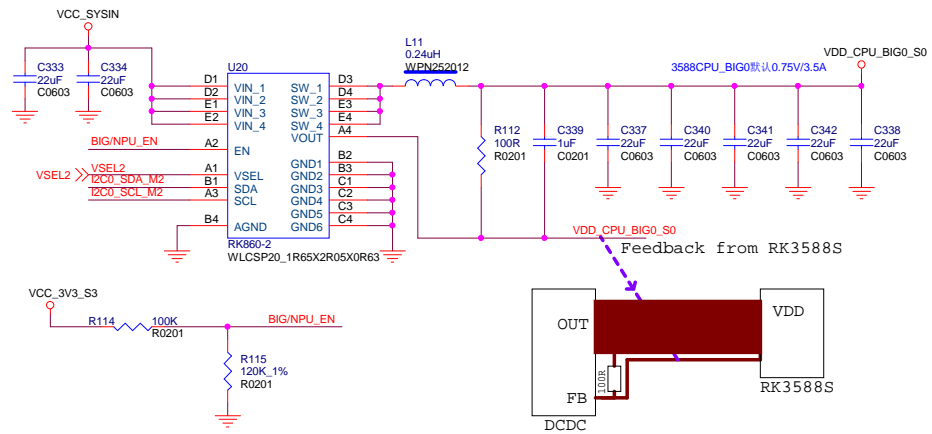
VCCA



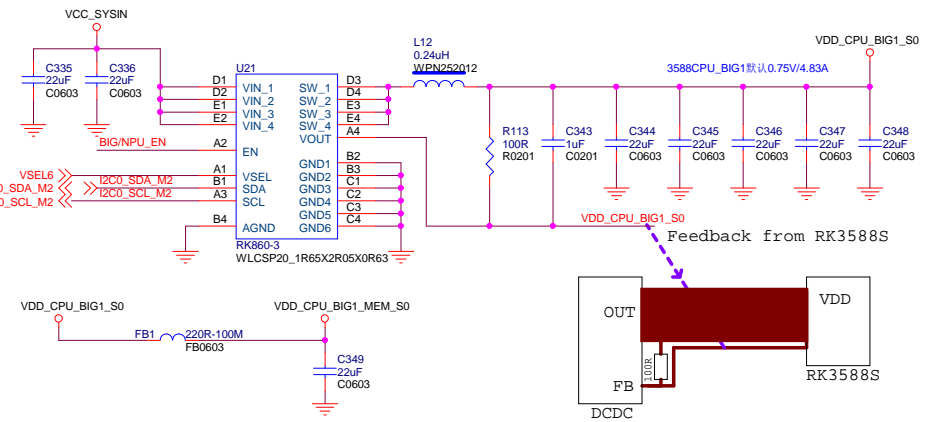
VCC_MCU



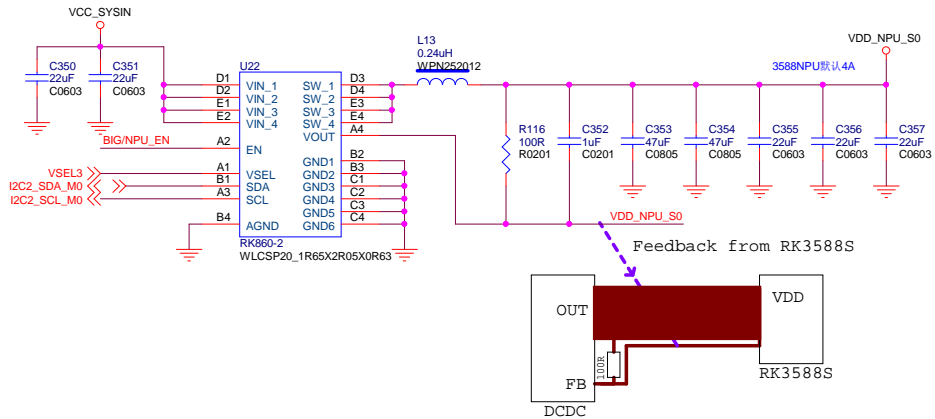
VDD_CPU_BIG0



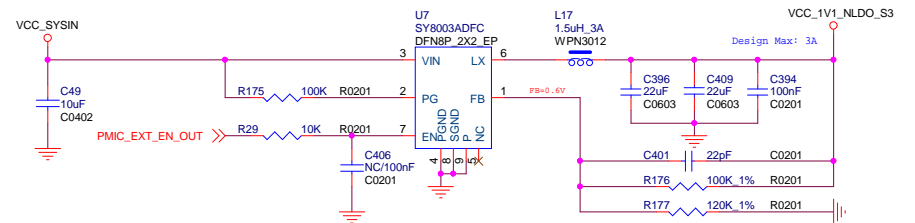
VDD_CPU_BIG1



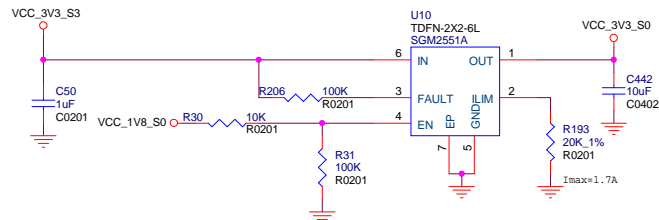
VDD_NPU



VCC_1V1_NLDO

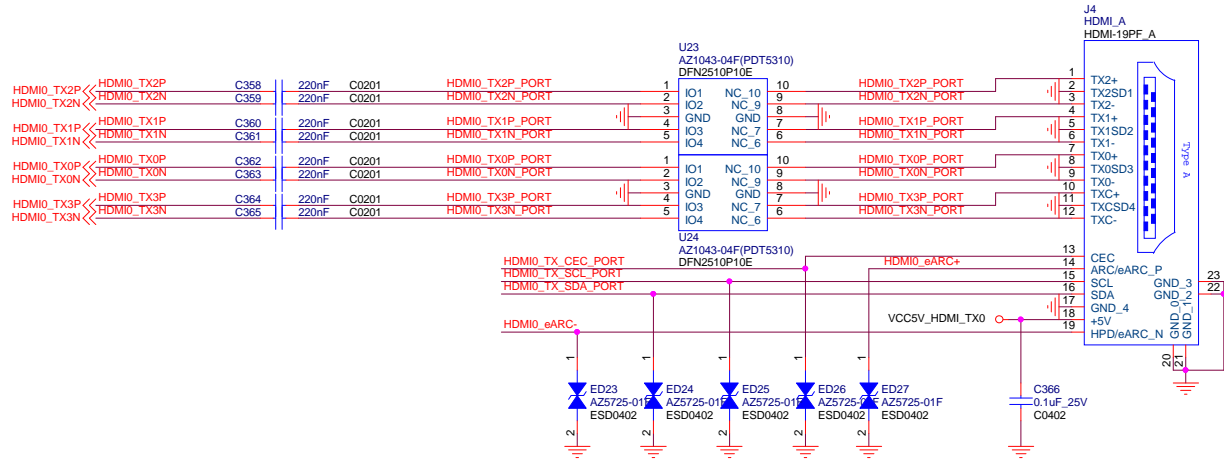


VCC_3V3_S0

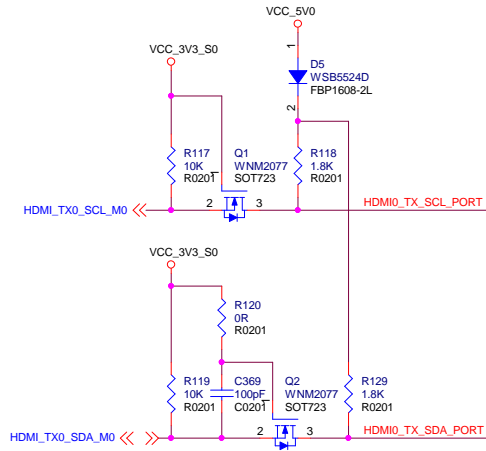


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	17_Power_Ext Discrete		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Toil	Sheet:	17

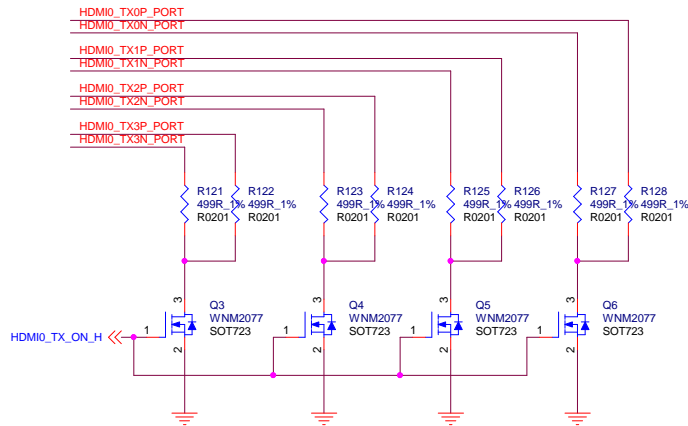
HDMI2.1 TX0



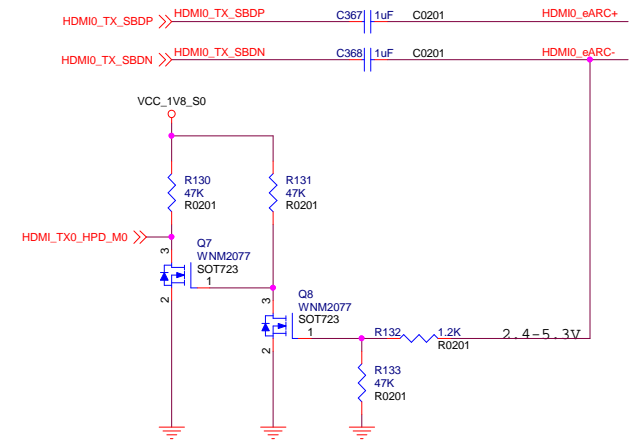
HDMI TX DDC



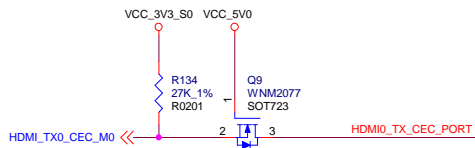
HDMI 2.0/2.1 SW



HDMI TX eARC



HDMI TX CEC

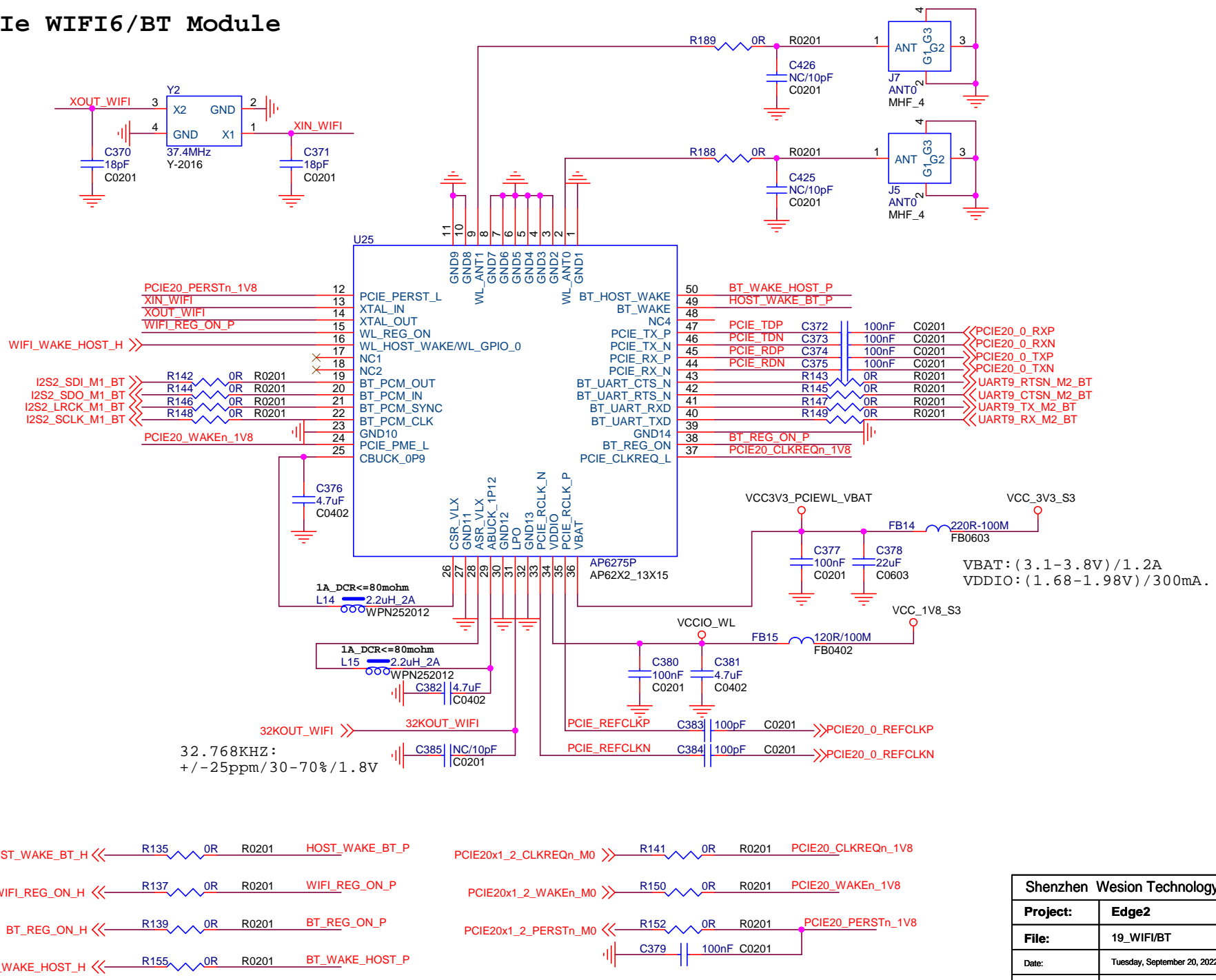


HDMI Power



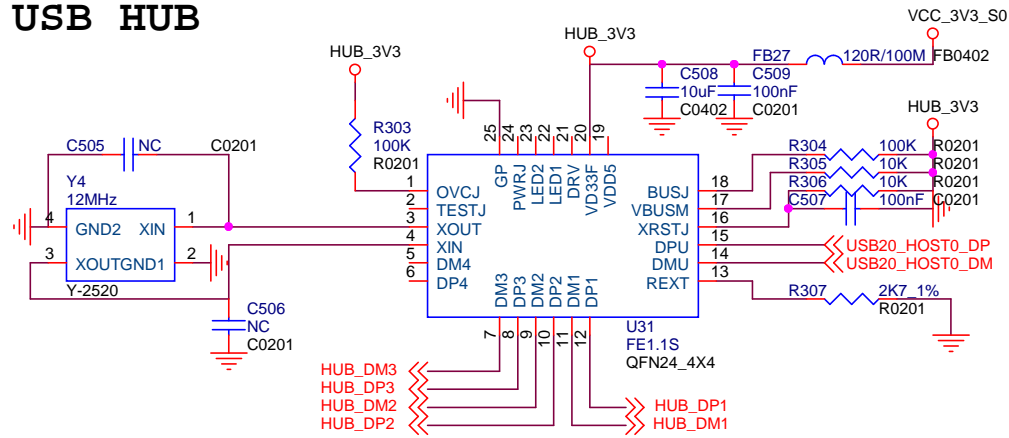
Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	18_HDMI OUT		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Toil	Sheet:	18

PCIe WIFI6/BT Module

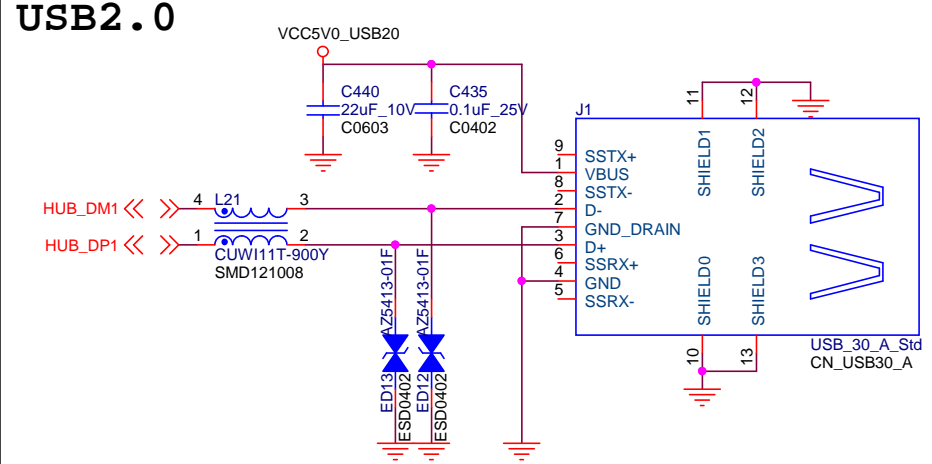


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	19_WIFI/BT		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	19

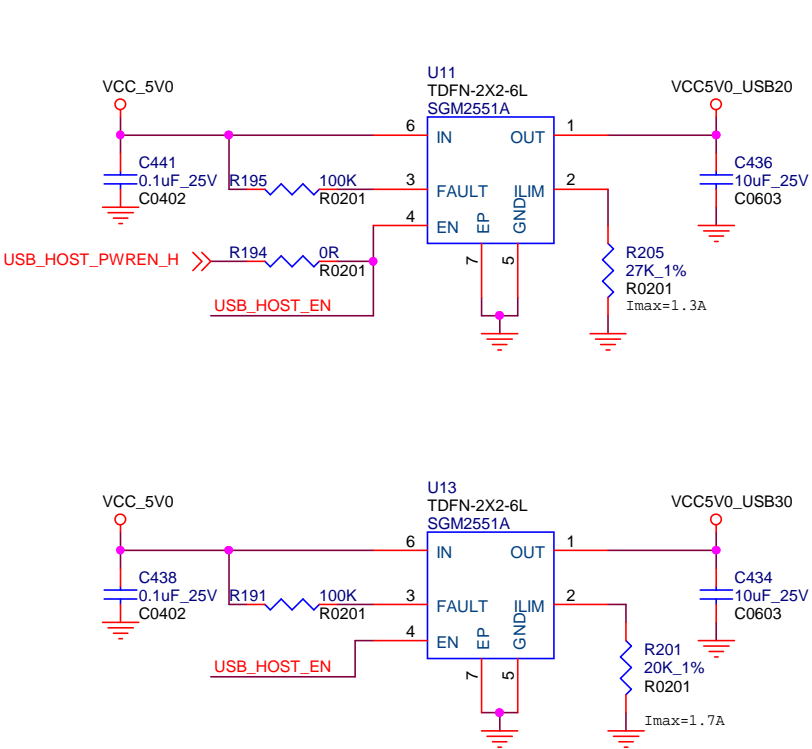
USB HUB



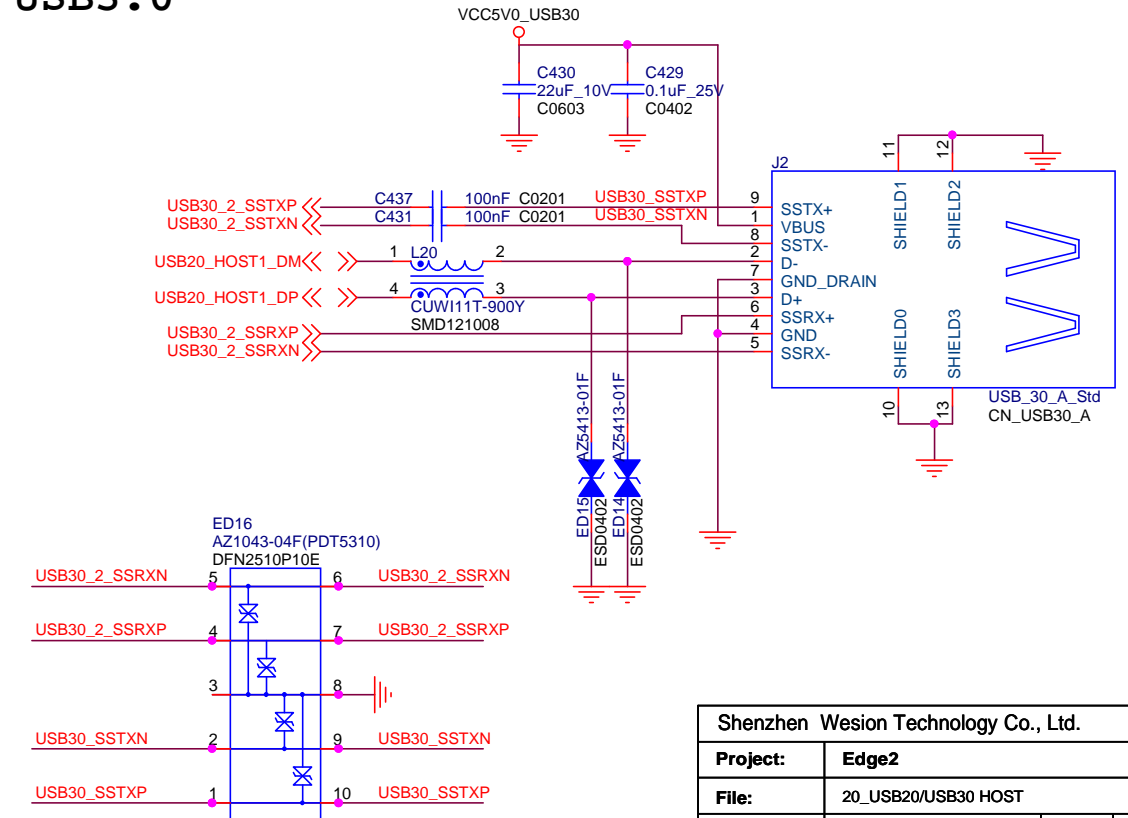
USB2.0



USB PWR SW

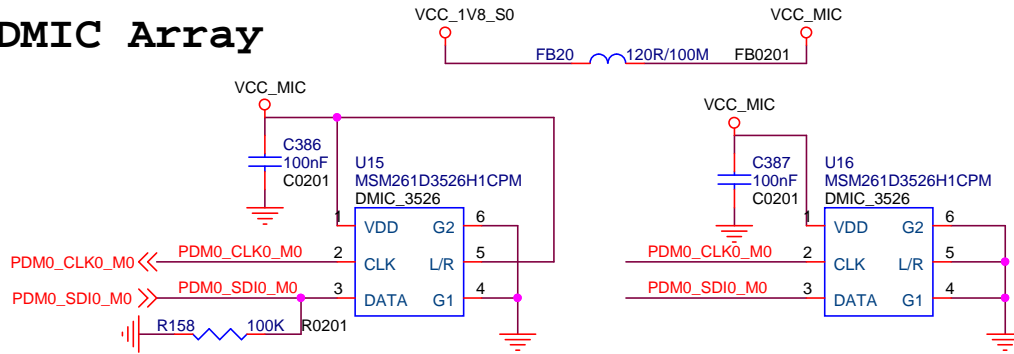


USB3.0

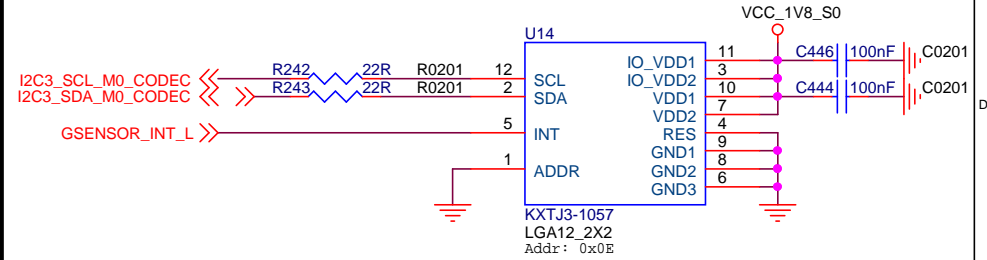


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	20_USB20/USB30 HOST		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totit	Sheet:	20

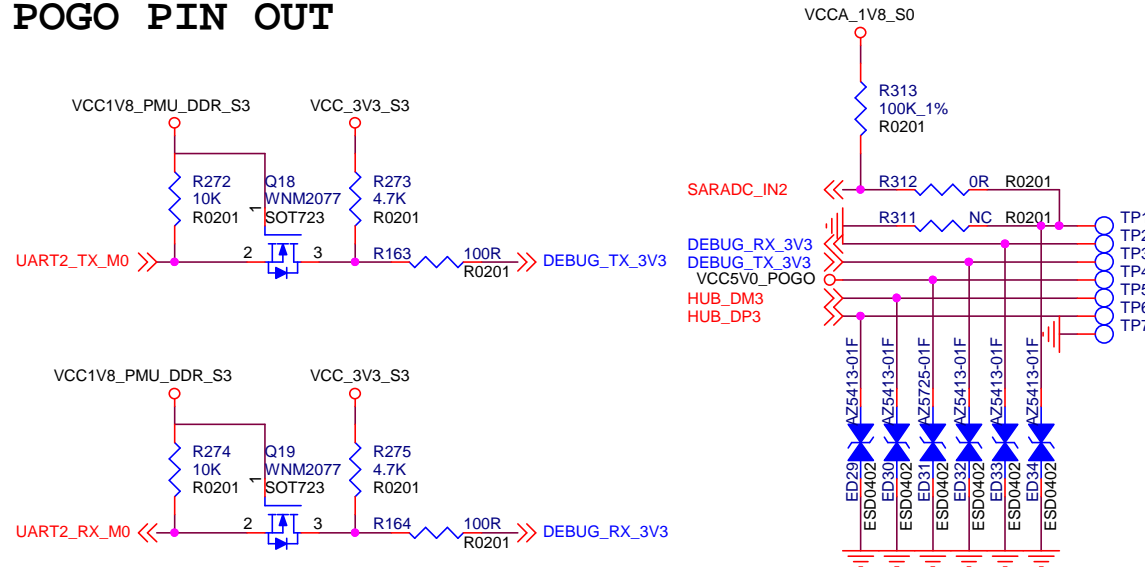
DMIC Array



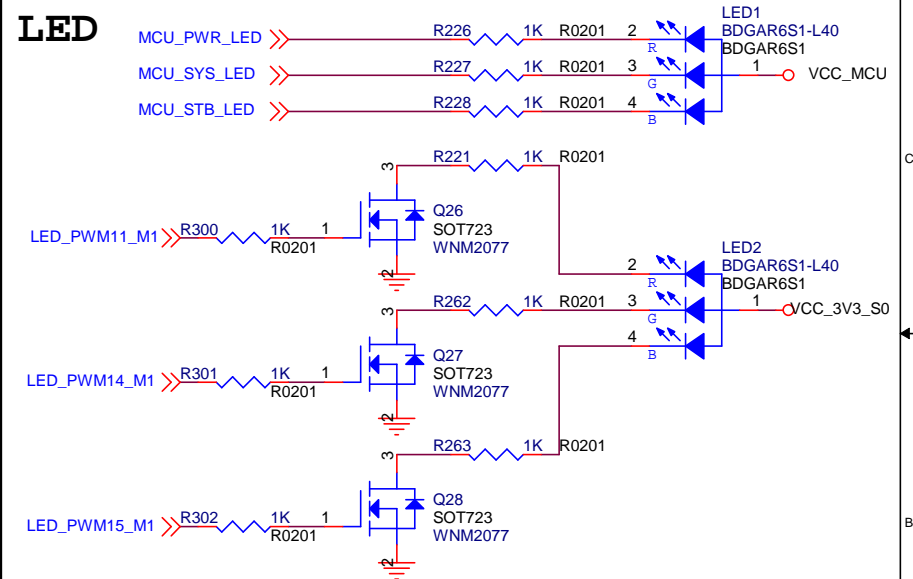
G-sensor



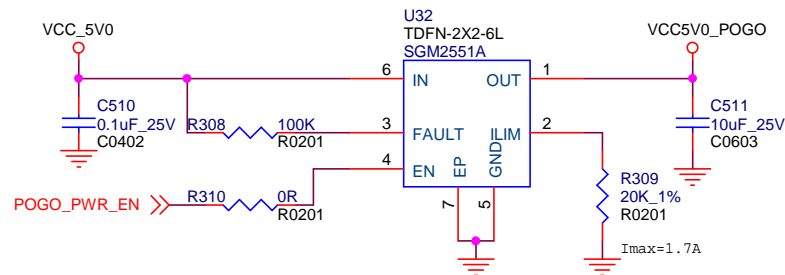
POGO PIN OUT



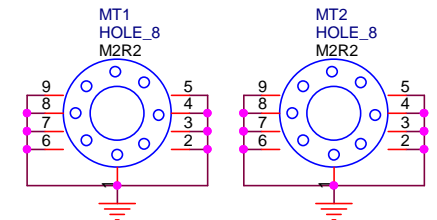
LED



POGO PIN PWR

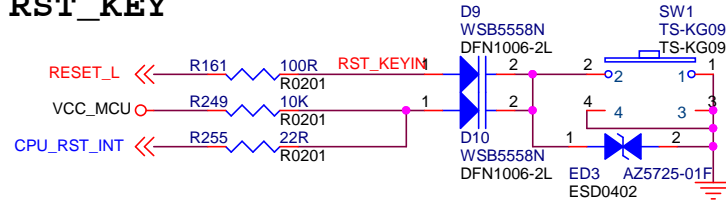


Mouting Holes

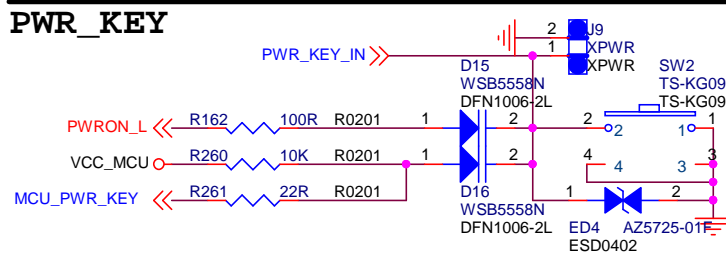


Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	21_DMIC/Sensor/DEBUG/LED		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed by:	Totti	Sheet:	21

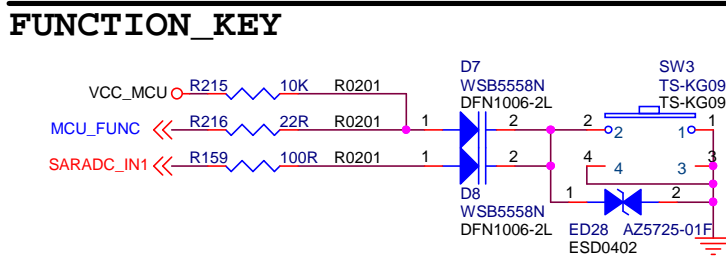
RST_KEY



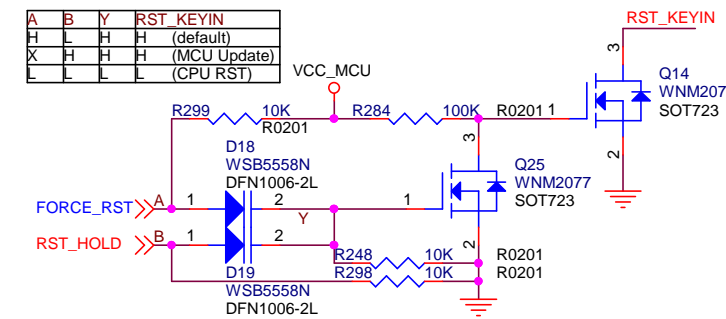
PWR_KEY



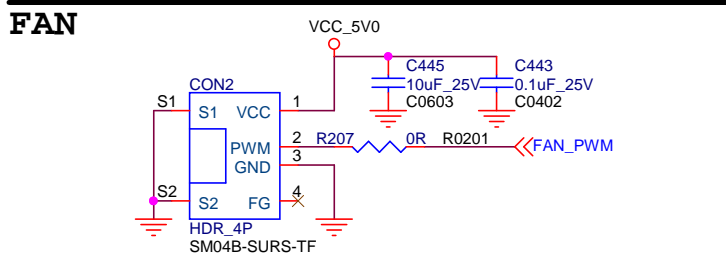
FUNCTION_KEY



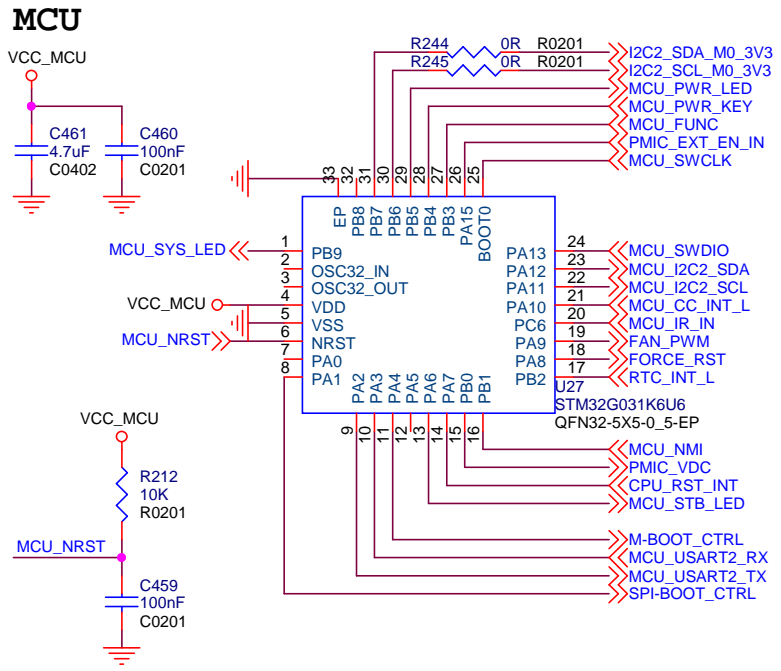
CPU_RST_CTRL



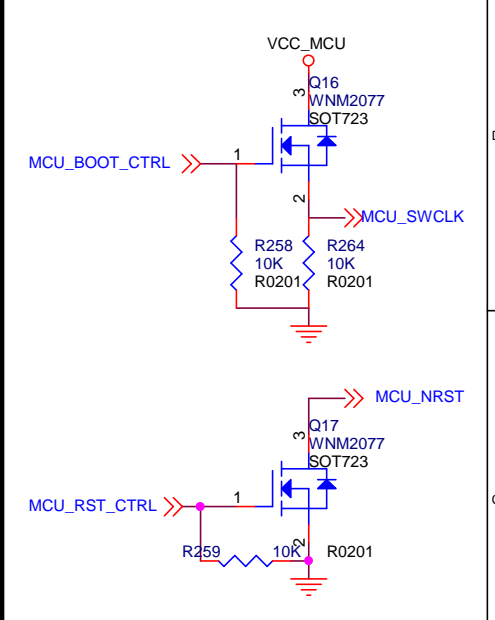
FAN



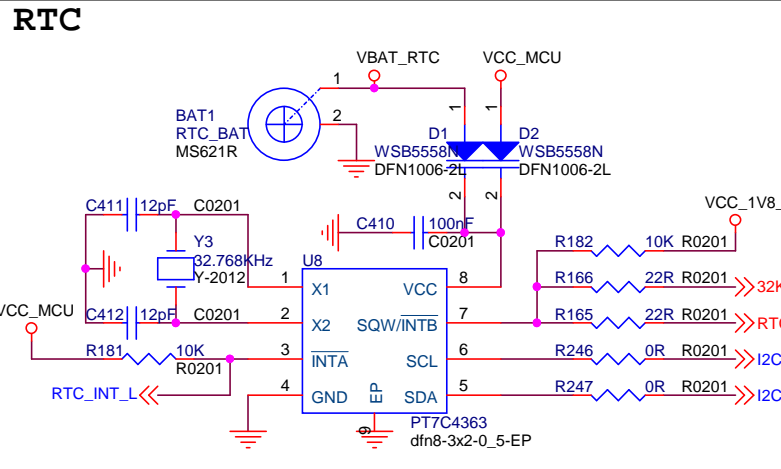
MCU



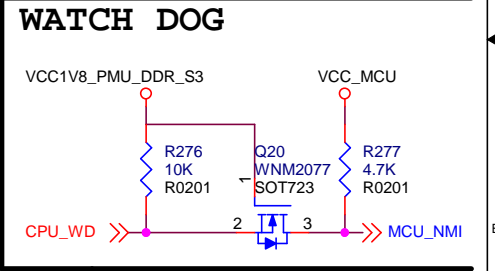
MCU BOOT CTRL



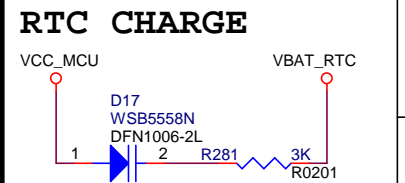
RTC



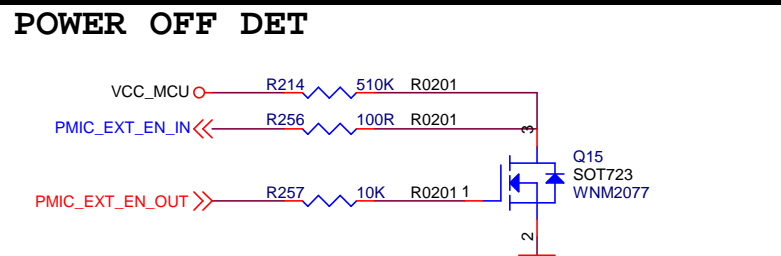
WATCH DOG



RTC CHARGE



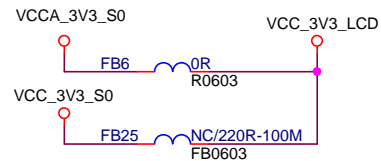
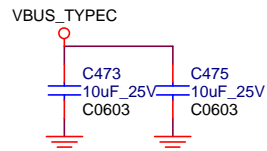
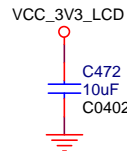
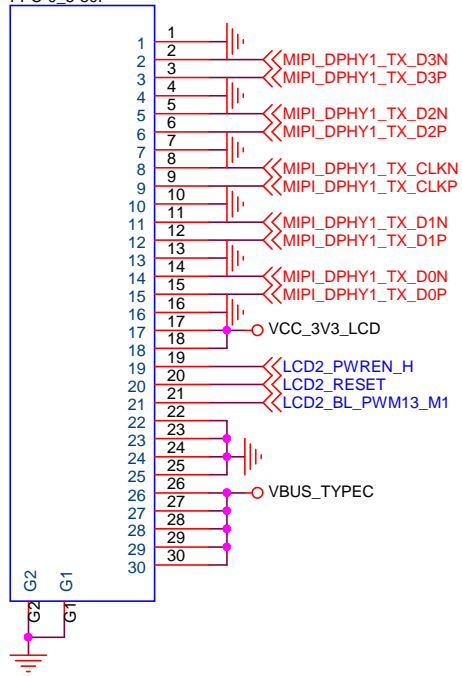
POWER OFF DET



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	22_MCU/RTC/KEY/FAN		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totit	Sheet:	22

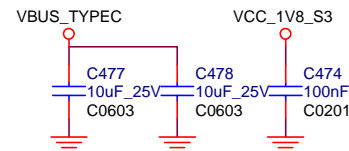
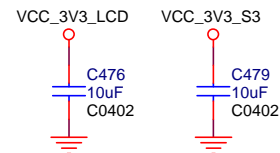
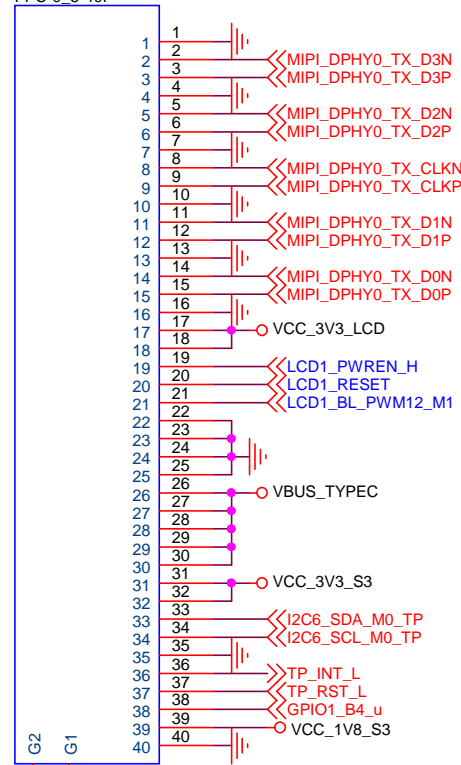
DSI 2

CON7
FH34SRJ-30S
FPC-0_5-30P



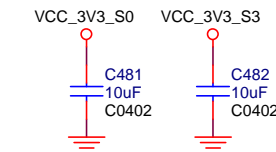
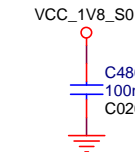
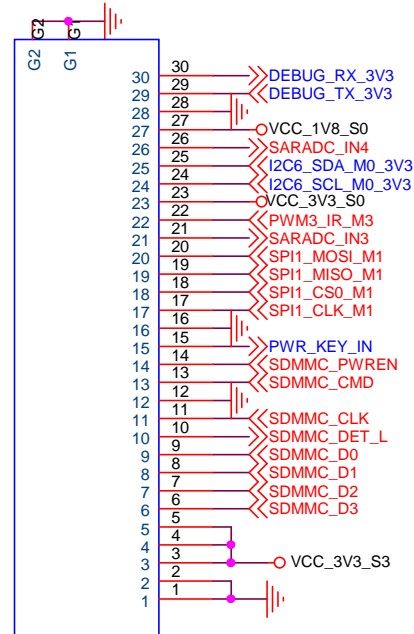
DSI 1

CON4
FH34SRJ-40S
FPC-0_5-40P



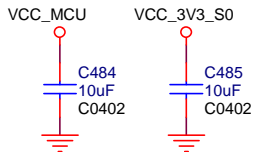
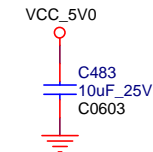
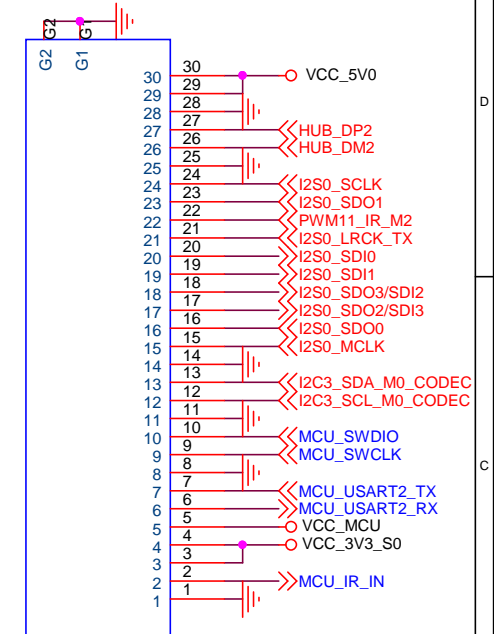
EXTIO 1

CON5
FH34SRJ-30S
FPC-0_5-30P



EXTIO 2

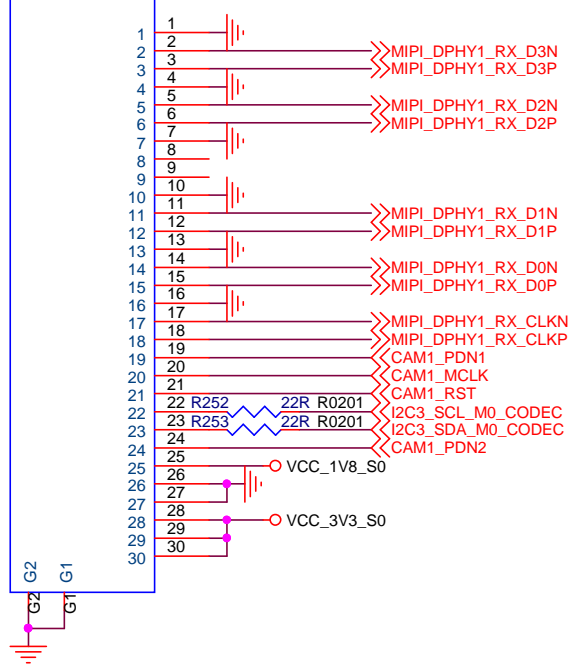
CON6
FH34SRJ-30S
FPC-0_5-30P



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	23_EXT IO/MIPI LCD		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	23

Camera1

CON9
FH34SRJ-30S
FPC-0_5-30P



VCC_1V8_S0

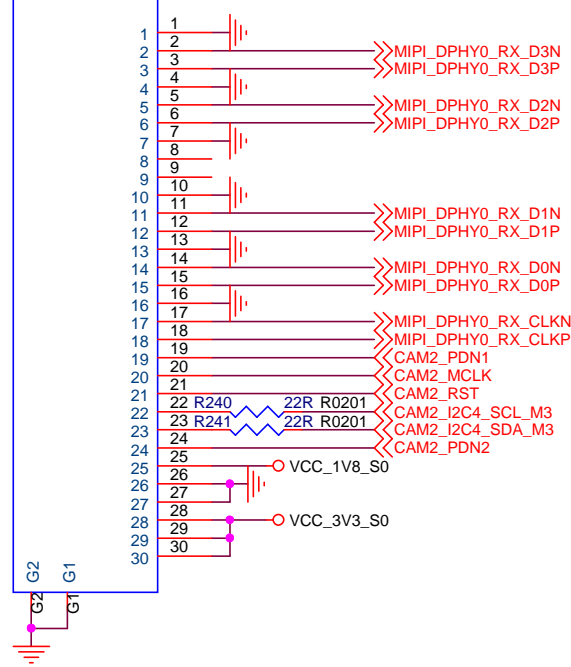


VCC_3V3_S0 VCC_3V3_S0



Camera2

CON8
FH34SRJ-30S
FPC-0_5-30P



VCC_1V8_S0

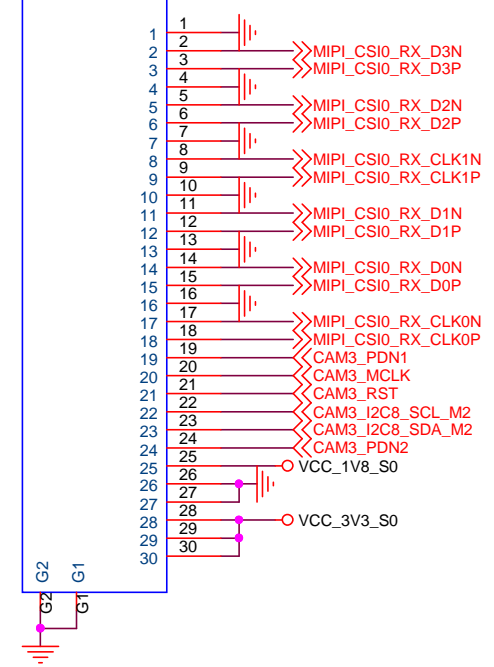


VCC_3V3_S0 VCC_3V3_S0



Camera3

CON3
FH34SRJ-30S
FPC-0_5-30P



VCC_1V8_S0



VCC_3V3_S0 VCC_3V3_S0



Shenzhen Wesion Technology Co., Ltd.			
Project:	Edge2		
File:	24_CAMERA_Connector		
Date:	Tuesday, September 20, 2022	Rev:	V11
Designed_by:	Totti	Sheet:	24